



SPEC® CFP2006 Result

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NTT System S. A.

SPECfp®2006 = 17.1

NTT Business W 907G

SPECfp_base2006 = 16.6

CPU2006 license: 9013

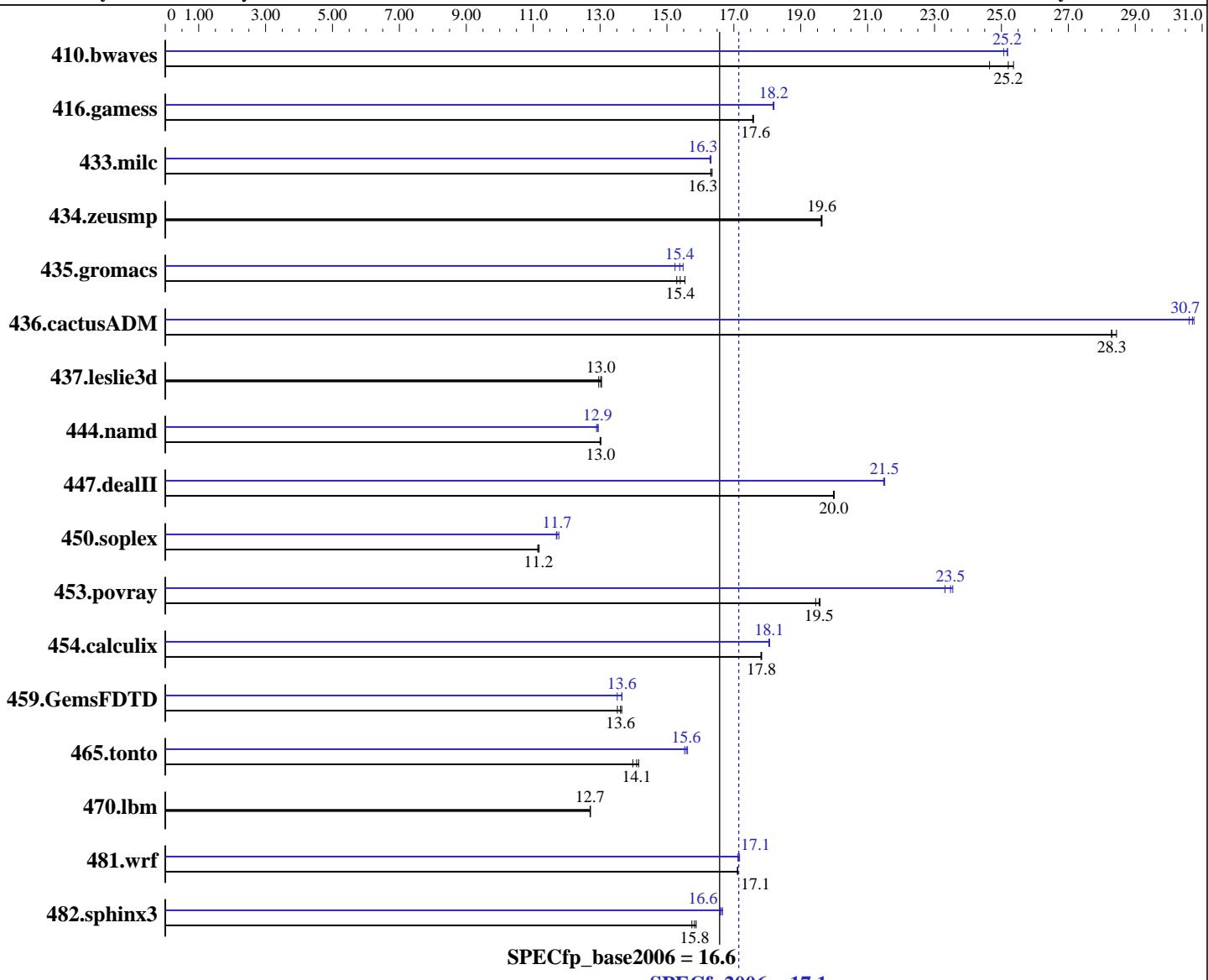
Test date: Jul-2009

Test sponsor: NTT System S. A.

Hardware Availability: Jul-2009

Tested by: NTT System S. A.

Software Availability: Nov-2008



Hardware

CPU Name: Intel Celeron E3200
CPU Characteristics:
CPU MHz: 2400
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 1 MB I+D on chip per chip

Software

Operating System: SuSe Linux SLES10 SP2, Kernel 2.6.16.60-0.21-smp
Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20080930 Package ID: l_cproc_p_11.0.066, l_cprof_p_11.0.066
Auto Parallel: Yes
File System: ReiserFS
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit

Continued on next page

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L3 Cache: None
 Other Cache: None
 Memory: 2 GB (2x1GB) DDR2 800Mhz
 Disk Subsystem: 250 GB SATA, 7200RPM
 Other Hardware: None

Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	551	24.6	536	25.4	539	25.2	540	25.2	540	25.2	542	25.1
416.gamess	1115	17.6	1113	17.6	1114	17.6	1076	18.2	1076	18.2	1077	18.2
433.milc	563	16.3	563	16.3	561	16.4	563	16.3	564	16.3	563	16.3
434.zeusmp	464	19.6	464	19.6	464	19.6	464	19.6	464	19.6	464	19.6
435.gromacs	467	15.3	460	15.5	464	15.4	469	15.2	464	15.4	461	15.5
436.cactusADM	422	28.3	420	28.4	422	28.3	389	30.7	390	30.6	389	30.8
437.leslie3d	725	13.0	721	13.0	722	13.0	725	13.0	721	13.0	722	13.0
444.namd	616	13.0	616	13.0	617	13.0	619	12.9	622	12.9	621	12.9
447.dealII	573	20.0	572	20.0	572	20.0	533	21.5	532	21.5	532	21.5
450.soplex	746	11.2	749	11.1	747	11.2	712	11.7	709	11.8	713	11.7
453.povray	274	19.4	272	19.5	272	19.6	226	23.5	227	23.5	228	23.3
454.calculix	463	17.8	463	17.8	463	17.8	457	18.1	457	18.1	457	18.0
459.GemsFDTD	785	13.5	780	13.6	777	13.6	785	13.5	777	13.6	777	13.7
465.tonto	695	14.2	704	14.0	698	14.1	630	15.6	632	15.6	634	15.5
470.lbm	1081	12.7	1082	12.7	1082	12.7	1081	12.7	1082	12.7	1082	12.7
481.wrf	653	17.1	653	17.1	653	17.1	652	17.1	652	17.1	651	17.2
482.sphinx3	1228	15.9	1238	15.7	1232	15.8	1170	16.7	1173	16.6	1175	16.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

OMP_NUM_THREADS set to number of processors
 KMP_AFFINITY set to "physical,0"
 KMP_STACKSIZE set to 200M

Base Compiler Invocation

C benchmarks:
 icc

C++ benchmarks:
 icpc

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Base Compiler Invocation (Continued)

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
        444.namd: -DSPEC_CPU_LP64
        447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

-xSSE3 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:

-xSSE3 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:

-xSSE3 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE3 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

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Peak Compiler Invocation (Continued)

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):

`icpc`

450.soplex: `icpc -m32`

Fortran benchmarks:

`ifort`

Benchmarks using both Fortran and C:

`icc ifort`

Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
  433.milc: -DSPEC_CPU_LP64
  434.zeusmp: -DSPEC_CPU_LP64
  435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
  437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
  454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
  465.tonto: -DSPEC_CPU_LP64
  470.lbm: -DSPEC_CPU_LP64
  481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

```

Peak Optimization Flags

C benchmarks:

433.milc: `-prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -fno-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `-xSSSE3 -ipo -O3 -no-prec-div -static -unroll12`

C++ benchmarks:

444.namd: `-prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -fno-alias -auto-ilp32`

Continued on next page



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Peak Optimization Flags (Continued)

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
 -no-prec-div -static -unroll2 -ansi-alias -scalar-rep-
 -opt-prefetch

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
 -no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
 -no-prec-div -static -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch
 -parallel

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
 -no-prec-div -static -unroll2 -Obo -ansi-alias
 -scatter-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
 -no-prec-div -static -unroll2 -Obo -opt-prefetch
 -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
 -no-prec-div -static -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
 -no-prec-div -static -opt-prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
 -no-prec-div -static -unroll2 -opt-prefetch -parallel
 -auto-ilp32

454.calculix: -xSSSE3 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: -xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch
 -parallel -auto-ilp32

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090827.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090827.xml>



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