



# SPEC<sup>®</sup> CFP2006 Result

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## NEC Corporation

Express5800/B120a-d  
(Intel Xeon L5520)

SPECfp<sup>®</sup>\_rate2006 = 161

SPECfp\_rate\_base2006 = 155

CPU2006 license: 9006

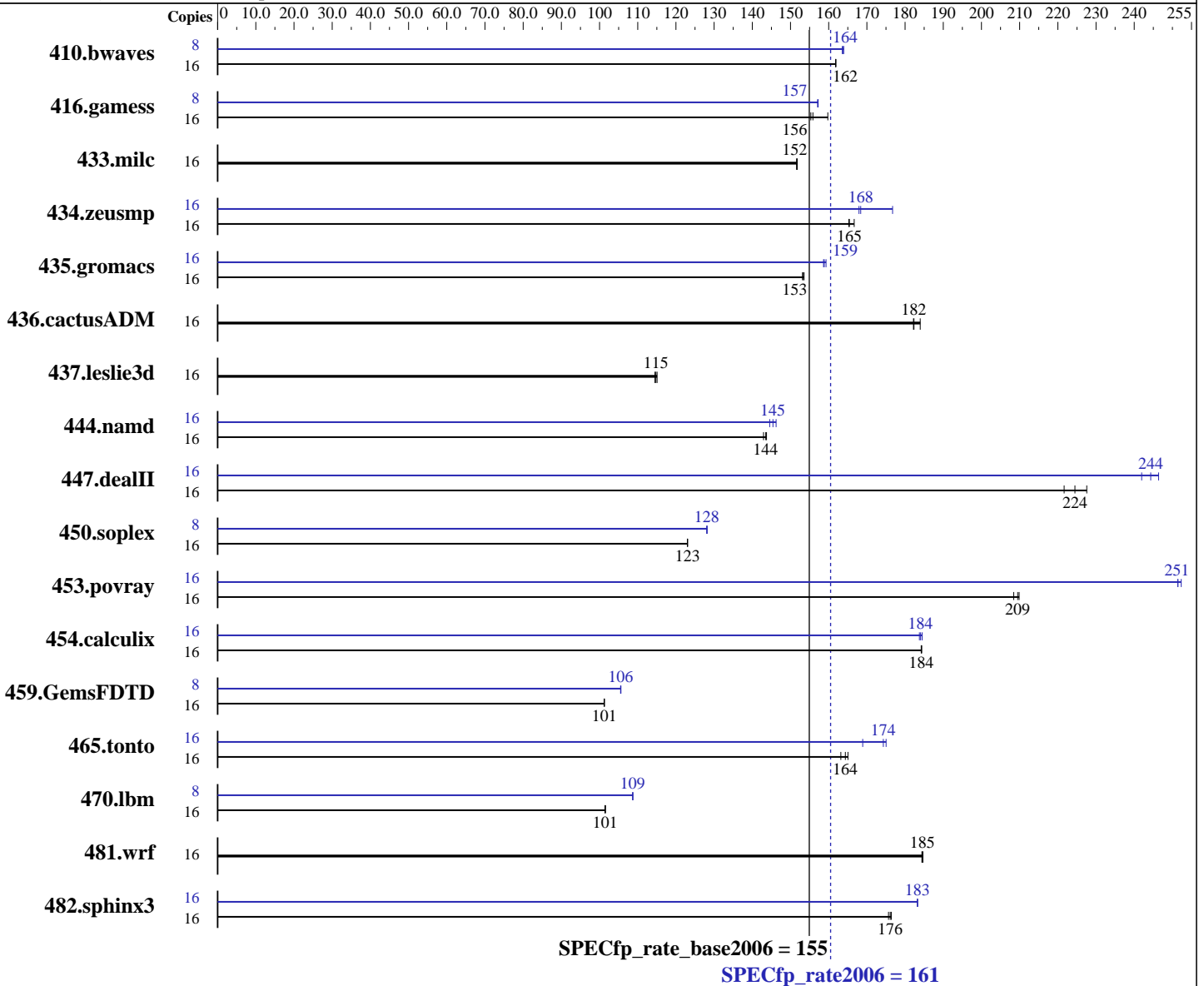
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Aug-2009

Hardware Availability: Jun-2009

Software Availability: Feb-2009



### Hardware

CPU Name: Intel Xeon L5520  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.53 GHz  
 CPU MHz: 2267  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2 with patch Linux kernel 20090119, Kernel 2.6.16.60-0.34-smp  
 Compiler: Intel C++ and Fortran Compiler Professional 11.0 for Linux  
 Build 20090131 Package ID: l\_cproc\_p\_11.0.081, l\_cprof\_p\_11.0.081  
 Auto Parallel: No  
 File System: ReiserFS

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L3 Cache: 8 MB I+D on chip per chip  
Other Cache: None  
Memory: 48 GB (12 X 4 GB PC3-8500R, 2 rank, CL7, ECC)  
Disk Subsystem: 1x73.2 GB SAS, 10000 RPM  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: Binutils 2.18.50.0.7.20080502

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	1344	162	<u>1344</u>	<u>162</u>	1343	162	8	<u>664</u>	<u>164</u>	663	164	665	164
416.gamess	16	1961	160	2017	155	<u>2010</u>	<u>156</u>	8	997	157	996	157	<u>996</u>	<u>157</u>
433.milc	16	968	152	<u>968</u>	<u>152</u>	969	152	16	968	152	<u>968</u>	<u>152</u>	969	152
434.zeusmp	16	881	165	<u>880</u>	<u>165</u>	874	167	16	<u>865</u>	<u>168</u>	824	177	867	168
435.gromacs	16	746	153	<u>745</u>	<u>153</u>	744	154	16	717	159	<u>719</u>	<u>159</u>	720	159
436.cactusADM	16	<u>1049</u>	<u>182</u>	1039	184	1049	182	16	<u>1049</u>	<u>182</u>	1039	184	1049	182
437.leslie3d	16	1313	115	1307	115	<u>1311</u>	<u>115</u>	16	1313	115	1307	115	<u>1311</u>	<u>115</u>
444.namd	16	898	143	<u>894</u>	<u>144</u>	893	144	16	<u>882</u>	<u>145</u>	888	145	878	146
447.dealII	16	804	228	<u>815</u>	<u>224</u>	826	222	16	757	242	<u>749</u>	<u>244</u>	743	246
450.soplex	16	1084	123	<u>1085</u>	<u>123</u>	1085	123	8	521	128	521	128	<u>521</u>	<u>128</u>
453.povray	16	<u>406</u>	<u>209</u>	406	210	408	208	16	<u>339</u>	<u>251</u>	337	252	339	251
454.calculix	16	717	184	716	184	<u>716</u>	<u>184</u>	16	716	184	718	184	<u>717</u>	<u>184</u>
459.GemsFDTD	16	1676	101	1678	101	<u>1677</u>	<u>101</u>	8	804	106	<u>804</u>	<u>106</u>	805	106
465.tonto	16	954	165	<u>958</u>	<u>164</u>	965	163	16	<u>903</u>	<u>174</u>	932	169	900	175
470.lbm	16	2165	102	<u>2166</u>	<u>101</u>	2168	101	8	<u>1011</u>	<u>109</u>	1011	109	1011	109
481.wrf	16	968	185	969	184	<u>968</u>	<u>185</u>	16	968	185	969	184	<u>968</u>	<u>185</u>
482.sphinx3	16	<u>1771</u>	<u>176</u>	1768	176	1775	176	16	1702	183	1701	183	<u>1702</u>	<u>183</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

BIOS setting:  
NUMA configuration: Enabled



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## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.lelie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static



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## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc

450.soplex: icpc -m32

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch  
-auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

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## Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-fno-alias -auto-ilp32

447.dealIII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias -scalar-rep-

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -Ob0 -opt-prefetch

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

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## Peak Optimization Flags (Continued)

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revH.html>  
<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revH.xml>  
<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revE.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.  
Report generated on Wed Jul 23 02:08:28 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 1 September 2009.