SPEC® CINT2006 Result
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ACTION S.A.

ACTINA SOLAR 200 S4+ (Intel Xeon E5520)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 185

CPU2006 license: 9008	**Test date:** Aug-2009
Test sponsor: ACTION S.A.	**Hardware Availability:** Apr-2009
Tested by: ACTION S.A.	**Software Availability:** Feb-2009

Copies 0 20.0 50.0 80.0 110 130 150 170 190 210 230 250 270 290 310 330 350 370 390 410 430 450 470 490 510 530 550 570 590 630

Benchmark	Copies	Result
400.perlbench	16	167
401.bzip2	16	117
403.gcc	16	157
429.mcf	16	225
445.gobmk	16	183
456.hmmer	16	137
458.sjeng	16	173
462.libquantum	16	625
464.h264ref	16	229
471.omnetpp	16	147
473.astar	16	122
483.xalancbmk	16	213

SPECint_rate_base2006 = 185

Hardware

CPU Name:	Intel Xeon E5520
CPU Characteristics:	Intel Turbo Boost Technology up to 2.53 GHz
CPU MHz:	2267
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	24 GB (6 x 4 GB PC3-8500, 1066 MHz, DDR3, ECC)
Disk Subsystem:	500 GB SATA, 7200 RPM
Other Hardware:	None

Software

Operating System:	SuSe Linux Enterprise Server 10 (x86_64) with SP2, kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20080930 Package ID: l_cproc_p_11.0.066
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Binutils 2.18.50.0.7.20080502 Microquill SmartHeap V8.1

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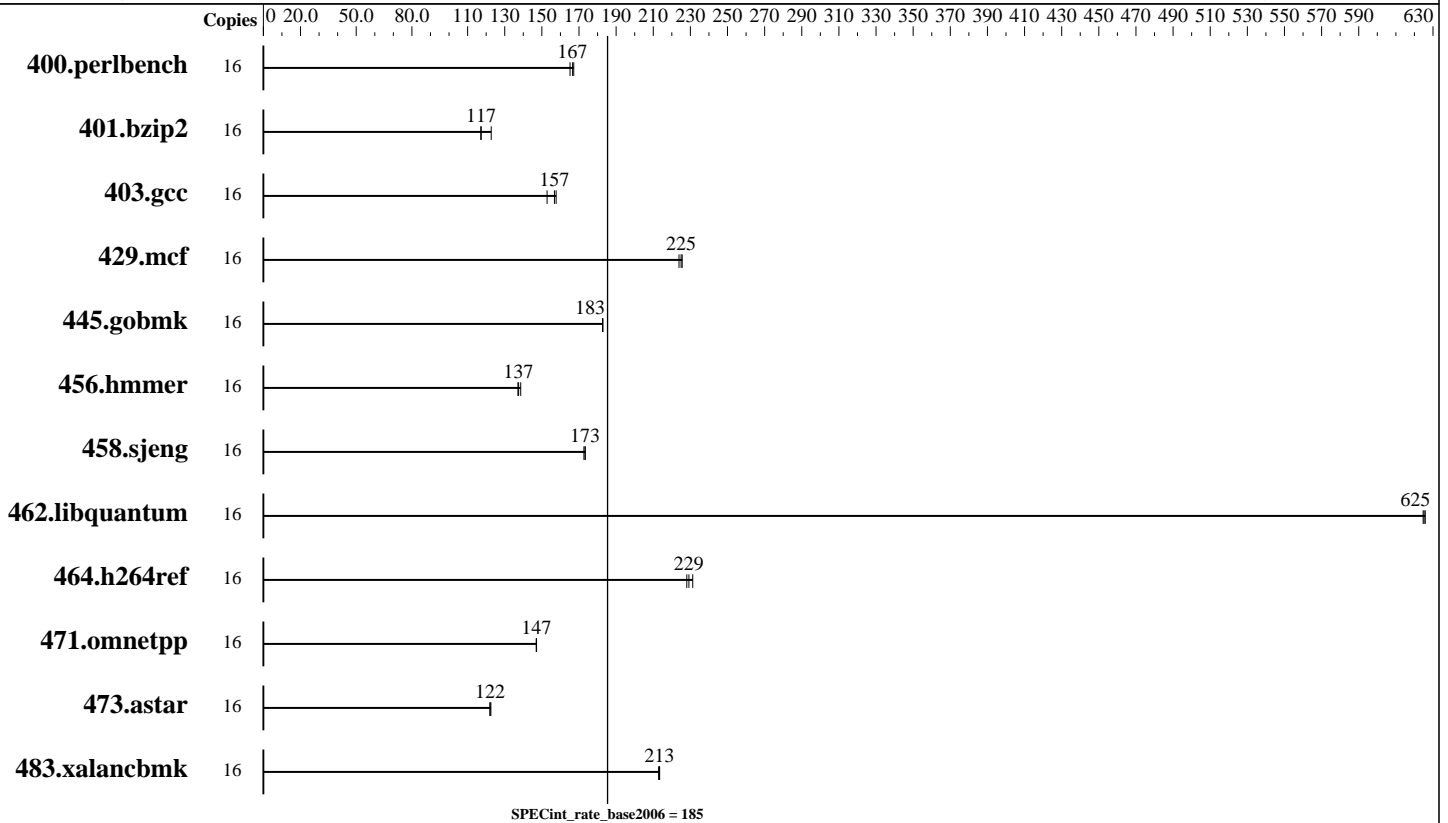
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Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	**939**	**167**	946	165	935	167							
401.bzip2	16	1258	123	**1316**	**117**	1319	117							
403.gcc	16	**822**	**157**	843	153	817	158							
429.mcf	16	**649**	**225**	647	226	652	224							
445.gobmk	16	919	183	**919**	**183**	918	183							
456.hmmer	16	**1086**	**137**	1089	137	1077	139							
458.sjeng	16	**1117**	**173**	1117	173	1121	173							
462.libquantum	16	531	625	**530**	**625**	530	626							
464.h264ref	16	1531	231	1553	228	**1545**	**229**							
471.omnetpp	16	680	147	**680**	**147**	681	147							
473.astar	16	**920**	**122**	917	123	920	122							
483.xalancbmk	16	519	213	**517**	**213**	517	213							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
     'numactl' was used to bind copies to the cores

General Notes

    'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Base Compiler Invocation

C benchmarks:
     icc

C++ benchmarks:
     icpc

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

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Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revG.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revG.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
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