



# SPEC® CFP2006 Result

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## Fujitsu

### SPECfp®\_rate2006 = 74.7

### PRIMERGY TX300 S6, Intel Xeon E5607, 2.27 GHz

### SPECfp\_rate\_base2006 = 72.1

CPU2006 license: 19

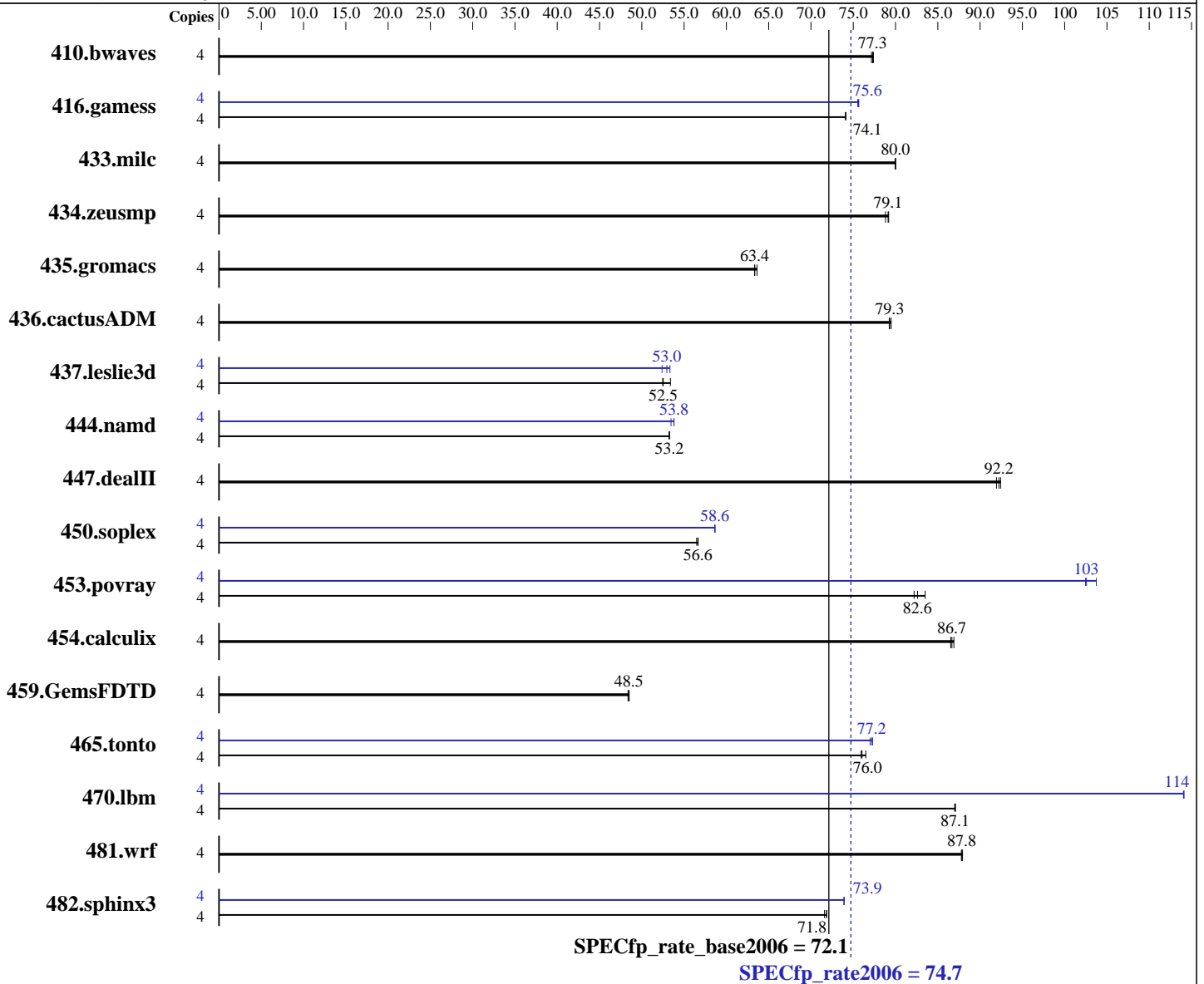
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jan-2011

Hardware Availability: Feb-2011

Software Availability: Nov-2010



#### Hardware

CPU Name: Intel Xeon E5607  
 CPU Characteristics: 2267  
 CPU MHz: Integrated  
 FPU: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) enabled: 1,2 chips  
 CPU(s) orderable: 32 KB I + 32 KB D on chip per core  
 Primary Cache: 256 KB I+D on chip per core  
 Secondary Cache:

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#### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64) with SP1, Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.0.082 Build 20101006  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit

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L3 Cache: 8 MB I+D on chip per chip  
Other Cache: None  
Memory: 24 GB (6 x 4 GB 2Rx4 PC3-10600R-9, ECC, running at 1067 MHz and CL7)  
Disk Subsystem: 1 x SAS, 300 GB, 10000 RPM  
Other Hardware: --

Peak Pointers: 32/64-bit  
Other Software: none

## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	4	705	77.1	702	77.4	<b>703</b>	<b>77.3</b>	4	705	77.1	702	77.4	<b>703</b>	<b>77.3</b>		
416.gamess	4	1057	74.1	<b>1057</b>	<b>74.1</b>	1057	74.1	4	<b>1035</b>	<b>75.6</b>	1035	75.6	1037	75.5		
433.milc	4	459	80.0	<b>459</b>	<b>80.0</b>	459	80.0	4	459	80.0	<b>459</b>	<b>80.0</b>	459	80.0		
434.zeusmp	4	462	78.8	460	79.2	<b>460</b>	<b>79.1</b>	4	462	78.8	460	79.2	<b>460</b>	<b>79.1</b>		
435.gromacs	4	449	63.6	<b>451</b>	<b>63.4</b>	451	63.3	4	449	63.6	<b>451</b>	<b>63.4</b>	451	63.3		
436.cactusADM	4	<b>603</b>	<b>79.3</b>	601	79.5	603	79.3	4	<b>603</b>	<b>79.3</b>	601	79.5	603	79.3		
437.leslie3d	4	704	53.4	<b>716</b>	<b>52.5</b>	717	52.5	4	<b>710</b>	<b>53.0</b>	718	52.4	705	53.3		
444.namd	4	<b>602</b>	<b>53.2</b>	603	53.2	602	53.3	4	600	53.5	<b>596</b>	<b>53.8</b>	596	53.8		
447.dealII	4	495	92.4	498	91.9	<b>496</b>	<b>92.2</b>	4	495	92.4	498	91.9	<b>496</b>	<b>92.2</b>		
450.soplex	4	<b>589</b>	<b>56.6</b>	589	56.7	591	56.5	4	569	58.6	<b>569</b>	<b>58.6</b>	569	58.7		
453.povray	4	259	82.2	255	83.5	<b>258</b>	<b>82.6</b>	4	<b>207</b>	<b>103</b>	208	102	205	104		
454.calculix	4	<b>381</b>	<b>86.7</b>	380	86.9	381	86.5	4	<b>381</b>	<b>86.7</b>	380	86.9	381	86.5		
459.GemsFDTD	4	875	48.5	878	48.4	<b>876</b>	<b>48.5</b>	4	875	48.5	878	48.4	<b>876</b>	<b>48.5</b>		
465.tonto	4	<b>518</b>	<b>76.0</b>	518	75.9	515	76.5	4	509	77.3	511	77.0	<b>510</b>	<b>77.2</b>		
470.lbm	4	631	87.1	<b>631</b>	<b>87.1</b>	632	87.0	4	<b>482</b>	<b>114</b>	482	114	482	114		
481.wrf	4	508	87.9	509	87.8	<b>509</b>	<b>87.8</b>	4	508	87.9	509	87.8	<b>509</b>	<b>87.8</b>		
482.sphinx3	4	1089	71.6	1085	71.9	<b>1085</b>	<b>71.8</b>	4	<b>1054</b>	<b>73.9</b>	1054	73.9	1055	73.9		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
Large pages were not enabled for this run

## Platform Notes

BIOS configuration:  
Data Reuse Optimization = Disable



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## General Notes

This result was measured on the PRIMERGY TX300 S6. The PRIMERGY TX300 S6 and the PRIMERGY TX300 S6 are electronically equivalent.

For information about Fujitsu please visit: <http://www.fujitsu.com>  
Binaries were compiled on SLES 10 SP1 with Binutils 2.18.50.0.7.20080502

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.deallI: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -ansi-alias

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## Base Optimization Flags (Continued)

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -ansi-alias

Fortran benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div

Benchmarks using both Fortran and C:  
-xSSE4.2 -ipo -O3 -no-prec-div -ansi-alias

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):  
icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.deall: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

### C benchmarks:

433.milc: basepeak = yes

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3  
-ansi-alias -opt-prefetch -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2

### C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3

### Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

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## Peak Optimization Flags (Continued)

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110222.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110222.00.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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