SPEC® CINT2006 Result
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Bull SAS

SPECint®_rate2006 =   152

BL265 (Intel Xeon L5609, 1.86 GHz)

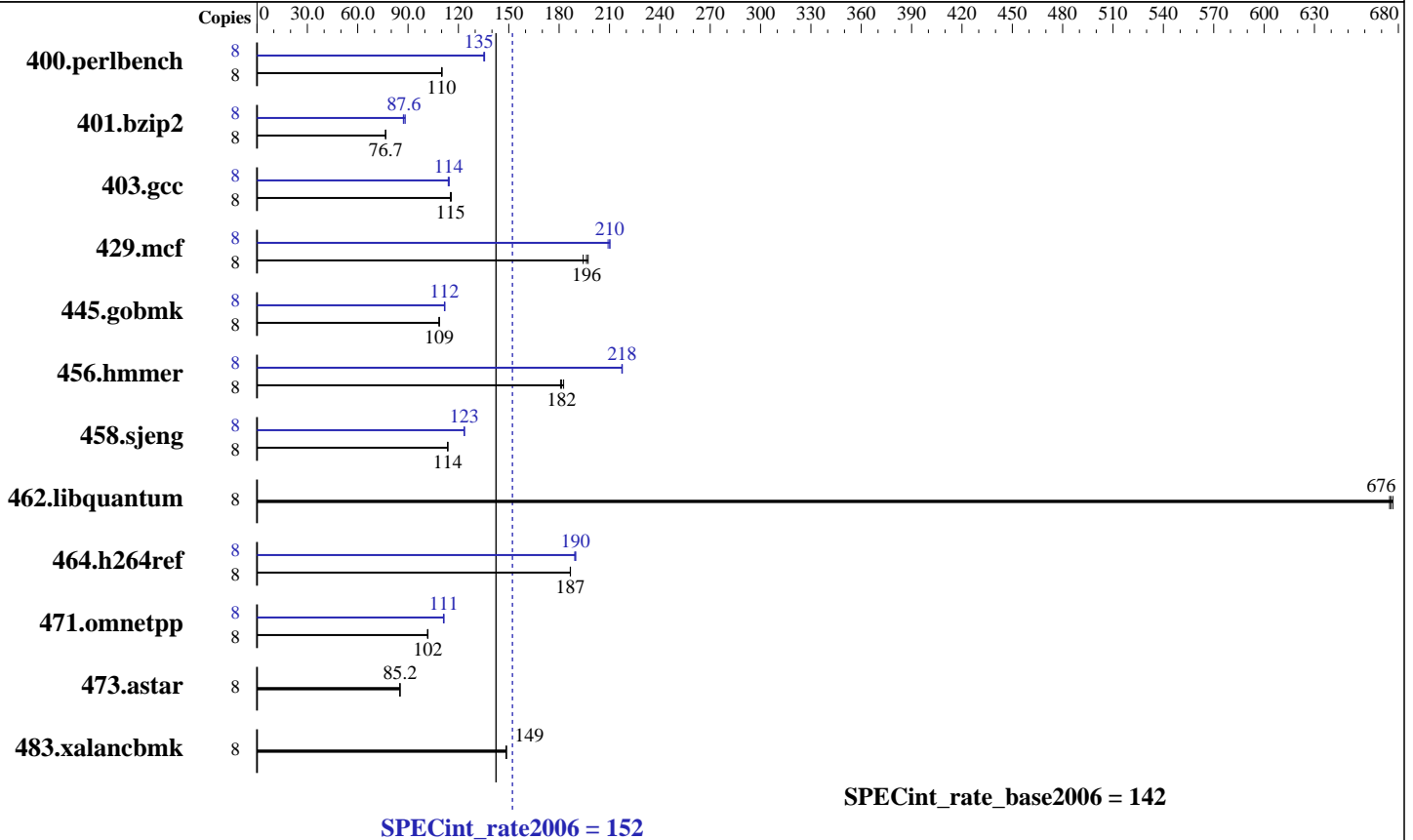SPECint_rate_base2006 =   142

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: Bull SAS

Test date: Jan-2011
Hardware Availability: Mar-2010
Software Availability: Nov-2010

SPECint_rate2006 = 152

SPECint_rate_base2006 = 142

Hardware

CPU Name: Intel Xeon L5609
CPU Characteristics:
CPU MHz: 1866
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC, running at 1066 MHz)
Disk Subsystem: 2 x 50 GB SATA, SSD
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) SP1, Kernel 2.6.32.12-0.7-default
Compiler: Intel C++ Compiler XE for applications running on IA-32
Version 12.0.1.116 Build 20101116
Auto Parallel: No
File System: ext3
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V9.01

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Results Table

Benchmark	Base Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Peak Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	**711**	**110**	711	110	709	110	8	**577**	**135**	578	135	576	136
401.bzip2	8	**1007**	**76.7**	1007	76.7	1008	76.6	8	885	87.2	874	88.3	**882**	**87.6**
403.gcc	8	559	115	556	116	**558**	**115**	8	562	114	565	114	**564**	**114**
429.mcf	8	375	194	370	197	**372**	**196**	8	**347**	**210**	347	210	349	209
445.gobmk	8	**773**	**109**	773	109	774	108	8	749	112	**751**	**112**	752	112
456.hmmer	8	413	181	409	183	**411**	**182**	8	**343**	**218**	343	217	343	218
458.sjeng	8	852	114	**852**	**114**	851	114	8	784	124	784	123	**784**	**123**
462.libquantum	8	**245**	**676**	246	675	245	677	8	**245**	**676**	246	675	245	677
464.h264ref	8	949	187	**948**	**187**	948	187	8	933	190	935	189	**933**	**190**
471.omnetpp	8	**491**	**102**	491	102	492	102	8	449	111	450	111	**449**	**111**
473.astar	8	659	85.2	660	85.1	**659**	**85.2**	8	659	85.2	660	85.1	**659**	**85.2**
483.xalancbmk	8	372	148	371	149	**372**	**149**	8	372	148	371	149	**372**	**149**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
     numactl was used to bind copies to the cores

Operating System Notes

    'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
    Hugepages was enabled with the following:
      'nodev   /mnt/hugepages   hugetlbfs defaults   0 0' added to /etc/fstab
      echo 3600 > /proc/sys/vm/nr_hugepages
      export HUGETLB_MORECORE=yes
      export LD_PRELOAD=/usr/lib64/libhugetlbfs.so

Platform Notes

    Power C-states enabled in BIOS
    Demand Scrub disabled in BIOS

Base Compiler Invocation

C benchmarks:
      icc -m32

C++ benchmarks:
      icpc -m32

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Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
    -B /usr/share/libhugetlbfs/ -Wl,--hugetlbfs-link=BDT

C++ benchmarks:

    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
    -L/smartheap -lsmartheap
    -B /usr/share/libhugetlbfs/ -Wl,--hugetlbfs-link=BDT

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m32

    400.perlbench: icc -m64

        401.bzip2: icc -m64

      456.hmmer: icc -m64

       458.sjeng: icc -m64

C++ benchmarks:
    icpc -m32

Peak Portability Flags

    400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
        401.bzip2: -DSPEC_CPU_LP64

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Test sponsor: Bull SAS		**Hardware Availability:**	Mar-2010
Tested by: Bull SAS		**Software Availability:**	Nov-2010

Peak Portability Flags (Continued)

```
     456.hmmer: -DSPEC_CPU_LP64
     458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LINUX
 483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
  400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

     401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -opt-prefetch -auto-ilp32 -ansi-alias
                 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

       403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div
                 -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

       429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -ansi-alias -auto-ilp32

     445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
                 -ansi-alias -auto-ilp32

     456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
                 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

     458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -unroll4 -auto-ilp32
                 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

 462.libquantum: basepeak = yes

    464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -unroll2 -ansi-alias
```

C++ benchmarks:

```
    471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
                 -L/smartheap -lsmartheap
```

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Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.xml

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