



# SPEC® CINT2006 Result

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## Hewlett-Packard Company

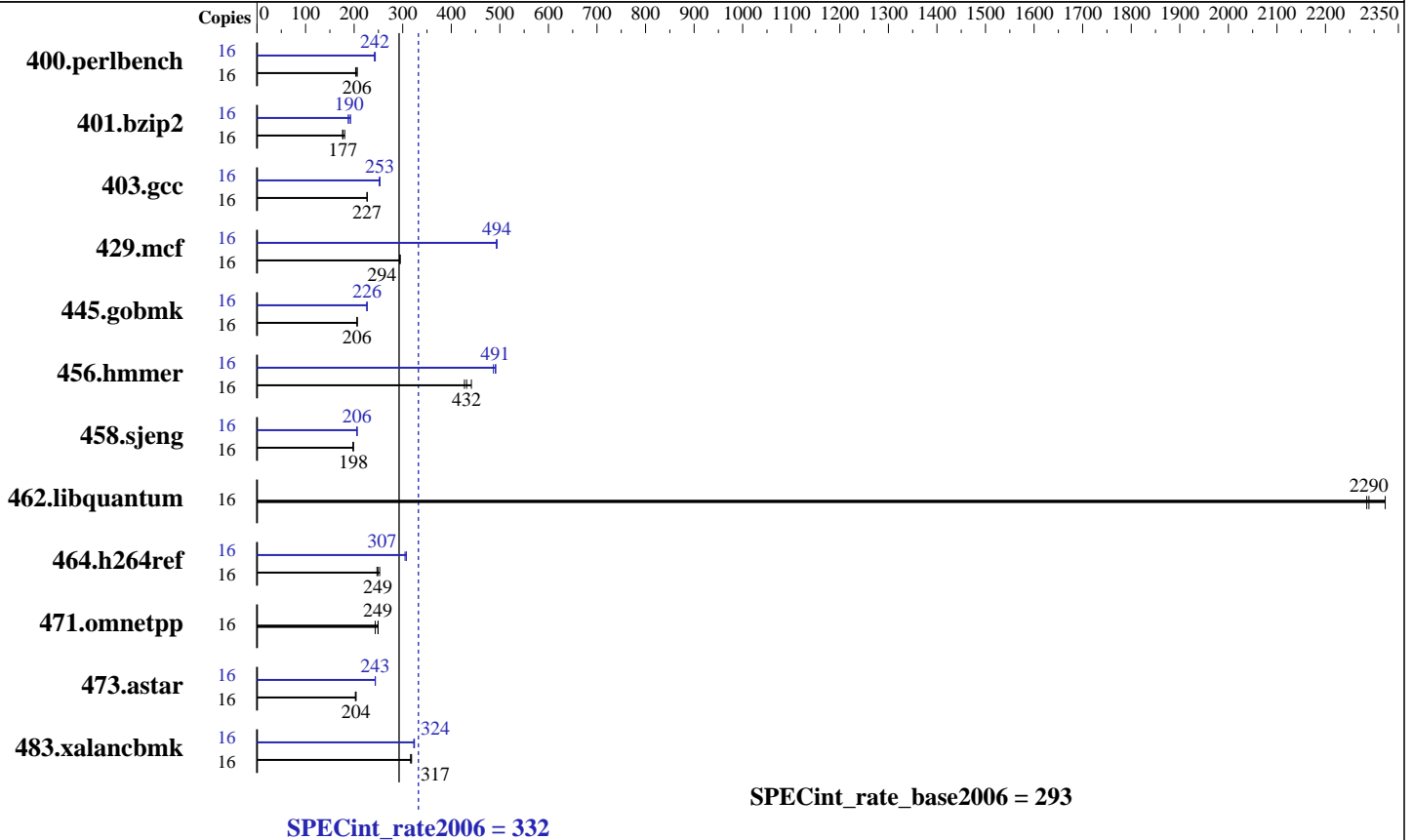
SPECint®\_rate2006 = 332

ProLiant BL465c Gen8  
(2.80 GHz AMD Opteron 6320)

SPECint\_rate\_base2006 = 293

CPU2006 license: 3  
Test sponsor: Hewlett-Packard Company  
Tested by: Hewlett-Packard Company

Test date: Dec-2012  
Hardware Availability: Nov-2012  
Software Availability: Aug-2012



### Hardware

CPU Name: AMD Opteron 6320  
 CPU Characteristics: AMD Turbo CORE technology up to 3.30 GHz  
 CPU MHz: 2800  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 256 KB I on chip per chip,  
64 KB I shared / 2 cores;  
16 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 2 MB shared / 2 cores  
 L3 Cache: 16 MB I+D on chip per chip, 8 MB shared / 4 cores  
 Other Cache: None  
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC)  
 Disk Subsystem: 2 x 200 GB SSD SAS, RAID 1  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.3,  
Kernel 2.6.32-279.el6.x86\_64  
 Compiler: C/C++: Version 4.5.2 of  
x86 Open64 Compiler Suite (from AMD)  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: SmartHeap 10.0 32-bit Library for Linux



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	<u>759</u>	<u>206</u>	770	203	758	206	16	<u>645</u>	<u>242</u>	642	244	646	242
401.bzip2	16	<u>873</u>	<u>177</u>	876	176	855	181	16	<u>813</u>	<u>190</u>	801	193	823	188
403.gcc	16	568	227	568	227	<u>568</u>	<u>227</u>	16	509	253	511	252	<u>510</u>	<u>253</u>
429.mcf	16	497	294	495	295	<u>496</u>	<u>294</u>	16	295	494	296	493	<u>296</u>	<u>494</u>
445.gobmk	16	815	206	<u>814</u>	<u>206</u>	814	206	16	742	226	740	227	<u>741</u>	<u>226</u>
456.hammer	16	350	427	338	441	<u>346</u>	<u>432</u>	16	307	487	304	492	<u>304</u>	<u>491</u>
458.sjeng	16	979	198	<u>977</u>	<u>198</u>	975	198	16	940	206	941	206	<u>940</u>	<u>206</u>
462.libquantum	16	145	2280	143	2320	<u>145</u>	<u>2290</u>	16	145	2280	143	2320	<u>145</u>	<u>2290</u>
464.h264ref	16	<u>1422</u>	<u>249</u>	1402	253	1434	247	16	1162	305	1151	308	<u>1153</u>	<u>307</u>
471.omnetpp	16	411	243	401	249	<u>402</u>	<u>249</u>	16	411	243	401	249	<u>402</u>	<u>249</u>
473.astar	16	551	204	555	202	<u>551</u>	<u>204</u>	16	462	243	461	244	<u>461</u>	<u>243</u>
483.xalancbmk	16	347	318	349	316	<u>348</u>	<u>317</u>	16	342	323	341	324	<u>341</u>	<u>324</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

Set transparent\_hugepage=never as a boot parameter in /boot/grub/menu.lst

Set vm/nr\_hugepages=14336 in /etc/sysctl.conf  
mount -t hugetlbfs nodev /mnt/hugepages

## Platform Notes

BIOS configuration:  
HP Power Profile set to Maximum Performance  
Thermal Configuration set to Maximum Cooling  
Collaborative Power Control set to Disable  
Minimum Processor Idle Power C1e State to Enabled  
Processor Power and Utilization Monitoring set to Disable



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## General Notes

Environment variables set by runspec before the start of the run:

HUGETLB\_LIMIT = "896"

LD\_LIBRARY\_PATH = "/cpu2006/amd1206-rate-libs-revA/32:/cpu2006/amd1206-rate-libs-revA/64"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at <http://developer.amd.com/cpu/open64>

Binaries were compiled on a system with 2x AMD Opteron 6386SE chips + 128GB Memory using RHEL 6.3

## Base Compiler Invocation

C benchmarks:

openc

C++ benchmarks:

openCC

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-Ofast -CG:local\_sched\_alg=1 -INLINE:aggressive=ON -IPA:plimit=8000  
-IPA:small\_pu=100 -HP:bd=2m:heap=2m -mso -LNO:prefetch=2  
-march=bdver1

C++ benchmarks:

-Ofast -m32 -INLINE:aggressive=on -CG:cmp\_peep=on -D\_\_OPEN64\_FAST\_SET  
-march=bdver1 -L/root/work/libraries/SmartHeap-10/lib -lsmartheap



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## Peak Compiler Invocation

C benchmarks:  
opencc

C++ benchmarks:  
openCC

## Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

```

## Peak Optimization Flags

C benchmarks:

```

400.perlbench: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-LNO:prefetch=2 -LNO:opt=0 -IPA:plimit=20000
-OPT:unroll_times_max=8 -OPT:unroll_size=256
-OPT:unroll_level=2 -OPT:keep_ext=on -WOPT:if_conv=0
-WOPT:sib=on -CG:local_sched_alg=1 -CG:unroll_fb_req=on
-CG:movext_icmp=off -HP:bd=2m:heap=2m -march=bdver1
-GRA:aggr_loop_splitting=off -GRA:loop_splitting=off

401.bzip2: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -O3
-LNO:prefetch=2 -LNO:pf2=0 -OPT:alias=disjoint
-OPT:goto=off -CG:local_sched_alg=1 -HP:bd=2m:heap=2m
-march=bdver2

403.gcc: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-LNO:trip_count=256 -CG:cmp_peep=on -CG:pre_minreg_level=2
-m32 -HP:bd=2m:heap=2m -GRA:unspill=on -IPA:small_pu=200
-WOPT:sib=on -march=bdver2 -mno-fma4

429.mcf: -O3 -OPT:unroll_times_max=5 -ipa -INLINE:aggressive=on
-CG:gcm=off -CG:dsched=on -GRA:prioritize_by_density=on
-m32 -HP:bd=2m:heap=2m -mso -march=bdver1

445.gobmk: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-OPT:unroll_size=256 -OPT:unroll_times_max=8
-OPT:keep_ext=on -IPA:plimit=750 -IPA:min_hotness=300

```

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## Peak Optimization Flags (Continued)

445.gobmk (continued):

-IPA:pu\_reorder=1 -LNO:ignore\_feedback=off -WOPT:if\_conv=2  
-HP:bd=2m:heap=2m -march=bdver1

456.hmmer: -fb\_create fbdata(pass 1) -fb\_opt fbdata(pass 2) -Ofast

-LNO:prefetch=2 -OPT:alias=disjoint  
-OPT:unroll\_times\_max=16 -OPT:unroll\_size=512  
-OPT:unroll\_level=2 -OPT:keep\_ext=on -CG:cflow=0  
-CG:cmp\_peep=on -CG:pre\_local\_sched=off -HP:bdt=2m:heap=2m  
-CG:p2align=0 -CG:load\_exe=3 -CG:dsched=on -march=bdver1

458.sjeng: -fb\_create fbdata(pass 1) -fb\_opt fbdata(pass 2) -Ofast

-CG:ptr\_load\_use=0 -CG:divrem\_opt=on -CG:movext\_icmp=off  
-CG:locs\_best=on -LNO:full\_unroll=10 -IPA:pu\_reorder=2  
-HP:heap=2m:bd=2m -WOPT:sib=on -march=bdver1

462.libquantum: basepeak = yes

464.h264ref: -fb\_create fbdata(pass 1) -fb\_opt fbdata(pass 2) -O3

-OPT:unroll\_size=256 -OPT:unroll\_times\_max=2  
-IPA:plimit=20000 -OPT:alias=disjoint -CG:ptr\_load\_use=0  
-CG:local\_sched\_alg=1 -HP:bdt=2m:heap=2m -march=bdver1

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -fb\_create fbdata(pass 1) -fb\_opt fbdata(pass 2) -Ofast

-WOPT:if\_conv=0 -WOPT:sib=on -CG:divrem\_opt=on  
-CG:p2align=1 -CG:dsched=on -GRA:optimize\_boundary=on  
-OPT:alias=disjoint -INLINE:aggressive=on  
-IPA:small\_pu=3000 -IPA:plimit=3000 -HP:bdt=2m:heap=2m  
-march=bdver1

483.xalancbmk: -Ofast -LNO:prefetch=2 -OPT:unroll\_size=512

-OPT:unroll\_times\_max=8 -D\_OPEN64\_FAST\_SET  
-INLINE:aggressive=on -m32 -CG:cmp\_peep=on  
-CG:local\_sched=off -CG:p2align=1 -GRA:unspill=on  
-TENV:frame\_pointer=off -fno-emit-exceptions -march=bdver2  
-mno-fma4  
-L/root/work/libraries/SmartHeap-10/lib -lsmarheap

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/x86-open64-452-flags-rate-revA-II.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-AMD-V1.2-A.20121205.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/x86-open64-452-flags-rate-revA-II.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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