



# SPEC® CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

**Huawei**

**Huawei XH321 V5 (Intel Xeon Gold 6136)**

CPU2017 License: 3175

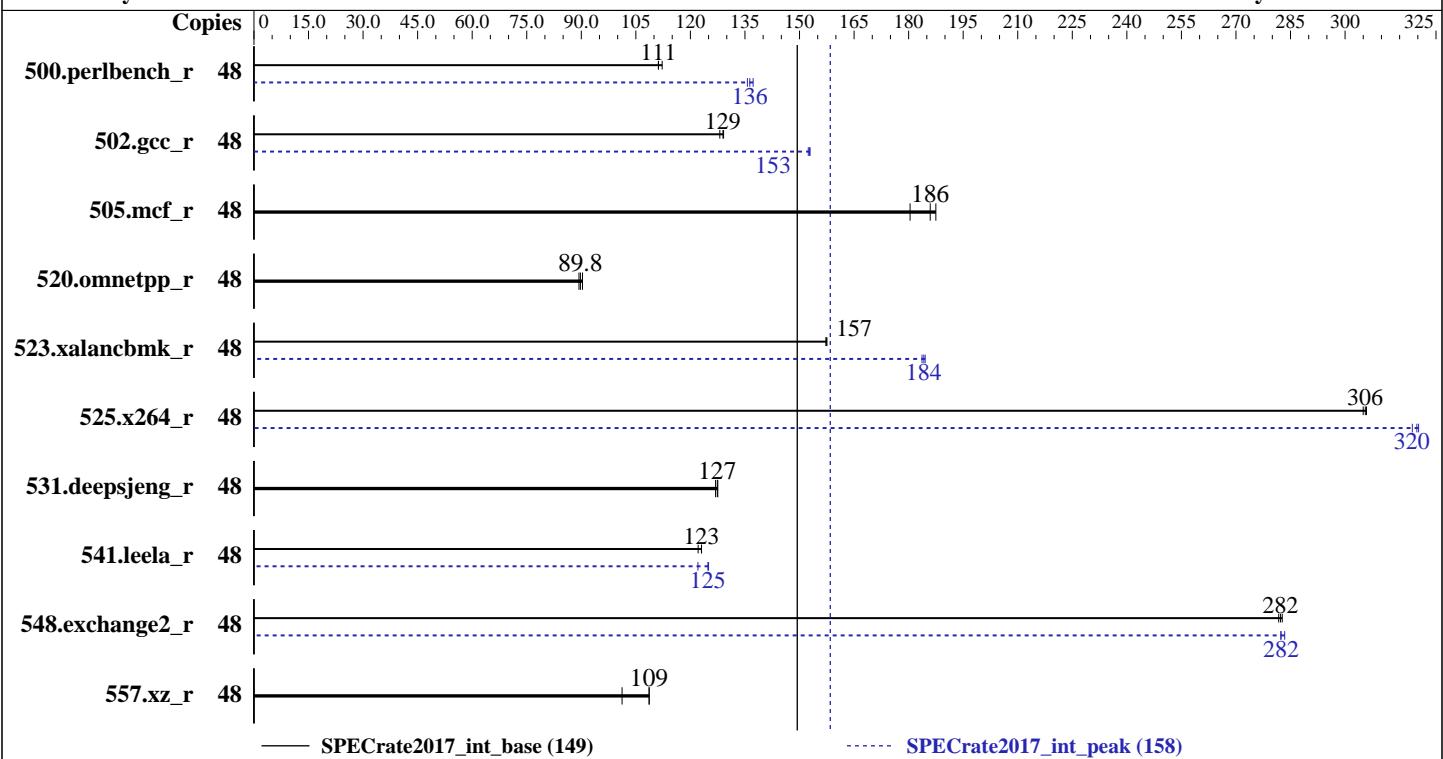
Test Sponsor: Huawei

Tested by: Huawei

Test Date: May-2018

Hardware Availability: Jul-2017

Software Availability: Jan-2018



Hardware	
CPU Name:	Intel Xeon Gold 6136
Max MHz.:	3700
Nominal:	3000
Enabled:	24 cores, 2 chips, 2 threads/core
Orderable:	1,2 chips
Cache L1:	32 KB I + 32 KB D on chip per core
L2:	1 MB I+D on chip per core
L3:	24.75 MB I+D on chip per chip
Other:	None
Memory:	384 GB (12 x 32 GB 2Rx4 PC4-2666V-R)
Storage:	1 x 1200 GB SAS, 10000 RPM
Other:	None

Software	
OS:	Red Hat Enterprise Linux Server release 7.3 (Maipo)
Compiler:	3.10.0-693.11.6.el7.x86_64 C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux; Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux
Parallel:	No
Firmware:	Version 0.59 Released Feb-2018
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	32/64-bit
Other:	jemalloc: jemalloc memory allocator library V5.0.1;



# SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

**Huawei**

**SPECrate2017\_int\_base = 149**

**Huawei XH321 V5 (Intel Xeon Gold 6136)**

**SPECrate2017\_int\_peak = 158**

CPU2017 License: 3175

Test Date: May-2018

Test Sponsor: Huawei

Hardware Availability: Jul-2017

Tested by: Huawei

Software Availability: Jan-2018

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	48	681	112	<b>687</b>	<b>111</b>	687	111	48	557	137	563	136	<b>561</b>	<b>136</b>
502.gcc_r	48	<b>527</b>	<b>129</b>	526	129	531	128	48	445	153	446	152	<b>445</b>	<b>153</b>
505.mcf_r	48	<b>417</b>	<b>186</b>	414	187	430	180	48	<b>417</b>	<b>186</b>	414	187	430	180
520.omnetpp_r	48	<b>702</b>	<b>89.8</b>	697	90.3	705	89.4	48	<b>702</b>	<b>89.8</b>	697	90.3	705	89.4
523.xalancbmk_r	48	322	158	<b>322</b>	<b>157</b>	322	157	48	276	184	275	185	<b>275</b>	<b>184</b>
525.x264_r	48	276	305	275	306	<b>275</b>	<b>306</b>	48	264	318	262	320	<b>263</b>	<b>320</b>
531.deepsjeng_r	48	<b>432</b>	<b>127</b>	431	128	433	127	48	<b>432</b>	<b>127</b>	431	128	433	127
541.leela_r	48	646	123	<b>646</b>	<b>123</b>	651	122	48	<b>637</b>	<b>125</b>	651	122	636	125
548.exchange2_r	48	<b>446</b>	<b>282</b>	446	282	445	283	48	445	282	444	283	<b>445</b>	<b>282</b>
557.xz_r	48	477	109	<b>477</b>	<b>109</b>	512	101	48	477	109	<b>477</b>	<b>109</b>	512	101

**SPECrate2017\_int\_base = 149**

**SPECrate2017\_int\_peak = 158**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runcpu before the start of the run:

LD\_LIBRARY\_PATH = "/spec/lib/ia32:/spec/lib/intel64:/spec/je5.0.1-32:/spec/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

runcpu command invoked through numactl i.e.:

```
numactl --interleave=all runcpu <etc>
```

jemalloc: configured and built at default for

32bit (i686) and 64bit (x86\_64) targets;

jemalloc: built with the RedHat Enterprise 7.4,

and the system compiler gcc 4.8.5;

jemalloc: sources available from jemalloc.net or

<https://github.com/jemalloc/jemalloc/releases>

(Continued on next page)



# SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Huawei

SPECrate2017\_int\_base = 149

Huawei XH321 V5 (Intel Xeon Gold 6136)

SPECrate2017\_int\_peak = 158

CPU2017 License: 3175

Test Date: May-2018

Test Sponsor: Huawei

Hardware Availability: Jul-2017

Tested by: Huawei

Software Availability: Jan-2018

## General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Platform Notes

BIOS configuration:

Power Policy Set to Performance

SNC Set to Enabled

IMC Interleaving Set to 1-way Interleave

XPT Prefetch Set to Enabled

ADDDC Sparing Set to Disabled

Sysinfo program /spec/bin/sysinfo

Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f

running on localhost.localdomain Wed May 16 10:10:58 2018

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Gold 6136 CPU @ 3.00GHz

2 "physical id"s (chips)

48 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 12

siblings : 24

physical 0: cores 0 1 2 3 4 9 10 16 18 19 25 26

physical 1: cores 0 3 4 5 6 7 16 18 19 20 21 22

From lscpu:

Architecture: x86\_64

CPU op-mode(s): 32-bit, 64-bit

Byte Order: Little Endian

CPU(s): 48

On-line CPU(s) list: 0-47

Thread(s) per core: 2

Core(s) per socket: 12

Socket(s): 2

NUMA node(s): 4

Vendor ID: GenuineIntel

CPU family: 6

(Continued on next page)



# SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Huawei

SPECCrate2017\_int\_base = 149

Huawei XH321 V5 (Intel Xeon Gold 6136)

SPECCrate2017\_int\_peak = 158

CPU2017 License: 3175

Test Date: May-2018

Test Sponsor: Huawei

Hardware Availability: Jul-2017

Tested by: Huawei

Software Availability: Jan-2018

## Platform Notes (Continued)

Model: 85  
Model name: Intel(R) Xeon(R) Gold 6136 CPU @ 3.00GHz  
Stepping: 4  
CPU MHz: 3000.000  
BogoMIPS: 6005.21  
Virtualization: VT-x  
L1d cache: 32K  
L1i cache: 32K  
L2 cache: 1024K  
L3 cache: 25344K  
NUMA node0 CPU(s): 0-2,5,7,10,24-26,29,31,34  
NUMA node1 CPU(s): 3,4,6,8,9,11,27,28,30,32,33,35  
NUMA node2 CPU(s): 12-14,18-20,36-38,42-44  
NUMA node3 CPU(s): 15-17,21-23,39-41,45-47

/proc/cpuinfo cache data  
cache size : 25344 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

available: 4 nodes (0-3)  
node 0 cpus: 0 1 2 5 7 10 24 25 26 29 31 34  
node 0 size: 96437 MB  
node 0 free: 93873 MB  
node 1 cpus: 3 4 6 8 9 11 27 28 30 32 33 35  
node 1 size: 98304 MB  
node 1 free: 95992 MB  
node 2 cpus: 12 13 14 18 19 20 36 37 38 42 43 44  
node 2 size: 98304 MB  
node 2 free: 96052 MB  
node 3 cpus: 15 16 17 21 22 23 39 40 41 45 46 47  
node 3 size: 98304 MB  
node 3 free: 95581 MB  
node distances:  
node 0 1 2 3  
0: 10 11 21 21  
1: 11 10 21 21  
2: 21 21 10 11  
3: 21 21 11 10

From /proc/meminfo  
MemTotal: 394174484 kB  
HugePages\_Total: 0  
Hugepagesize: 2048 kB

From /etc/\*release\* /etc/\*version\*  
os-release:

(Continued on next page)



# SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Huawei

SPECrate2017\_int\_base = 149

Huawei XH321 V5 (Intel Xeon Gold 6136)

SPECrate2017\_int\_peak = 158

CPU2017 License: 3175

Test Date: May-2018

Test Sponsor: Huawei

Hardware Availability: Jul-2017

Tested by: Huawei

Software Availability: Jan-2018

## Platform Notes (Continued)

```
NAME="Red Hat Enterprise Linux Server"
VERSION="7.3 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.3"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.3:ga:server

uname -a:
Linux localhost.localdomain 3.10.0-693.11.6.el7.x86_64 #1 SMP Thu Dec 28 14:23:39 EST
2017 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 May 16 10:09

SPEC is set to: /spec
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda8        xfs   325G   26G   300G   8%  /


Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS INSYDE Corp. 0.59 02/24/2018
Memory:
 4x NO DIMM NO DIMM
 12x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666

(End of data from sysinfo program)
```

## Compiler Version Notes

```
=====
CC 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
 525.x264_r(base, peak) 557.xz_r(base, peak)
-----
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
-----
=====

CC 500.perlbench_r(peak) 502.gcc_r(peak)
-----
```

(Continued on next page)



# SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Huawei

SPECrate2017\_int\_base = 149

Huawei XH321 V5 (Intel Xeon Gold 6136)

SPECrate2017\_int\_peak = 158

CPU2017 License: 3175

Test Date: May-2018

Test Sponsor: Huawei

Hardware Availability: Jul-2017

Tested by: Huawei

Software Availability: Jan-2018

## Compiler Version Notes (Continued)

icc (ICC) 18.0.0 20170811

Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

=====

CXXC 520.omnetpp\_r(base) 523.xalancbmk\_r(base) 531.deepsjeng\_r(base)  
541.leela\_r(base)

=====

icpc (ICC) 18.0.0 20170811

Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

=====

CXXC 520.omnetpp\_r(peak) 523.xalancbmk\_r(peak) 531.deepsjeng\_r(peak)  
541.leela\_r(peak)

=====

icpc (ICC) 18.0.0 20170811

Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

=====

FC 548.exchange2\_r(base, peak)

=====

ifort (IFORT) 18.0.0 20170811

Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

## Base Portability Flags

500.perlbench\_r: -DSPEC\_LP64 -DSPEC\_LINUX\_X64

502.gcc\_r: -DSPEC\_LP64

505.mcf\_r: -DSPEC\_LP64

(Continued on next page)



# SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

<b>Huawei</b>	<b>SPECrate2017_int_base = 149</b>
<b>Huawei XH321 V5 (Intel Xeon Gold 6136)</b>	<b>SPECrate2017_int_peak = 158</b>
<b>CPU2017 License:</b> 3175	<b>Test Date:</b> May-2018
<b>Test Sponsor:</b> Huawei	<b>Hardware Availability:</b> Jul-2017
<b>Tested by:</b> Huawei	<b>Software Availability:</b> Jan-2018

## **Base Portability Flags (Continued)**

```
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```

## C++ benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-fopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```

Fortran benchmarks

```
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte  
-L/usr/local/ie5.0.1-64/lib -lijemalloc
```

## Base Other Flags

C benchmarks:

-m64 -std=c11

## C++ benchmarks:

-m64

## Fortran benchmarks:

-m64

# Peak Compiler Invocation

C benchmarks:

icc

(Continued on next page)



# SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Huawei

SPECrate2017\_int\_base = 149

Huawei XH321 V5 (Intel Xeon Gold 6136)

SPECrate2017\_int\_peak = 158

CPU2017 License: 3175

Test Date: May-2018

Test Sponsor: Huawei

Hardware Availability: Jul-2017

Tested by: Huawei

Software Availability: Jan-2018

## Peak Compiler Invocation (Continued)

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

## Peak Portability Flags

500.perlbench\_r: -DSPEC\_LP64 -DSPEC\_LINUX\_X64  
502.gcc\_r: -D\_FILE\_OFFSET\_BITS=64  
505.mcf\_r: -DSPEC\_LP64  
520.omnetpp\_r: -DSPEC\_LP64  
523.xalancbmk\_r: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_LINUX  
525.x264\_r: -DSPEC\_LP64  
531.deepsjeng\_r: -DSPEC\_LP64  
541.leela\_r: -DSPEC\_LP64  
548.exchange2\_r: -DSPEC\_LP64  
557.xz\_r: -DSPEC\_LP64

## Peak Optimization Flags

C benchmarks:

500.perlbench\_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo  
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3  
-fno-strict-overflow -L/usr/local/je5.0.1-64/lib  
-ljemalloc

502.gcc\_r: -L/opt/intel/compilers\_and\_libraries\_2018/linux/lib/ia32  
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo  
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3  
-L/usr/local/je5.0.1-32/lib -ljemalloc

505.mcf\_r: basepeak = yes

525.x264\_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -fno-alias  
-L/usr/local/je5.0.1-64/lib -ljemalloc

557.xz\_r: basepeak = yes

(Continued on next page)



# SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Huawei

Huawei XH321 V5 (Intel Xeon Gold 6136)

CPU2017 License: 3175

Test Sponsor: Huawei

Tested by: Huawei

SPECrate2017\_int\_base = 149

SPECrate2017\_int\_peak = 158

Test Date: May-2018

Hardware Availability: Jul-2017

Software Availability: Jan-2018

## Peak Optimization Flags (Continued)

C++ benchmarks:

520.omnetpp\_r: basepeak = yes

523.xalancbmk\_r: -L/opt/intel/compilers\_and\_libraries\_2018/linux/lib/ia32  
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo  
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3  
-L/usr/local/je5.0.1-32/lib -ljemalloc

531.deepsjeng\_r: basepeak = yes

541.leela\_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo  
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3  
-L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:

-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte  
-L/usr/local/je5.0.1-64/lib -ljemalloc

## Peak Other Flags

C benchmarks (except as noted below):

-m64 -std=c11

502.gcc\_r: -m32 -std=c11

C++ benchmarks (except as noted below):

-m64

523.xalancbmk\_r: -m32

Fortran benchmarks:

-m64

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.9-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.9-revC.xml>



# SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Huawei

SPECCrate2017\_int\_base = 149

Huawei XH321 V5 (Intel Xeon Gold 6136)

SPECCrate2017\_int\_peak = 158

CPU2017 License: 3175

Test Date: May-2018

Test Sponsor: Huawei

Hardware Availability: Jul-2017

Tested by: Huawei

Software Availability: Jan-2018

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU2017 v1.0.2 on 2018-05-16 10:10:57-0400.

Report generated on 2018-10-31 17:47:44 by CPU2017 PDF formatter v6067.

Originally published on 2018-06-12.