



# SPEC® CPU2017 Floating Point Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

## Dell Inc.

SPECrate2017\_fp\_base = 205

PowerEdge FC640 (Intel Xeon Gold 6240M, 2.60GHz)

SPECrate2017\_fp\_peak = 210

CPU2017 License: 55

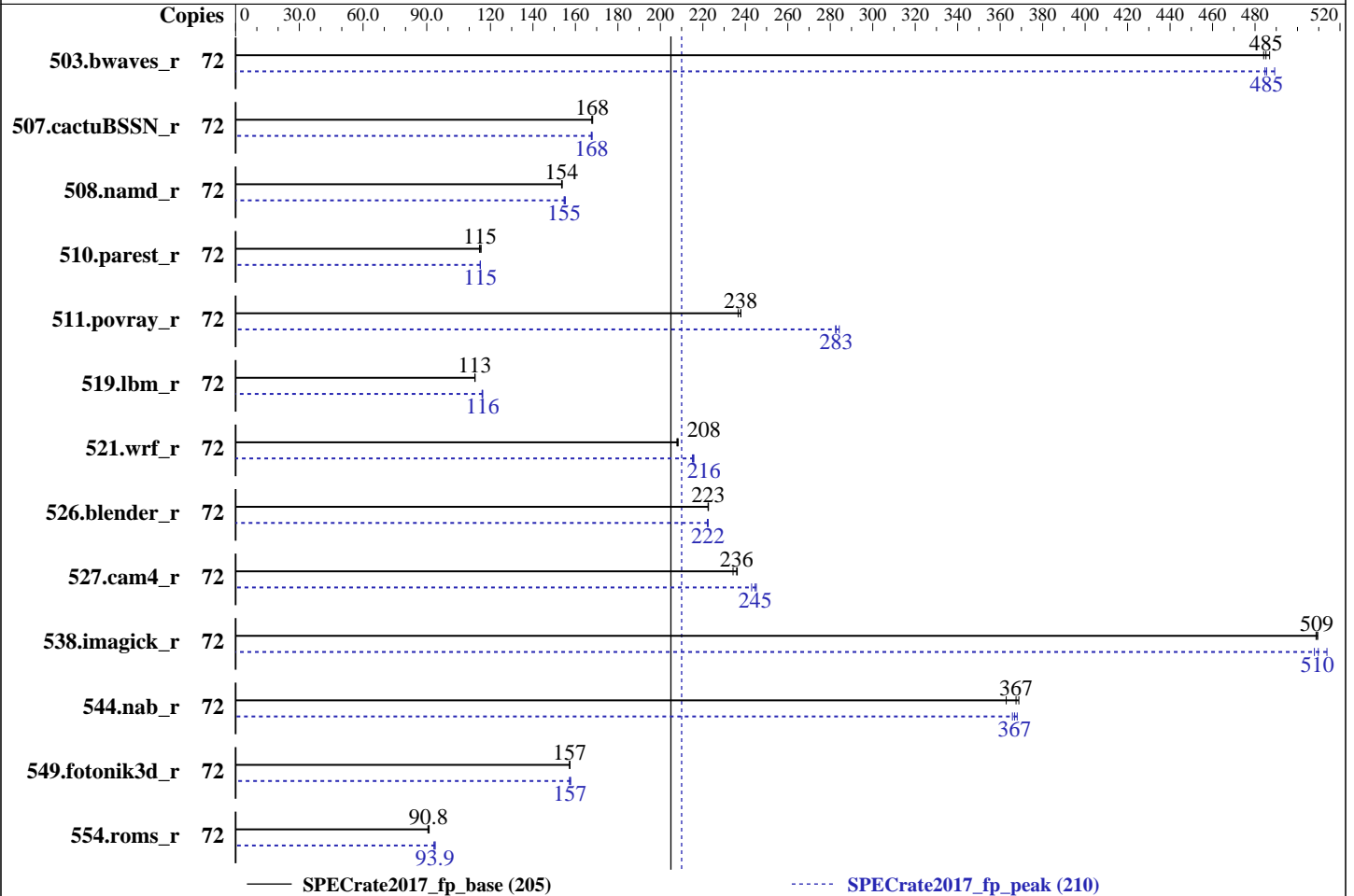
Test Date: Mar-2019

Test Sponsor: Dell Inc.

Hardware Availability: Apr-2019

Tested by: Dell Inc.

Software Availability: Feb-2019



### Hardware

CPU Name: Intel Xeon Gold 6240M  
 Max MHz.: 3900  
 Nominal: 2600  
 Enabled: 36 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 24.75 MB I+D on chip per chip  
 Other: None  
 Memory: 384 GB (12 x 32 GB 2Rx4 PC4-2933Y-R)  
 Storage: 1 x 480 GB SATA SSD  
 Other: None

### Software

OS: Ubuntu 18.04.2 LTS  
 kernel 4.15.0-45-generic  
 Compiler: C/C++: Version 19.0.1.144 of Intel C/C++  
 Compiler Build 20181018 for Linux;  
 Fortran: Version 19.0.1.144 of Intel Fortran  
 Compiler Build 20181018 for Linux  
 Parallel: No  
 Firmware: Version 2.2.2 released Mar-2019  
 File System: ext4  
 System State: Run level 5 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: None



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## Results Table

| Benchmark       | Base   |                    |                    |                    |                   |                   |                   | Peak   |                    |                    |                    |                   |                    |                   |
|-----------------|--------|--------------------|--------------------|--------------------|-------------------|-------------------|-------------------|--------|--------------------|--------------------|--------------------|-------------------|--------------------|-------------------|
|                 | Copies | Seconds            | Ratio              | Seconds            | Ratio             | Seconds           | Ratio             | Copies | Seconds            | Ratio              | Seconds            | Ratio             | Seconds            | Ratio             |
| 503.bwaves_r    | 72     | 1483               | 487                | <b><u>1488</u></b> | <b><u>485</u></b> | 1492              | 484               | 72     | 1490               | 485                | <b><u>1487</u></b> | <b><u>485</u></b> | 1476               | 489               |
| 507.cactuBSSN_r | 72     | 542                | 168                | <b><u>543</u></b>  | <b><u>168</u></b> | 543               | 168               | 72     | 543                | 168                | <b><u>544</u></b>  | <b><u>168</u></b> | 544                | 167               |
| 508.namd_r      | 72     | <b><u>446</u></b>  | <b><u>154</u></b>  | 446                | 154               | 445               | 154               | 72     | 442                | 155                | <b><u>442</u></b>  | <b><u>155</u></b> | 441                | 155               |
| 510.parest_r    | 72     | 1629               | 116                | <b><u>1635</u></b> | <b><u>115</u></b> | 1640              | 115               | 72     | 1636               | 115                | 1634               | 115               | <b><u>1635</u></b> | <b><u>115</u></b> |
| 511.povray_r    | 72     | 707                | 238                | 710                | 237               | <b><u>707</u></b> | <b><u>238</u></b> | 72     | 595                | 283                | 591                | 284               | <b><u>595</u></b>  | <b><u>283</u></b> |
| 519.lbm_r       | 72     | 674                | 113                | <b><u>674</u></b>  | <b><u>113</u></b> | 673               | 113               | 72     | 652                | 116                | 654                | 116               | <b><u>653</u></b>  | <b><u>116</u></b> |
| 521.wrf_r       | 72     | 776                | 208                | <b><u>775</u></b>  | <b><u>208</u></b> | 774               | 208               | 72     | <b><u>748</u></b>  | <b><u>216</u></b>  | 747                | 216               | 750                | 215               |
| 526.blender_r   | 72     | 493                | 222                | 493                | 223               | <b><u>493</u></b> | <b><u>223</u></b> | 72     | <b><u>493</u></b>  | <b><u>222</u></b>  | 493                | 222               | 494                | 222               |
| 527.cam4_r      | 72     | <b><u>534</u></b>  | <b><u>236</u></b>  | 538                | 234               | 533               | 236               | 72     | 514                | 245                | 518                | 243               | <b><u>515</u></b>  | <b><u>245</u></b> |
| 538.imagick_r   | 72     | 352                | 509                | <b><u>352</u></b>  | <b><u>509</u></b> | 351               | 510               | 72     | 348                | 514                | <b><u>351</u></b>  | <b><u>510</u></b> | 353                | 508               |
| 544.nab_r       | 72     | 334                | 363                | 329                | 369               | <b><u>330</u></b> | <b><u>367</u></b> | 72     | <b><u>330</u></b>  | <b><u>367</u></b>  | 329                | 368               | 331                | 366               |
| 549.fotonik3d_r | 72     | 1784               | 157                | <b><u>1784</u></b> | <b><u>157</u></b> | 1782              | 157               | 72     | 1778               | 158                | <b><u>1782</u></b> | <b><u>157</u></b> | 1785               | 157               |
| 554.roms_r      | 72     | <b><u>1260</u></b> | <b><u>90.8</u></b> | 1263               | 90.5              | 1255              | 91.1              | 72     | <b><u>1219</u></b> | <b><u>93.9</u></b> | 1218               | 93.9              | 1225               | 93.4              |

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5  
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation

(Continued on next page)



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## General Notes (Continued)

Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

## Platform Notes

BIOS settings:  
ADDDC setting disabled  
Sub NUMA Cluster enabled  
Virtualization Technology disabled  
DCU Streamer Prefetcher enabled  
System Profile set to Custom  
CPU Performance set to Maximum Performance  
C States set to Autonomous  
C1E disabled  
Uncore Frequency set to Dynamic  
Energy Efficiency Policy set to Performance  
Memory Patrol Scrub disabled  
Logical Processor enabled  
CPU Interconnect Bus Link Power Management disabled  
PCI ASPM L1 Link Power Management disabled  
Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9  
running on intel-sut Sun May 19 23:31:57 2019

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) Gold 6240M CPU @ 2.60GHz  
2 "physical id"s (chips)  
72 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 18  
siblings : 36  
physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27  
physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27

From lscpu:  
Architecture: x86\_64  
CPU op-mode(s): 32-bit, 64-bit  
Byte Order: Little Endian  
CPU(s): 72

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## Platform Notes (Continued)

```

On-line CPU(s) list: 0-71
Thread(s) per core: 2
Core(s) per socket: 18
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6240M CPU @ 2.60GHz
Stepping: 7
CPU MHz: 2423.132
BogoMIPS: 5200.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 25344K
NUMA node0 CPU(s): 0,4,8,12,16,20,24,28,32,36,40,44,48,52,56,60,64,68
NUMA node1 CPU(s): 1,5,9,13,17,21,25,29,33,37,41,45,49,53,57,61,65,69
NUMA node2 CPU(s): 2,6,10,14,18,22,26,30,34,38,42,46,50,54,58,62,66,70
NUMA node3 CPU(s): 3,7,11,15,19,23,27,31,35,39,43,47,51,55,59,63,67,71
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand
lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single ssbd mba ibrs
ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust
bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed adx
smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1
xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local dtherm ida arat pln pts pku
ospke avx512_vnni flush_lld arch_capabilities

```

```

/proc/cpuinfo cache data
cache size : 25344 KB

```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```

available: 4 nodes (0-3)
node 0 cpus: 0 4 8 12 16 20 24 28 32 36 40 44 48 52 56 60 64 68
node 0 size: 95144 MB
node 0 free: 94612 MB
node 1 cpus: 1 5 9 13 17 21 25 29 33 37 41 45 49 53 57 61 65 69
node 1 size: 96764 MB
node 1 free: 96177 MB
node 2 cpus: 2 6 10 14 18 22 26 30 34 38 42 46 50 54 58 62 66 70
node 2 size: 96764 MB

```

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## Platform Notes (Continued)

```
node 2 free: 96236 MB
node 3 cpus: 3 7 11 15 19 23 27 31 35 39 43 47 51 55 59 63 67 71
node 3 size: 96763 MB
node 3 free: 96303 MB
node distances:
node 0 1 2 3
0: 10 21 11 21
1: 21 10 21 11
2: 11 21 10 21
3: 21 11 21 10
```

```
From /proc/meminfo
MemTotal: 394686856 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

```
/usr/bin/lsb_release -d
Ubuntu 18.04.2 LTS
```

```
From /etc/*release* /etc/*version*
debian_version: buster/sid
os-release:
NAME="Ubuntu"
VERSION="18.04.2 LTS (Bionic Beaver)"
ID=ubuntu
ID_LIKE=debian
PRETTY_NAME="Ubuntu 18.04.2 LTS"
VERSION_ID="18.04"
HOME_URL="https://www.ubuntu.com/"
SUPPORT_URL="https://help.ubuntu.com/"
```

```
uname -a:
Linux intel-sut 4.15.0-45-generic #48-Ubuntu SMP Tue Jan 29 16:28:13 UTC 2019 x86_64
x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

```
CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB
```

run-level 5 May 19 15:00

```
SPEC is set to: /home/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 ext4 439G 19G 398G 5% /
```

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## Platform Notes (Continued)

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 2.2.2 03/05/2019

Memory:

3x 00AD00B300AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933

9x 00AD063200AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933

4x Not Specified Not Specified

(End of data from sysinfo program)

## Compiler Version Notes

=====  
CC 519.lbm\_r(base) 538.imagick\_r(base, peak) 544.nab\_r(base, peak)  
-----

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.1.144 Build 20181018  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
-----

=====  
CC 519.lbm\_r(peak)  
-----

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
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=====  
CXXC 508.namd\_r(base) 510.parest\_r(base, peak)  
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CXXC 508.namd\_r(peak)  
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## Compiler Version Notes (Continued)

=====  
CC 511.povray\_r(base) 526.blender\_r(base, peak)  
-----

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,  
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Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.1.144 Build 20181018  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
-----

=====  
CC 511.povray\_r(peak)  
-----

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Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
-----

=====  
FC 507.cactuBSSN\_r(base, peak)  
-----

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,  
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Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.1.144 Build 20181018  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.0.1.144 Build 20181018  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
-----

=====  
FC 503.bwaves\_r(base, peak) 549.fotonik3d\_r(base, peak) 554.roms\_r(base)  
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Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
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-----

=====  
FC 554.roms\_r(peak)  
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=====  
CC 521.wrf\_r(base) 527.cam4\_r(base)  
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CC 521.wrf\_r(peak) 527.cam4\_r(peak)  
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Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

## Base Compiler Invocation

C benchmarks:

icc -m64 -std=c11

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:

icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:

icpc -m64 icc -m64 -std=c11 ifort -m64





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## Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64  
507.cactuBSSN\_r: -DSPEC\_LP64  
508.namd\_r: -DSPEC\_LP64  
510.parest\_r: -DSPEC\_LP64  
511.povray\_r: -DSPEC\_LP64  
519.lbm\_r: -DSPEC\_LP64  
521.wrf\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian  
526.blender\_r: -DSPEC\_LP64 -DSPEC\_LINUX -funsigned-char  
527.cam4\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG  
538.imagick\_r: -DSPEC\_LP64  
544.nab\_r: -DSPEC\_LP64  
549.fotonik3d\_r: -DSPEC\_LP64  
554.roms\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=4

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=4

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs  
-align array32byte

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs  
-align array32byte

Benchmarks using both C and C++:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=4

Benchmarks using Fortran, C, and C++:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs  
-align array32byte



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## Peak Compiler Invocation

C benchmarks:

```
icc -m64 -std=c11
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
ifort -m64 icc -m64 -std=c11
```

Benchmarks using both C and C++:

```
icpc -m64 icc -m64 -std=c11
```

Benchmarks using Fortran, C, and C++:

```
icpc -m64 icc -m64 -std=c11 ifort -m64
```

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
519.lbm_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4
```

```
538.imagick_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4
```

544.nab\_r: Same as 538.imagick\_r

C++ benchmarks:

```
508.namd_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4
```

(Continued on next page)



# SPEC CPU2017 Floating Point Rate Result

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Dell Inc.

SPECrate2017\_fp\_base = 205

PowerEdge FC640 (Intel Xeon Gold 6240M, 2.60GHz)

SPECrate2017\_fp\_peak = 210

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Mar-2019

Hardware Availability: Apr-2019

Software Availability: Feb-2019

## Peak Optimization Flags (Continued)

```
510.parest_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4
```

Fortran benchmarks:

```
503.bwaves_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -auto
-nostandard-realloc-lhs -align array32byte
```

549.fotonik3d\_r: Same as 503.bwaves\_r

```
554.roms_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte
```

Benchmarks using both Fortran and C:

```
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte
```

Benchmarks using both C and C++:

```
511.povray_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4
```

```
526.blender_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4
```

Benchmarks using Fortran, C, and C++:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2019-04-02.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge14G-revE2.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2019-04-02.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge14G-revE2.xml>



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