



# SPEC CPU®2017 Integer Speed Result

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**Huawei**  
(Test Sponsor: China Academy of Information and Communications Technology)

SPECspeed®2017\_int\_base = 11.3

Huawei 1288H V5 (Intel Xeon Gold 5218R, 2.10 GHz)

SPECspeed®2017\_int\_peak = 11.6

CPU2017 License: 6177

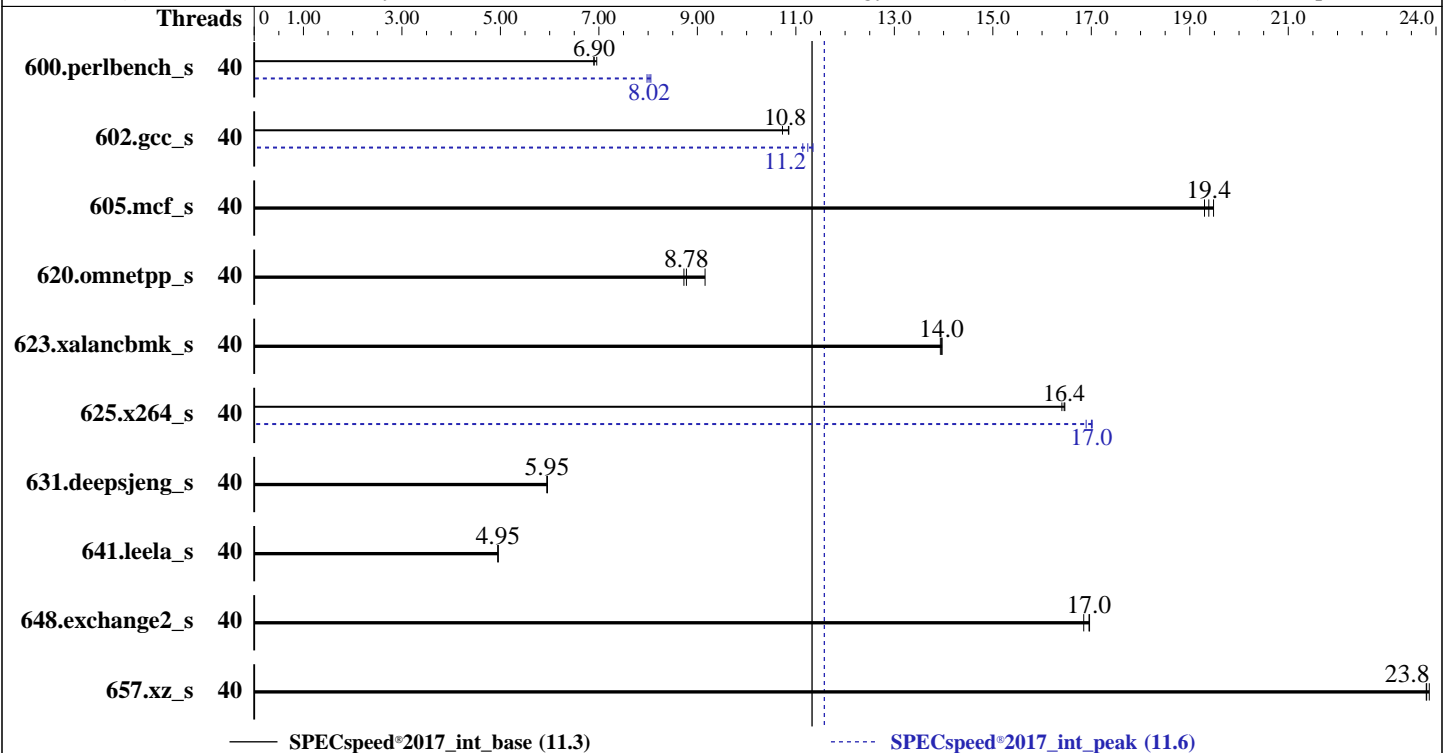
Test Date: Oct-2020

Test Sponsor: China Academy of Information and Communications Technology

Hardware Availability: Jul-2020

Tested by: China Academy of Information and Communications Technology

Software Availability: Apr-2020



## Hardware

CPU Name: Intel Xeon Gold 5218R  
 Max MHz: 4000  
 Nominal: 2100  
 Enabled: 40 cores, 2 chips  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 27.5 MB I+D on chip per chip  
 Other: None  
 Memory: 768 GB (24 x 32 GB 2Rx4 PC4-2933Y-R, running at 2666)  
 Storage: 1 x 900 GB SAS SSD  
 Other: None

## Software

OS: SUSE Linux Enterprise Server 12 SP4 (x86\_64)  
 Kernel 4.12.14-94.41-default  
 Compiler: C/C++: Version 19.1.1.217 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 19.1.1.217 of Intel Fortran Compiler for Linux  
 Parallel: Yes  
 Firmware: Version 6.83 released Jun-2019  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS set to prefer performance at the cost of additional power usage.



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## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	40	257	6.90	255	6.96	<b>257</b>	<b>6.90</b>	40	222	7.98	<b>221</b>	<b>8.02</b>	220	8.06
602.gcc_s	40	367	10.9	<b>367</b>	<b>10.8</b>	371	10.7	40	357	11.1	<b>354</b>	<b>11.2</b>	351	11.3
605.mcf_s	40	<b>244</b>	<b>19.4</b>	245	19.3	242	19.5	40	<b>244</b>	<b>19.4</b>	245	19.3	242	19.5
620.omnetpp_s	40	187	8.73	<b>186</b>	<b>8.78</b>	178	9.16	40	187	8.73	<b>186</b>	<b>8.78</b>	178	9.16
623.xalancbmk_s	40	102	13.9	<b>102</b>	<b>14.0</b>	101	14.0	40	102	13.9	<b>102</b>	<b>14.0</b>	101	14.0
625.x264_s	40	<b>107</b>	<b>16.4</b>	107	16.5	108	16.4	40	104	16.9	104	17.0	<b>104</b>	<b>17.0</b>
631.deepsjeng_s	40	241	5.94	241	5.96	<b>241</b>	<b>5.95</b>	40	241	5.94	241	5.96	<b>241</b>	<b>5.95</b>
641.leela_s	40	<b>344</b>	<b>4.95</b>	344	4.95	345	4.95	40	<b>344</b>	<b>4.95</b>	344	4.95	345	4.95
648.exchange2_s	40	175	16.8	173	17.0	<b>173</b>	<b>17.0</b>	40	175	16.8	173	17.0	<b>173</b>	<b>17.0</b>
657.xz_s	40	259	23.9	260	23.8	<b>260</b>	<b>23.8</b>	40	259	23.9	260	23.8	<b>260</b>	<b>23.8</b>

SPECspeed®2017\_int\_base = **11.3**

SPECspeed®2017\_int\_peak = **11.6**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The inconsistent Compiler version information under Compiler Version section is due to a discrepancy in Intel Compiler. The correct version of C/C++ compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux  
The correct version of Fortran compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
KMP\_AFFINITY = "granularity=fine,compact,1,0"  
LD\_LIBRARY\_PATH =  
"/opt/intel/compilers\_and\_libraries\_2020.1.217/linux/compiler/lib/intel64:/usr/local/jemalloc64-5.0.1"  
MALLOC\_CONF = "retain:true"  
OMP\_STACKSIZE = "192M"

## General Notes

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)

(Continued on next page)



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## General Notes (Continued)

is mitigated in the system as tested and documented.  
 Transparent Huge Pages enabled by default  
 Prior to runcpu invocation  
 Filesystem page cache synced and cleared with:  

```
sync; echo 3> /proc/sys/vm/drop_caches
```

 jemalloc, a general purpose malloc implementation  
 built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
 sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

## Platform Notes

BIOS configuration:  
 Power Policy Set to Load Balance  
 Hyper-Threading Set to Disabled  
 XPT Prefetch Set to Enabled

Sysinfo program /spec2017/bin/sysinfo  
 Rev: r6365 of 2019-08-21 295195f888a3d7edble6e46a485a0011  
 running on linux-j3dr Sat Oct 24 18:59:05 2020

SUT (System Under Test) info as seen by some common utilities.  
 For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) Gold 5218R CPU @ 2.10GHz
 2 "physical id"s (chips)
 40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 20
siblings : 20
physical 0: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
physical 1: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
```

From lscpu:

```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 40
On-line CPU(s) list: 0-39
Thread(s) per core: 1
Core(s) per socket: 20
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
```

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### Platform Notes (Continued)

```

CPU family:           6
Model:                85
Model name:           Intel(R) Xeon(R) Gold 5218R CPU @ 2.10GHz
Stepping:             7
CPU MHz:              2100.000
CPU max MHz:          4000.0000
CPU min MHz:          800.0000
BogoMIPS:             4200.00
Virtualization:       VT-x
L1d cache:            32K
L1i cache:            32K
L2 cache:             1024K
L3 cache:             28160K
NUMA node0 CPU(s):   0-19
NUMA node1 CPU(s):   20-39

```

```

Flags:                fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
invpcid_single ssbd mba ibrs ibpb stibp tpr_shadow vnmi flexpriority ept vpid
fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f
avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl
xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
dtherm ida arat pln pts pku ospke avx512_vnni flush_lld arch_capabilities

```

```

/proc/cpuinfo cache data
cache size : 28160 KB

```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```

available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19
node 0 size: 385551 MB
node 0 free: 383665 MB
node 1 cpus: 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39
node 1 size: 387065 MB
node 1 free: 384749 MB
node distances:
node  0  1
  0:  10  21
  1:  21  10

```

```

From /proc/meminfo
MemTotal:          791159924 kB
HugePages_Total:      0

```

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### Platform Notes (Continued)

Hugepagesize: 2048 kB

From /etc/\*release\* /etc/\*version\*

SuSE-release:

SUSE Linux Enterprise Server 12 (x86\_64)

VERSION = 12

PATCHLEVEL = 4

# This file is deprecated and will be removed in a future service pack or release.

# Please check /etc/os-release for details about this release.

os-release:

NAME="SLES"

VERSION="12-SP4"

VERSION\_ID="12.4"

PRETTY\_NAME="SUSE Linux Enterprise Server 12 SP4"

ID="sles"

ANSI\_COLOR="0;32"

CPE\_NAME="cpe:/o:suse:sles:12:sp4"

uname -a:

Linux linux-j3dr 4.12.14-94.41-default #1 SMP Wed Oct 31 12:25:04 UTC 2018 (3090901)  
x86\_64 x86\_64 x86\_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-3620 (L1 Terminal Fault):

Not affected

Microarchitectural Data Sampling:

No status reported

CVE-2017-5754 (Meltdown):

Not affected

CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp

CVE-2017-5753 (Spectre variant 1):

Mitigation: \_\_user pointer sanitization

CVE-2017-5715 (Spectre variant 2):

Mitigation: Indirect Branch Restricted

Speculation, IBPB, IBRS\_FW

run-level 3 Oct 24 14:50

SPEC is set to: /spec2017

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda3	xfs	734G	45G	690G	7%	/

From /sys/devices/virtual/dmi/id

BIOS: INSYDE Corp. 6.83 06/29/2019

Vendor: Huawei

Product: 1288H V5

Product Family: Purley

Serial: 210200351910KC000123

Additional information from dmidecode follows. WARNING: Use caution when you interpret

(Continued on next page)



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### Platform Notes (Continued)

this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

24x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933, configured at 2666

(End of data from sysinfo program)

### Compiler Version Notes

```

=====
C          | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)
          | 625.x264_s(base, peak) 657.xz_s(base, peak)
-----

```

```

Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
-----

```

```

=====
C          | 600.perlbench_s(peak)
-----

```

```

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
-----

```

```

=====
C          | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)
          | 625.x264_s(base, peak) 657.xz_s(base, peak)
-----

```

```

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-----

```

```

=====
C          | 600.perlbench_s(peak)
-----

```

```

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
-----

```

```

=====
C++       | 620.omnetpp_s(base, peak) 623.xalanbmk_s(base, peak)
-----

```

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## Compiler Version Notes (Continued)

| 631.deepsjeng\_s(base, peak) 641.leela\_s(base, peak)

-----  
Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1  
NextGen Build 20200304  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====  
Fortran | 648.exchange2\_s(base, peak)

-----  
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.1.1.217 Build 20200306  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

## Base Portability Flags

600.perlbench\_s: -DSPEC\_LP64 -DSPEC\_LINUX\_X64  
602.gcc\_s: -DSPEC\_LP64  
605.mcf\_s: -DSPEC\_LP64  
620.omnetpp\_s: -DSPEC\_LP64  
623.xalancbmk\_s: -DSPEC\_LP64 -DSPEC\_LINUX  
625.x264\_s: -DSPEC\_LP64  
631.deepsjeng\_s: -DSPEC\_LP64  
641.leela\_s: -DSPEC\_LP64  
648.exchange2\_s: -DSPEC\_LP64  
657.xz\_s: -DSPEC\_LP64



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## Base Optimization Flags

C benchmarks:

```
-m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse -funroll-loops
-fuse-ld=gold -qopt-mem-layout-trans=4 -fopenmp -DSPEC_OPENMP
-L/usr/local/jemalloc64-5.0.1/ -ljemalloc
```

C++ benchmarks:

```
-m64 -qnextgen -Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse
-funroll-loops -fuse-ld=gold -qopt-mem-layout-trans=4
-L/opt/intel/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin
-lqkmalloc
```

Fortran benchmarks:

```
-m64 -Wl,-plugin-opt=-x86-branches-within-32B-boundaries -xCORE-AVX512
-O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte
-mbranches-within-32B-boundaries
```

## Peak Compiler Invocation

C benchmarks:

```
icc
```

C++ benchmarks:

```
icpc
```

Fortran benchmarks:

```
ifort
```

## Peak Portability Flags

```
600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gc_s: -DSPEC_LP64(*) -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
```

(Continued on next page)





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## Peak Portability Flags (Continued)

648.exchange2\_s: -DSPEC\_LP64

657.xz\_s: -DSPEC\_LP64

(\*) Indicates a portability flag that was found in a non-portability variable.

## Peak Optimization Flags

C benchmarks:

```
600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/ -ljemalloc
```

```
602.gcc_s: -m64 -qnextgen -std=c11 -fuse-ld=gold
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
-L/usr/local/jemalloc64-5.0.1/ -ljemalloc
```

605.mcf\_s: basepeak = yes

```
625.x264_s: -m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -xCORE-AVX512 -flto -O3 -ffast-math
-fuse-ld=gold -qopt-mem-layout-trans=4 -fno-alias
-L/usr/local/jemalloc64-5.0.1/ -ljemalloc
```

657.xz\_s: basepeak = yes

C++ benchmarks:

620.omnetpp\_s: basepeak = yes

623.xalancbmk\_s: basepeak = yes

631.deepsjeng\_s: basepeak = yes

641.leela\_s: basepeak = yes

Fortran benchmarks:

(Continued on next page)



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## Peak Optimization Flags (Continued)

648.exchange2\_s:basepeak = yes

The flags files that were used to format this result can be browsed at

[http://www.spec.org/cpu2017/flags/Intel-ic19.1ul-official-linux64\\_revB.html](http://www.spec.org/cpu2017/flags/Intel-ic19.1ul-official-linux64_revB.html)

<http://www.spec.org/cpu2017/flags/CAICT-Platform-Settings-V1.3.html>

You can also download the XML flags sources by saving the following links:

[http://www.spec.org/cpu2017/flags/Intel-ic19.1ul-official-linux64\\_revB.xml](http://www.spec.org/cpu2017/flags/Intel-ic19.1ul-official-linux64_revB.xml)

<http://www.spec.org/cpu2017/flags/CAICT-Platform-Settings-V1.3.xml>

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