



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R7525 (AMD EPYC 7773X 64-Core Processor)

SPECSspeed®2017_fp_base = 269

SPECSspeed®2017_fp_peak = 278

CPU2017 License: 55

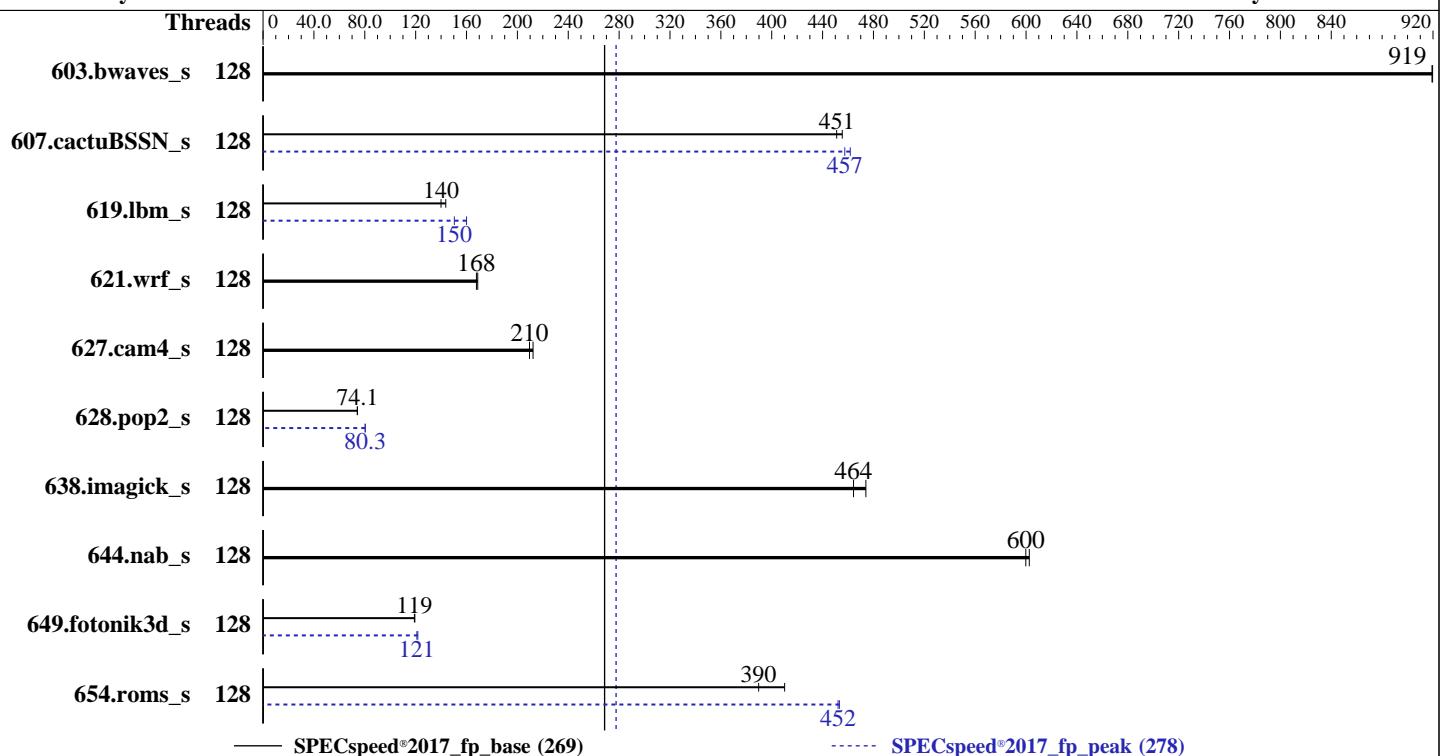
Test Date: Feb-2022

Test Sponsor: Dell Inc.

Hardware Availability: Mar-2022

Tested by: Dell Inc.

Software Availability: Feb-2022



Hardware		Software	
CPU Name:	AMD EPYC 7773X	OS:	Ubuntu 20.04.4 LTS
Max MHz:	3500	Compiler:	5.4.0-100-generic
Nominal:	2200	Parallel:	C/C++/Fortran: Version 3.2.0 of AOCC
Enabled:	128 cores, 2 chips	Firmware:	Yes
Orderable:	1,2 chips	File System:	Version 2.6.5 released Dec-2021
Cache L1:	32 KB I + 32 KB D on chip per core	System State:	tmpfs
L2:	512 KB I+D on chip per core	Base Pointers:	Run level 3 (multi-user)
L3:	768 MB I+D on chip per chip, 96 MB shared / 8 cores	Peak Pointers:	64-bit
Other:	None	Other:	64-bit
Memory:	2 TB (16 x 128 GB 4Rx4 PC4-3200AA-L)	Power Management:	jemalloc: jemalloc memory allocator library v5.1.0
Storage:	125 GB on tmpfs		BIOS and OS set to prefer performance at the cost of additional power usage.
Other:	None		



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Results Table

Benchmark	Base								Peak							
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	128	64.2	919	64.2	920			128	64.2	919	64.2	920				
607.cactuBSSN_s	128	37.0	451	36.6	455			128	36.4	457	36.1	462				
619.lbm_s	128	36.5	144	37.4	140			128	34.8	150	32.7	160				
621.wrf_s	128	78.8	168	78.3	169			128	78.8	168	78.3	169				
627.cam4_s	128	42.3	210	41.7	212			128	42.3	210	41.7	212				
628.pop2_s	128	160	74.2	160	74.1			128	148	80.3	148	80.4				
638.imagick_s	128	30.4	474	31.1	464			128	30.4	474	31.1	464				
644.nab_s	128	29.1	600	29.0	603			128	29.1	600	29.0	603				
649.fotonik3d_s	128	76.6	119	76.4	119			128	74.9	122	75.4	121				
654.roms_s	128	38.4	410	40.4	390			128	34.7	453	34.8	452				
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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
 'numactl' was used to bind copies to the cores.
 See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
 'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
 numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
 To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
 To free node-local memory and avoid remote memory usage,
 'sysctl -w vm.zone_reclaim_mode=1' run as root.
 To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
 To disable address space layout randomization (ASLR) to reduce run-to-run
 variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,

(Continued on next page)



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Operating System Notes (Continued)

```
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.  
To enable THP only on request for peak runs of 628.pop2_s:  
'echo madvise > /sys/kernel/mm/transparent_hugepage/enabled' run as root.  
To disable THP for peak runs of 627.cam4_s, 649.fotonik3d_s, and 654.roms_s,  
'echo never > /sys/kernel/mm/transparent_hugepage/enabled' run as root.
```

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
GOMP_CPU_AFFINITY = "0-127"  
LD_LIBRARY_PATH =  
    "/mnt/ramdisk/cpu2017-1.1.8-aocc320-A1/amd_speed_aocc320_milanx_A_lib/li  
b:/mnt/ramdisk/cpu2017-1.1.8-aocc320-A1/amd_speed_aocc320_milanx_A_lib/l  
ib32:"  
LIBOMP_NUM_HIDDEN_HELPER_THREADS = "0"  
MALLOC_CONF = "retain:true"  
OMP_DYNAMIC = "false"  
OMP_SCHEDULE = "static"  
OMP_STACKSIZE = "128M"  
OMP_THREAD_LIMIT = "128"
```

Environment variables set by runcpu during the 607.cactubssn_s peak run:

```
GOMP_CPU_AFFINITY = "0-127"
```

Environment variables set by runcpu during the 619.lbm_s peak run:

```
GOMP_CPU_AFFINITY = "0-127"
```

Environment variables set by runcpu during the 628.pop2_s peak run:

```
GOMP_CPU_AFFINITY = "0-127"
```

Environment variables set by runcpu during the 649.fotonik3d_s peak run:

```
GOMP_CPU_AFFINITY = "0-127"
```

```
PGHPF_ZMEM = "yes"
```

Environment variables set by runcpu during the 654.roms_s peak run:

```
GOMP_CPU_AFFINITY = "0-127"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 7742 CPU + 1TiB Memory using openSUSE 15.2

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

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General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v4.8.2 in RHEL 7.4 (No options specified)

jemalloc 5.1.0 is available here:

<https://github.com/jemalloc/jemalloc/releases/download/5.1.0/jemalloc-5.1.0.tar.bz2>

Benchmark run from a 125 GB ramdisk created with the cmd: "mount -t tmpfs -o size=125G tmpfs /mnt/ramdisk"

Platform Notes

BIOS settings:

Logical Processor : Disabled

L3 Cache as NUMA Domain : Enabled

Virtualization Technology : Disabled

DRAM Refresh Delay : Performance

System Profile : Custom

CPU Power Management : Maximum Performance

Memory Patrol Scrub : Disabled

PCI ASPM L1 Link

Power Management : Disabled

Algorithm Performance

Boost Disable (ApbDis): Enabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.8-aocc320-A1/bin/sysinfo

Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d

running on spa-dev-sut Fri Feb 25 16:36:33 2022

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : AMD EPYC 7773X 64-Core Processor

2 "physical id"s (chips)

128 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 64

siblings : 64

physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24

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Platform Notes (Continued)

```
25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52
53 54 55 56 57 58 59 60 61 62 63
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52
53 54 55 56 57 58 59 60 61 62 63
```

From lscpu from util-linux 2.34:

Architecture:	x86_64
CPU op-mode(s):	32-bit, 64-bit
Byte Order:	Little Endian
Address sizes:	48 bits physical, 48 bits virtual
CPU(s):	128
On-line CPU(s) list:	0-127
Thread(s) per core:	1
Core(s) per socket:	64
Socket(s):	2
NUMA node(s):	16
Vendor ID:	AuthenticAMD
CPU family:	25
Model:	1
Model name:	AMD EPYC 7773X 64-Core Processor
Stepping:	2
CPU MHz:	1796.149
BogoMIPS:	4391.64
Virtualization:	AMD-V
L1d cache:	4 MiB
L1i cache:	4 MiB
L2 cache:	64 MiB
L3 cache:	1.5 GiB
NUMA node0 CPU(s):	0-7
NUMA node1 CPU(s):	8-15
NUMA node2 CPU(s):	16-23
NUMA node3 CPU(s):	24-31
NUMA node4 CPU(s):	32-39
NUMA node5 CPU(s):	40-47
NUMA node6 CPU(s):	48-55
NUMA node7 CPU(s):	56-63
NUMA node8 CPU(s):	64-71
NUMA node9 CPU(s):	72-79
NUMA node10 CPU(s):	80-87
NUMA node11 CPU(s):	88-95
NUMA node12 CPU(s):	96-103
NUMA node13 CPU(s):	104-111
NUMA node14 CPU(s):	112-119
NUMA node15 CPU(s):	120-127
Vulnerability Itlb multihit:	Not affected
Vulnerability L1tf:	Not affected

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Platform Notes (Continued)

Vulnerability Mds: Not affected
 Vulnerability Meltdown: Not affected
 Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
 Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and __user pointer sanitization
 Vulnerability Spectre v2: Mitigation; Full AMD retrpoline, IBPB conditional, IBRS_FW, STIBP disabled, RSB filling
 Vulnerability Srbds: Not affected
 Vulnerability Tsx async abort: Not affected
 Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmpf perf pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osrw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwmtx cpb cat_13 cdp_13 invpcid_single hw_pstate ssbd mba ibrs ibpb stibp vmmcall fsgsbase bmi1 avx2 smep bmi2 invpcid cqmq rdt_a rdseed adx smap clflushopt clwb sha_ni xsaveopt xsavec xgetbv1 xsaves cqmq_llc cqmq_occup_llc cqmq_mbm_total cqmq_mbm_local clzero irperf xsaveerptr wbnoinvd arat npt lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter pfthreshold v_vmsave_vmload vgif umip pku ospke vaes vpclmulqdq rdpid overflow_recov succor smca

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL
L1d	32K	4M	8	Data	1
L1i	32K	4M	8	Instruction	1
L2	512K	64M	8	Unified	2
L3	96M	1.5G	16	Unified	3

/proc/cpuinfo cache data
 cache size : 512 KB

From numactl --hardware

WARNING: a numactl 'node' might or might not correspond to a physical chip.

```

available: 16 nodes (0-15)
node 0 cpus: 0 1 2 3 4 5 6 7
node 0 size: 128650 MB
node 0 free: 128481 MB
node 1 cpus: 8 9 10 11 12 13 14 15
node 1 size: 129021 MB
node 1 free: 128915 MB
node 2 cpus: 16 17 18 19 20 21 22 23
node 2 size: 129021 MB
node 2 free: 128903 MB
node 3 cpus: 24 25 26 27 28 29 30 31
node 3 size: 129021 MB

```

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Platform Notes (Continued)

```
node 3 free: 125448 MB
node 4 cpus: 32 33 34 35 36 37 38 39
node 4 size: 129021 MB
node 4 free: 128895 MB
node 5 cpus: 40 41 42 43 44 45 46 47
node 5 size: 129021 MB
node 5 free: 128906 MB
node 6 cpus: 48 49 50 51 52 53 54 55
node 6 size: 129021 MB
node 6 free: 128862 MB
node 7 cpus: 56 57 58 59 60 61 62 63
node 7 size: 116908 MB
node 7 free: 116793 MB
node 8 cpus: 64 65 66 67 68 69 70 71
node 8 size: 129021 MB
node 8 free: 128923 MB
node 9 cpus: 72 73 74 75 76 77 78 79
node 9 size: 129021 MB
node 9 free: 128929 MB
node 10 cpus: 80 81 82 83 84 85 86 87
node 10 size: 129021 MB
node 10 free: 128933 MB
node 11 cpus: 88 89 90 91 92 93 94 95
node 11 size: 129021 MB
node 11 free: 128927 MB
node 12 cpus: 96 97 98 99 100 101 102 103
node 12 size: 128993 MB
node 12 free: 128837 MB
node 13 cpus: 104 105 106 107 108 109 110 111
node 13 size: 129021 MB
node 13 free: 128802 MB
node 14 cpus: 112 113 114 115 116 117 118 119
node 14 size: 129021 MB
node 14 free: 128923 MB
node 15 cpus: 120 121 122 123 124 125 126 127
node 15 size: 129016 MB
node 15 free: 128922 MB
node distances:
node   0    1    2    3    4    5    6    7    8    9    10   11   12   13   14   15
  0: 10  11  11  11  11  11  11  11  32  32  32  32  32  32  32  32
  1: 11  10  11  11  11  11  11  11  32  32  32  32  32  32  32  32
  2: 11  11  10  11  11  11  11  11  32  32  32  32  32  32  32  32
  3: 11  11  11  10  11  11  11  11  32  32  32  32  32  32  32  32
  4: 11  11  11  11  10  11  11  11  32  32  32  32  32  32  32  32
  5: 11  11  11  11  11  10  11  11  32  32  32  32  32  32  32  32
  6: 11  11  11  11  11  11  10  11  32  32  32  32  32  32  32  32
  7: 11  11  11  11  11  11  11  10  32  32  32  32  32  32  32  32
```

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Platform Notes (Continued)

```
8: 32 32 32 32 32 32 32 32 10 11 11 11 11 11 11 11 11 11
9: 32 32 32 32 32 32 32 32 11 10 11 11 11 11 11 11 11 11
10: 32 32 32 32 32 32 32 32 11 11 10 11 11 11 11 11 11 11
11: 32 32 32 32 32 32 32 32 11 11 11 10 11 11 11 11 11 11
12: 32 32 32 32 32 32 32 32 11 11 11 11 10 11 11 11 11 11
13: 32 32 32 32 32 32 32 32 11 11 11 11 11 10 11 11 11 11
14: 32 32 32 32 32 32 32 32 11 11 11 11 11 11 10 11 11 11
15: 32 32 32 32 32 32 32 32 11 11 11 11 11 11 11 11 10
```

From /proc/meminfo

```
MemTotal: 2101076456 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

```
/sbin/tuned-adm active
Current active profile: throughput-performance
```

```
/usr/bin/lsb_release -d
Ubuntu 20.04.4 LTS
```

```
From /etc/*release* /etc/*version*
debian_version: bullseye/sid
os-release:
  NAME="Ubuntu"
  VERSION="20.04.4 LTS (Focal Fossa)"
  ID=ubuntu
  ID_LIKE=debian
  PRETTY_NAME="Ubuntu 20.04.4 LTS"
  VERSION_ID="20.04"
  HOME_URL="https://www.ubuntu.com/"
  SUPPORT_URL="https://help.ubuntu.com/"
```

uname -a:

```
Linux spa-dev-sut 5.4.0-100-generic #113-Ubuntu SMP Thu Feb 3 18:43:29 UTC 2022 x86_64
x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):	Not affected
CVE-2018-3620 (L1 Terminal Fault):	Not affected
Microarchitectural Data Sampling:	Not affected
CVE-2017-5754 (Meltdown):	Not affected
CVE-2018-3639 (Speculative Store Bypass):	Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):	Mitigation: usercopy/swapgs barriers and __user pointer

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Platform Notes (Continued)

CVE-2017-5715 (Spectre variant 2):

sanitization

Mitigation: Full AMD retrpoline,

IBPB: conditional, IBRS_FW, STIBP:

disabled, RSB filling

CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected

CVE-2019-11135 (TSX Asynchronous Abort):

Not affected

run-level 3 Feb 25 14:05

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.8-aocc320-A1

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
tmpfs	tmpfs	125G	3.3G	122G	3%	/mnt/ramdisk

From /sys/devices/virtual/dmi/id

Vendor:	Dell Inc.
Product:	PowerEdge R7525
Product Family:	PowerEdge
Serial:	48LN333

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

16x 802C8632802C 72ASS16G72LZ-3G2B3 128 GB 4 rank 3200
16x Not Specified Not Specified

BIOS:

BIOS Vendor:	Dell Inc.
BIOS Version:	2.6.5
BIOS Date:	12/28/2021
BIOS Revision:	2.6

(End of data from sysinfo program)

Compiler Version Notes

=====

C	619.lbm_s(base, peak) 638.imagick_s(base, peak)
	644.nab_s(base, peak)

=====

AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on LLVM Mirror.Version.13.0.0)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin

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Compiler Version Notes (Continued)

=====
C++, C, Fortran | 607.cactusBSSN_s(base, peak)

AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
LLVM Mirror.Version.13.0.0)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin

AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
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Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin

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LLVM Mirror.Version.13.0.0)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin

=====
Fortran | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak)
| 654.roms_s(base, peak)

AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
LLVM Mirror.Version.13.0.0)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin

=====
Fortran, C | 621.wrf_s(base, peak) 627.cam4_s(base, peak)
| 628.pop2_s(base, peak)

AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
LLVM Mirror.Version.13.0.0)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin

AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
LLVM Mirror.Version.13.0.0)

Target: x86_64-unknown-linux-gnu

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Hardware Availability: Mar-2022

Software Availability: Feb-2022

Compiler Version Notes (Continued)

Base Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactusBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
627.cam4_s: -DSPEC_CASE_FLAG -DSPEC_LP64
628.pop2_s: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-m64 -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3
-fveclib=AMDLIBM -ffast-math -fopenmp -fsto -fstruct-layout=5
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3 -z muldefs

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SPECSpeed®2017_fp_base = 269

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PowerEdge R7525 (AMD EPYC 7773X 64-Core Processor)

CPU2017 License: 55

Test Date: Feb-2022

Test Sponsor: Dell Inc.

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Tested by: Dell Inc.

Software Availability: Feb-2022

Base Optimization Flags (Continued)

C benchmarks (continued):

```
-DSPEC_OPENMP -fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-licm-vrp -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Hz,1,0x1 -O3
-march=znver3 -fveclib=AMDLIBM -ffast-math -fopenmp -Mrecursive
-mllvm -fuse-tile-inner-loop -funroll-loops
-mllvm -extra-vectorizer-passes -mllvm -lsr-in-nested-loop
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -mllvm -enable-loopinterchange
-mllvm -compute-interchange-order -z muldefs -DSPEC_OPENMP
-fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang
```

Benchmarks using both Fortran and C:

```
-m64 -Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-licm-vrp -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=5
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3 -Hz,1,0x1
-Mrecursive -mllvm -fuse-tile-inner-loop -funroll-loops
-mllvm -extra-vectorizer-passes -mllvm -lsr-in-nested-loop
-mllvm -enable-loopinterchange -mllvm -compute-interchange-order
-z muldefs -DSPEC_OPENMP -fopenmp=libomp -lomp -lamdlibm -ljemalloc
-lflang
```

Benchmarks using Fortran, C, and C++:

```
-m64 -Wl,-mllvm -Wl,-x86-use-vzeroupper=false
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=5
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -enable-partial-unswitch -mllvm -unroll-threshold=100
-finline-aggressive -mllvm -loop-unswitch-threshold=200000
```

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Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):

```
-mllvm -reroll-loops -mllvm -aggressive-loop-unswitch  
-mllvm -extra-vectorizer-passes -mllvm -convert-pow-exp-to-int=false  
-Hz,1,0x1 -Mrecursive -mllvm -fuse-tile-inner-loop -funroll-loops  
-mllvm -lsr-in-nested-loop -mllvm -enable-loopinterchange  
-mllvm -compute-interchange-order -z muldefs -DSPEC_OPENMP  
-fopenmp=libomp -lomp -lamdlibm -ljemalloc -flang
```

Base Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument -Wno-return-type
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument -Wno-return-type
```

Benchmarks using both Fortran and C:

```
-Wno-unused-command-line-argument -Wno-return-type
```

Benchmarks using Fortran, C, and C++:

```
-Wno-unused-command-line-argument -Wno-return-type
```

Peak Compiler Invocation

C benchmarks:

```
clang
```

Fortran benchmarks:

```
flang
```

Benchmarks using both Fortran and C:

```
flang clang
```

Benchmarks using Fortran, C, and C++:

```
clang++ clang flang
```

Peak Portability Flags

Same as Base Portability Flags



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Peak Optimization Flags

C benchmarks:

```
619.lbm_s: -m64 -Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast  
-march=znver3 -fveclib=AMDLIBM -ffast-math -fopenmp  
-flto -fstruct-layout=5 -mllvm -unroll-threshold=50  
-fremap-arrays -flv-function-specialization  
-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist  
-mllvm -global-vectorize-slp=true  
-mllvm -function-specialize -mllvm -enable-licm-vrp  
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP  
-fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang
```

```
638.imagick_s: basepeak = yes
```

```
644.nab_s: basepeak = yes
```

Fortran benchmarks:

```
603.bwaves_s: basepeak = yes
```

```
649.fotonik3d_s: -m64 -Wl,-mllvm -Wl,-enable-X86-prefetching  
-Wl,-mllvm -Wl,-enable-licm-vrp  
-Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast  
-march=znver3 -fveclib=AMDLIBM -ffast-math -fopenmp  
-flto -Mrecursive -mllvm -reduce-array-computations=3  
-mllvm -global-vectorize-slp=true -mllvm -enable-licm-vrp  
-DSPEC_OPENMP -fopenmp=libomp -lomp -lamdlibm -ljemalloc  
-lflang
```

```
654.roms_s: -m64 -Wl,-mllvm -Wl,-enable-X86-prefetching  
-Wl,-mllvm -Wl,-enable-licm-vrp  
-Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast  
-march=znver3 -fveclib=AMDLIBM -ffast-math -fopenmp  
-Mrecursive -mllvm -reduce-array-computations=3  
-mllvm -global-vectorize-slp=true -mllvm -enable-licm-vrp  
-DSPEC_OPENMP -fopenmp=libomp -lomp -lamdlibm -ljemalloc  
-lflang
```

Benchmarks using both Fortran and C:

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Peak Optimization Flags (Continued)

621.wrf_s: basepeak = yes

627.cam4_s: basepeak = yes

```
628.pop2_s: -m64 -Wl,-mllvm -Wl,-enable-x86-prefetching
-Wl,-mllvm -Wl,-enable-licm-vrp
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -fstruct-layout=5 -mllvm -unroll-threshold=50
-freemap-arrays -flv-function-specialization
-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist
-mllvm -global-vectorize-slp=true
-mllvm -function-specialize -mllvm -enable-licm-vrp
-mllvm -reduce-array-computations=3 -Mrecursive
-DSPEC_OPENMP -fopenmp=libomp -lomp -lamdlibm -ljemalloc
-lflang
```

Benchmarks using Fortran, C, and C++:

```
-m64 -Wl,-mllvm -Wl,-x86-use-vzeroupper=false
-Wl,-mllvm -Wl,-enable-licm-vrp
-Wl,-mllvm -Wl,-do-block-reorder=aggressive
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast -march=znver3
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=5
-mllvm -unroll-threshold=50 -freemap-arrays -flv-function-specialization
-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist
-mllvm -global-vectorize-slp=true -mllvm -function-specialize
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-finline-aggressive -mllvm -unroll-threshold=100 -mllvm -reroll-loops
-mllvm -aggressive-loop-unswitch -Mrecursive
-mllvm -do-block-reorder=aggressive -DSPEC_OPENMP -fopenmp=libomp
-lomp -lamdlibm -ljemalloc -lflang
```

Peak Other Flags

C benchmarks:

-Wno-unused-command-line-argument -Wno-return-type

Fortran benchmarks:

-Wno-unused-command-line-argument -Wno-return-type

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Peak Other Flags (Continued)

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument -Wno-return-type

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument -Wno-return-type

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc320-flags-A1.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-Milan-rev2.4.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc320-flags-A1.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-Milan-rev2.4.xml>

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