SPEC CPU®2017 Integer Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Lenovo Global Technology

$SPECrate^{®}2017\_int\_base = 742$

ThinkSystem SR665 V3
(3.25 GHz,AMD EPYC 9354)

$SPECrate^{®}2017\_int\_peak = 761$

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jan-2023
Hardware Availability: Feb-2023
Software Availability: Nov-2022

Benchmark	Copies	base	peak
500.perlbench_r	128	524	
502.gcc_r	128	642	761
505.mcf_r	128	1070	
520.omnetpp_r	128	386	387
523.xalancbmk_r	128	990	1070
525.x264_r	128	1750	
531.deepsjeng_r	128	597	598
541.leela_r	128	568	569
548.exchange2_r	128	1680	1680
557.xz_r	128	367	367

SPECrate®2017_int_base (742)   SPECrate®2017_int_peak (761)

Hardware

CPU Name: AMD EPYC 9354
 Max MHz: 3800
 Nominal: 3250
Enabled: 64 cores, 2 chips, 2 threads/core
Orderable: 1,2 chips
Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 256 MB I+D on chip per chip,
  32 MB shared / 4 cores
 Other: None
Memory: 768 GB (24 x 32 GB 2Rx8 PC5-4800B-R)
Storage: 1 x 480 GB SATA SSD
Other: None

Software

OS: SUSE Linux Enterprise Server 15 SP4 (x86_64)
  Kernel 5.14.21-150400.22-default
Compiler: C/C++/Fortran: Version 4.0.0 of AOCC
Parallel: No
Firmware: Lenovo BIOS Version KAE105F 1.20 released Dec-2022
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other: None
Power Management: BIOS and OS set to prefer performance at the cost
  of additional power usage

Standard Performance Evaluation Corporation (info@spec.org)   https://www.spec.org/

SPEC CPU®2017 Integer Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR665 V3
(3.25 GHz,AMD EPYC 9354)

SPECrate®2017_int_base = 742

SPECrate®2017_int_peak = 761

Results Table
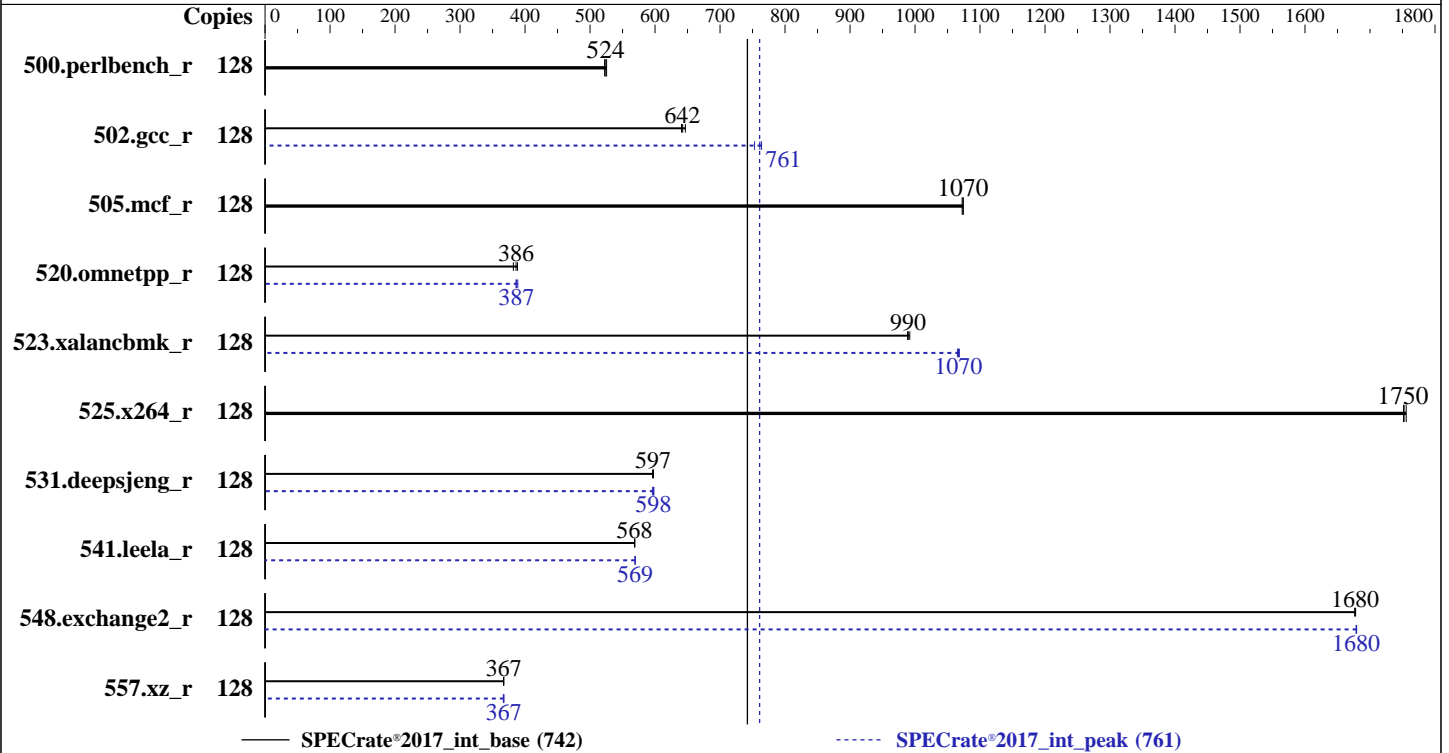
Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	128	390	522	**389**	**524**	388	525	128	390	522	**389**	**524**	388	525
502.gcc_r	128	280	647	283	641	**282**	**642**	128	241	753	237	764	**238**	**761**
505.mcf_r	128	193	1070	**193**	**1070**	193	1070	128	193	1070	**193**	**1070**	193	1070
520.omnetpp_r	128	432	388	440	382	**435**	**386**	128	435	386	432	389	**434**	**387**
523.xalancbmk_r	128	**137**	**990**	137	989	136	992	128	127	1070	127	1070	**127**	**1070**
525.x264_r	128	128	1760	**128**	**1750**	128	1750	128	128	1760	**128**	**1750**	128	1750
531.deepsjeng_r	128	246	596	**246**	**597**	246	597	128	245	598	**245**	**598**	246	596
541.leela_r	128	**373**	**568**	373	569	373	568	128	**372**	**569**	372	569	372	569
548.exchange2_r	128	200	1680	200	1680	**200**	**1680**	128	200	1680	200	1680	**200**	**1680**
557.xz_r	128	376	367	377	367	**376**	**367**	128	**376**	**367**	377	367	376	367
SPECrate®2017_int_base =		**742**												
SPECrate®2017_int_peak =		**761**												

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
http://developer.amd.com/amd-aocc/

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

(Continued on next page)

Lenovo Global Technology

ThinkSystem SR665 V3
(3.25 GHz,AMD EPYC 9354)

SPECrate®2017_int_base = 742

SPECrate®2017_int_peak = 761

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Jan-2023

Hardware Availability: Feb-2023

Software Availability: Nov-2022

Operating System Notes (Continued)

```
To enable Transparent Hugepages (THP) only on request for base runs,
'echo madvise > /sys/kernel/mm/transparent_hugepage/enabled' run as root.
To enable THP for all allocations for peak runs,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH =
      "/home/cpu2017-1.1.8-amd-aocc400-genoa-B1b/amd_rate_aocc400_genoa_B_lib/
      lib:/home/cpu2017-1.1.8-amd-aocc400-genoa-B1b/amd_rate_aocc400_genoa_B_l
      ib/lib32:"
MALLOC_CONF = "retain:true"

Environment variables set by runcpu during the 523.xalancbmk_r peak run:
MALLOC_CONF = "thp:never"
```

General Notes

```
Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
```

Platform Notes

```
BIOS configuration:
Operating Mode set to Maximum Performance and then set it to Custom Mode
NUMA Nodes per Socket set to NPS4

 Sysinfo program /home/cpu2017-1.1.8-amd-aocc400-genoa-B1b/bin/sysinfo
 Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d
 running on localhost Sun Jan  1 09:18:11 2023

 SUT (System Under Test) info as seen by some common utilities.
 For more information on this section, see
```

(Continued on next page)

Lenovo Global Technology

SPECrate®2017_int_base = 742

ThinkSystem SR665 V3
(3.25 GHz,AMD EPYC 9354)

SPECrate®2017_int_peak = 761

CPU2017 License:	9017	**Test Date:**	Jan-2023
Test Sponsor:	Lenovo Global Technology	**Hardware Availability:**	Feb-2023
Tested by:	Lenovo Global Technology	**Software Availability:**	Nov-2022

Platform Notes (Continued)

```
      https://www.spec.org/cpu2017/Docs/config.html#sysinfo

  From /proc/cpuinfo
     model name : AMD EPYC 9354 32-Core Processor
        2  "physical id"s (chips)
        128 "processors"
     cores, siblings (Caution: counting these is hw and system dependent. The following
     excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
        cpu cores : 32
        siblings  : 64
        physical 0: cores 0 1 2 3 10 11 16 17 18 19 24 25 26 27 32 33 34 35 40 41 42 43 48
        49 50 51 56 57 58 59
        physical 1: cores 0 1 2 3 10 11 16 17 18 19 24 25 26 27 32 33 34 35 40 41 42 43 48
        49 50 51 56 57 58 59

  From lscpu from util-linux 2.37.2:
        Architecture:              x86_64
        CPU op-mode(s):            32-bit, 64-bit
        Address sizes:             52 bits physical, 57 bits virtual
        Byte Order:                Little Endian
        CPU(s):                    128
        On-line CPU(s) list:       0-127
        Vendor ID:                 AuthenticAMD
        Model name:                AMD EPYC 9354 32-Core Processor
        CPU family:                25
        Model:                     17
        Thread(s) per core:        2
        Core(s) per socket:        32
        Socket(s):                 2
        Stepping:                  1
        Frequency boost:           enabled
        CPU max MHz:               3799.0720
        CPU min MHz:               1500.0000
        BogoMIPS:                  6490.10
        Flags:                     fpu vme de pse tsc msr pae mce cx8 apic sep mtrr
        pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt
        pdpe1gb rdtscp lm constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid
        aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe
        popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a
        misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb
        bpext perfctr_llc mwaitx cpb cat_l3 cdp_l3 invpcid_single hw_pstate ssbd mba ibrs
        ibpb stibp vmmcall fsgsbase bmi1 avx2 smep bmi2 erms invpcid cqm rdt_a avx512f
        avx512dq rdseed adx smap avx512ifma clflushopt clwb avx512cd sha_ni avx512bw
        avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
        cqm_mbm_local avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd amd_ppin arat npt
        lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter
        pfthreshold avic v_vmsave_vmload vgif v_spec_ctrl avx512vbmi umip pku ospke
```

(Continued on next page)

Lenovo Global Technology

SPECrate®2017_int_base = 742

ThinkSystem SR665 V3
(3.25 GHz,AMD EPYC 9354)

SPECrate®2017_int_peak = 761

CPU2017 License: 9017   **Test Date:** Jan-2023
Test Sponsor: Lenovo Global Technology   **Hardware Availability:** Feb-2023
Tested by: Lenovo Global Technology   **Software Availability:** Nov-2022

Platform Notes (Continued)

```
      avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg avx512_vpopcntdq la57
      rdpid overflow_recov succor smca fsrm flush_l1d
      Virtualization:               AMD-V
      L1d cache:                    2 MiB (64 instances)
      L1i cache:                    2 MiB (64 instances)
      L2 cache:                     64 MiB (64 instances)
      L3 cache:                     512 MiB (16 instances)
      NUMA node(s):                 8
      NUMA node0 CPU(s):            0-7,64-71
      NUMA node1 CPU(s):            8-15,72-79
      NUMA node2 CPU(s):            16-23,80-87
      NUMA node3 CPU(s):            24-31,88-95
      NUMA node4 CPU(s):            32-39,96-103
      NUMA node5 CPU(s):            40-47,104-111
      NUMA node6 CPU(s):            48-55,112-119
      NUMA node7 CPU(s):            56-63,120-127
      Vulnerability Itlb multihit:  Not affected
      Vulnerability L1tf:           Not affected
      Vulnerability Mds:            Not affected
      Vulnerability Meltdown:       Not affected
      Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via
      prctl and seccomp
      Vulnerability Spectre v1:        Mitigation; usercopy/swapgs barriers and __user
      pointer sanitization
      Vulnerability Spectre v2:        Mitigation; Retpolines, IBPB conditional, IBRS_FW,
      STIBP always-on, RSB filling
      Vulnerability Srbds:          Not affected
      Vulnerability Tsx async abort:   Not affected

  From lscpu --cache:
      NAME ONE-SIZE ALL-SIZE WAYS TYPE          LEVEL   SETS PHY-LINE COHERENCY-SIZE
      L1d        32K       2M    8 Data              1     64        1             64
      L1i        32K       2M    8 Instruction       1     64        1             64
      L2          1M      64M    8 Unified           2   2048        1             64
      L3         32M     512M   16 Unified           3  32768        1             64

  /proc/cpuinfo cache data
     cache size : 1024 KB

  From numactl --hardware
  WARNING: a numactl 'node' might or might not correspond to a physical chip.
     available: 8 nodes (0-7)
     node 0 cpus: 0 1 2 3 4 5 6 7 64 65 66 67 68 69 70 71
     node 0 size: 96379 MB
     node 0 free: 95452 MB
     node 1 cpus: 8 9 10 11 12 13 14 15 72 73 74 75 76 77 78 79
     node 1 size: 96752 MB
```

(Continued on next page)

Lenovo Global Technology

SPECrate®2017_int_base = 742

ThinkSystem SR665 V3
(3.25 GHz,AMD EPYC 9354)

SPECrate®2017_int_peak = 761

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jan-2023
Hardware Availability: Feb-2023
Software Availability: Nov-2022

Platform Notes (Continued)

```
   node 1 free: 95666 MB
   node 2 cpus: 16 17 18 19 20 21 22 23 80 81 82 83 84 85 86 87
   node 2 size: 96752 MB
   node 2 free: 96210 MB
   node 3 cpus: 24 25 26 27 28 29 30 31 88 89 90 91 92 93 94 95
   node 3 size: 96717 MB
   node 3 free: 96199 MB
   node 4 cpus: 32 33 34 35 36 37 38 39 96 97 98 99 100 101 102 103
   node 4 size: 96752 MB
   node 4 free: 96225 MB
   node 5 cpus: 40 41 42 43 44 45 46 47 104 105 106 107 108 109 110 111
   node 5 size: 96752 MB
   node 5 free: 96193 MB
   node 6 cpus: 48 49 50 51 52 53 54 55 112 113 114 115 116 117 118 119
   node 6 size: 96752 MB
   node 6 free: 96231 MB
   node 7 cpus: 56 57 58 59 60 61 62 63 120 121 122 123 124 125 126 127
   node 7 size: 96553 MB
   node 7 free: 96029 MB
   node distances:
   node   0   1   2   3   4   5   6   7
     0:  10  12  12  12  32  32  32  32
     1:  12  10  12  12  32  32  32  32
     2:  12  12  10  12  32  32  32  32
     3:  12  12  12  10  32  32  32  32
     4:  32  32  32  32  10  12  12  12
     5:  32  32  32  32  12  10  12  12
     6:  32  32  32  32  12  12  10  12
     7:  32  32  32  32  12  12  12  10

 From /proc/meminfo
    MemTotal:       791972504 kB
    HugePages_Total:        0
    Hugepagesize:        2048 kB

 /sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has
    performance

 From /etc/*release* /etc/*version*
    os-release:
       NAME="SLES"
       VERSION="15-SP4"
       VERSION_ID="15.4"
       PRETTY_NAME="SUSE Linux Enterprise Server 15 SP4"
       ID="sles"
       ID_LIKE="suse"
       ANSI_COLOR="0;32"
```

(Continued on next page)

Lenovo Global Technology

ThinkSystem SR665 V3
(3.25 GHz,AMD EPYC 9354)

SPECrate®2017_int_base = 742

SPECrate®2017_int_peak = 761

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jan-2023
Hardware Availability: Feb-2023
Software Availability: Nov-2022

Platform Notes (Continued)

```
      CPE_NAME="cpe:/o:suse:sles:15:sp4"

uname -a:
   Linux localhost 5.14.21-150400.22-default #1 SMP PREEMPT_DYNAMIC Wed May 11 06:57:18
   UTC 2022 (49db222) x86_64 x86_64 x86_64 GNU/Linux


Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):                  Not affected
CVE-2018-3620 (L1 Terminal Fault):               Not affected
Microarchitectural Data Sampling:                Not affected
CVE-2017-5754 (Meltdown):                         Not affected
CVE-2018-3639 (Speculative Store Bypass):        Mitigation: Speculative Store
                                                 Bypass disabled via prctl and
                                                 seccomp
CVE-2017-5753 (Spectre variant 1):               Mitigation: usercopy/swapgs
                                                 barriers and __user pointer
                                                 sanitization
CVE-2017-5715 (Spectre variant 2):               Mitigation: Retpolines, IBPB:
                                                 conditional, IBRS_FW, STIBP:
                                                 always-on, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort):         Not affected


run-level 3 Jan 1 04:32

SPEC is set to: /home/cpu2017-1.1.8-amd-aocc400-genoa-B1b
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda2      xfs   446G   31G  415G   7% /

From /sys/devices/virtual/dmi/id
    Vendor:          Lenovo
    Product:         ThinkSystem SR665 V3 MB,Genoa,Kauai,DDR5,Kauai,2U
    Product Family: ThinkSystem
    Serial:          1234567890

Additional information from dmidecode 3.2 follows.  WARNING: Use caution when you
interpret this section. The 'dmidecode' program reads system data which is "intended to
allow hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
   Memory:
     5x SK Hynix HMCG88AEBRA115N 32 GB 2 rank 4800
     19x SK Hynix HMCG88AEBRA168N 32 GB 2 rank 4800

BIOS:
    BIOS Vendor:      Lenovo
    BIOS Version:     KAE105F-1.20
```

(Continued on next page)

Lenovo Global Technology

SPECrate®2017_int_base = 742

ThinkSystem SR665 V3
(3.25 GHz,AMD EPYC 9354)

SPECrate®2017_int_peak = 761

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jan-2023
Hardware Availability: Feb-2023
Software Availability: Nov-2022

Platform Notes (Continued)

```
    BIOS Date:        12/01/2022
    BIOS Revision:    1.20
    Firmware Revision: 1.20

  (End of data from sysinfo program)
```

Compiler Version Notes

```
==============================================================================
C        | 502.gcc_r(peak)
------------------------------------------------------------------------------
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on
  LLVM Mirror.Version.14.0.6)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
------------------------------------------------------------------------------


==============================================================================
C        | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak)
         | 525.x264_r(base, peak) 557.xz_r(base, peak)
------------------------------------------------------------------------------
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on
  LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
------------------------------------------------------------------------------


==============================================================================
C        | 502.gcc_r(peak)
------------------------------------------------------------------------------
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on
  LLVM Mirror.Version.14.0.6)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
------------------------------------------------------------------------------


==============================================================================
C        | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak)
         | 525.x264_r(base, peak) 557.xz_r(base, peak)
------------------------------------------------------------------------------
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on
  LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
```

(Continued on next page)

Lenovo Global Technology

SPECrate®2017_int_base = 742

ThinkSystem SR665 V3
(3.25 GHz,AMD EPYC 9354)

SPECrate®2017_int_peak = 761

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jan-2023
Hardware Availability: Feb-2023
Software Availability: Nov-2022

Compiler Version Notes (Continued)

```
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
------------------------------------------------------------------------


==============================================================================
C++      | 523.xalancbmk_r(peak)
------------------------------------------------------------------------
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on
  LLVM Mirror.Version.14.0.6)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
------------------------------------------------------------------------


==============================================================================
C++      | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base)
         | 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)
------------------------------------------------------------------------
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on
  LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
------------------------------------------------------------------------


==============================================================================
C++      | 523.xalancbmk_r(peak)
------------------------------------------------------------------------
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on
  LLVM Mirror.Version.14.0.6)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
------------------------------------------------------------------------


==============================================================================
C++      | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base)
         | 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)
------------------------------------------------------------------------
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on
  LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
------------------------------------------------------------------------


==============================================================================
```

(Continued on next page)

Lenovo Global Technology

ThinkSystem SR665 V3
(3.25 GHz,AMD EPYC 9354)

SPECrate®2017_int_base = 742

SPECrate®2017_int_peak = 761

CPU2017 License:	9017	**Test Date:**	Jan-2023
Test Sponsor:	Lenovo Global Technology	**Hardware Availability:**	Feb-2023
Tested by:	Lenovo Global Technology	**Software Availability:**	Nov-2022

Compiler Version Notes (Continued)

```
Fortran | 548.exchange2_r(base, peak)
------------------------------------------------------------------------
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on
  LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
```
clang
```

C++ benchmarks:
```
clang++
```

Fortran benchmarks:
```
flang
```

Base Portability Flags

500.perlbench_r: `-DSPEC_LINUX_X64 -DSPEC_LP64`
502.gcc_r: `-DSPEC_LP64`
505.mcf_r: `-DSPEC_LP64`
520.omnetpp_r: `-DSPEC_LP64`
523.xalancbmk_r: `-DSPEC_LINUX -DSPEC_LP64`
525.x264_r: `-DSPEC_LP64`
531.deepsjeng_r: `-DSPEC_LP64`
541.leela_r: `-DSPEC_LP64`
548.exchange2_r: `-DSPEC_LP64`
557.xz_r: `-DSPEC_LP64`

Base Optimization Flags

C benchmarks:
```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather
-z muldefs -O3 -march=znver4 -fveclib=AMDLIBM -ffast-math
-fstruct-layout=7 -mllvm -unroll-threshold=50
```

(Continued on next page)

Lenovo Global Technology

ThinkSystem SR665 V3
(3.25 GHz,AMD EPYC 9354)

SPECrate®2017_int_base = 742

SPECrate®2017_int_peak = 761

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Jan-2023

Hardware Availability: Feb-2023

Software Availability: Nov-2022

Base Optimization Flags (Continued)

C benchmarks (continued):
```
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lflang
-lamdalloc
```

C++ benchmarks:
```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -z muldefs -O3
-march=znver4 -fveclib=AMDLIBM -ffast-math
-mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt
-fvirtual-function-elimination -fvisibility=hidden -lamdlibm -lflang
-lamdalloc-ext
```

Fortran benchmarks:
```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop
-Wl,-mllvm -Wl,-enable-iv-split -z muldefs -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fepilog-vectorization-of-inductions
-mllvm -optimize-strided-mem-cost -floop-transform
-mllvm -unroll-aggressive -mllvm -unroll-threshold=500 -lamdlibm
-lflang -lamdalloc
```

Base Other Flags

C benchmarks:
```
-Wno-unused-command-line-argument
```

C++ benchmarks:
```
-Wno-unused-command-line-argument
```

Fortran benchmarks:
```
-Wno-unused-command-line-argument
```

Peak Compiler Invocation

C benchmarks:
```
clang
```

(Continued on next page)

Lenovo Global Technology

ThinkSystem SR665 V3
(3.25 GHz,AMD EPYC 9354)

SPECrate®2017_int_base = 742

SPECrate®2017_int_peak = 761

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jan-2023
Hardware Availability: Feb-2023
Software Availability: Nov-2022

Peak Compiler Invocation (Continued)

C++ benchmarks:
```
clang++
```

Fortran benchmarks:
```
flang
```

Peak Portability Flags

500.perlbench_r: `-DSPEC_LINUX_X64 -DSPEC_LP64`
502.gcc_r: `-D_FILE_OFFSET_BITS=64`
505.mcf_r: `-DSPEC_LP64`
520.omnetpp_r: `-DSPEC_LP64`
523.xalancbmk_r: `-DSPEC_LINUX -DSPEC_LP64`
525.x264_r: `-DSPEC_LP64`
531.deepsjeng_r: `-DSPEC_LP64`
541.leela_r: `-DSPEC_LP64`
548.exchange2_r: `-DSPEC_LP64`
557.xz_r: `-DSPEC_LP64`

Peak Optimization Flags

C benchmarks:

500.perlbench_r: `basepeak = yes`

502.gcc_r: `-m32 -flto -z muldefs -Ofast -march=znver4`
`-fveclib=AMDLIBM -ffast-math -fstruct-layout=7`
`-mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining`
`-mllvm -inline-threshold=1000`
`-mllvm -reduce-array-computations=3 -zopt -fgnu89-inline`
`-lamdalloc`

505.mcf_r: `basepeak = yes`

525.x264_r: `basepeak = yes`

557.xz_r: `-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6`
`-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast`
`-march=znver4 -fveclib=AMDLIBM -ffast-math`
`-fstruct-layout=7 -mllvm -unroll-threshold=50`
`-fremap-arrays -fstrip-mining`
`-mllvm -inline-threshold=1000`

(Continued on next page)

Lenovo Global Technology

ThinkSystem SR665 V3
(3.25 GHz,AMD EPYC 9354)

SPECrate®2017_int_base = 742

SPECrate®2017_int_peak = 761

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jan-2023
Hardware Availability: Feb-2023
Software Availability: Nov-2022

Peak Optimization Flags (Continued)

557.xz_r (continued):
```
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lflang -lamdalloc
```

C++ benchmarks:

520.omnetpp_r: `-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6`
```
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math
-finline-aggressive -mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt
-fvirtual-function-elimination -fvisibility=hidden
-lamdlibm -lamdalloc-ext
```

523.xalancbmk_r: `-m32 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6`
```
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=aggressive
-fno-loop-reroll -Ofast -march=znver4 -fveclib=AMDLIBM
-ffast-math -finline-aggressive
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt
-mllvm -do-block-reorder=aggressive
-fvirtual-function-elimination -fvisibility=hidden
-lamdalloc-ext
```

531.deepsjeng_r: `-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6`
```
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3
-march=znver4 -fveclib=AMDLIBM -ffast-math
-mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt
-fvirtual-function-elimination -fvisibility=hidden
-lamdlibm -lamdalloc-ext
```

541.leela_r: `-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6`
```
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math
-finline-aggressive -mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt
-fvirtual-function-elimination -fvisibility=hidden
-lamdlibm -lflang -lamdalloc-ext
```

Fortran benchmarks:
```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop
```

(Continued on next page)

Lenovo Global Technology

ThinkSystem SR665 V3
(3.25 GHz,AMD EPYC 9354)

SPECrate®2017_int_base = 742

SPECrate®2017_int_peak = 761

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jan-2023
Hardware Availability: Feb-2023
Software Availability: Nov-2022

Peak Optimization Flags (Continued)

Fortran benchmarks (continued):
```
-Wl,-mllvm -Wl,-enable-iv-split -O3 -march=znver4 -fveclib=AMDLIBM
-ffast-math -fepilog-vectorization-of-inductions
-mllvm -optimize-strided-mem-cost -floop-transform
-mllvm -unroll-aggressive -mllvm -unroll-threshold=500 -lamdlibm
-lflang -lamdalloc
```

Peak Other Flags

C benchmarks (except as noted below):
```
-Wno-unused-command-line-argument
```

502.gcc_r: `-L/usr/lib32 -Wno-unused-command-line-argument`
`-L/home/work/cpu2017/v118/aocc4/b1/rate/amd_rate_aocc400_genoa_B_lib/lib32`

C++ benchmarks (except as noted below):
```
-Wno-unused-command-line-argument
```

523.xalancbmk_r: `-L/usr/lib32 -Wno-unused-command-line-argument`
`-L/home/work/cpu2017/v118/aocc4/b1/rate/amd_rate_aocc400_genoa_B_lib/lib32`

Fortran benchmarks:
```
-Wno-unused-command-line-argument
```

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Genoa-O.html
http://www.spec.org/cpu2017/flags/aocc400-flags.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Genoa-O.xml
http://www.spec.org/cpu2017/flags/aocc400-flags.xml

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.8 on 2022-12-31 20:18:10-0500.
Report generated on 2023-01-17 18:45:27 by CPU2017 PDF formatter v6442.
Originally published on 2023-01-17.