SPEC CPU®2017 Floating Point Speed Result
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xFusion

FusionServer 5885H V6 (Intel Xeon Gold 6348H)

$SPECspeed®2017\_fp\_base =$ 254

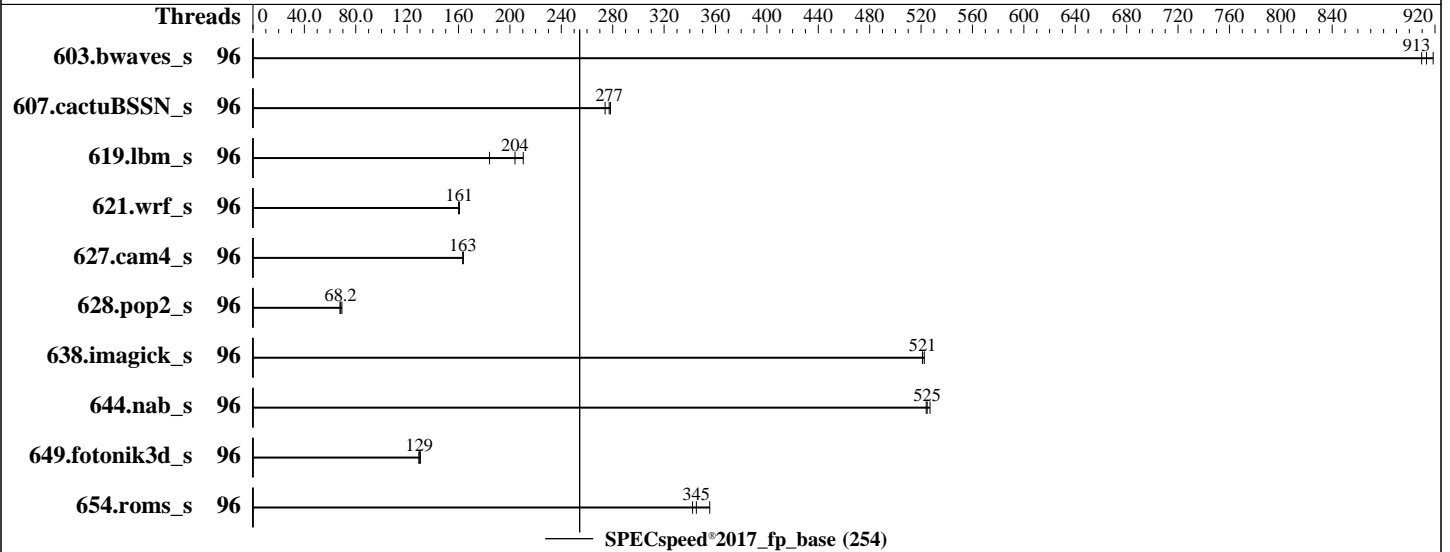$SPECspeed®2017\_fp\_peak =$ Not Run

CPU2017 License: 6488
Test Sponsor: xFusion
Tested by: xFusion

Test Date: Jan-2023
Hardware Availability: Jan-2023
Software Availability: May-2022

Threads | 0   40.0   80.0   120   160   200   240   280   320   360   400   440   480   520   560   600   640   680   720   760   800   840   920

- 603.bwaves_s 96 — 913
- 607.cactuBSSN_s 96 — 277
- 619.lbm_s 96 — 204
- 621.wrf_s 96 — 161
- 627.cam4_s 96 — 163
- 628.pop2_s 96 — 68.2
- 638.imagick_s 96 — 521
- 644.nab_s 96 — 525
- 649.fotonik3d_s 96 — 129
- 654.roms_s 96 — 345

SPECspeed®2017_fp_base (254)

Hardware

CPU Name: Intel Xeon Gold 6348H
  Max MHz: 4200
  Nominal: 2300
Enabled: 96 cores, 4 chips
Orderable: 1,2,4 chips
Cache L1: 32 KB I + 32 KB D on chip per core
  L2: 1 MB I+D on chip per core
  L3: 33 MB I+D on chip per chip
  Other: None
Memory: 1536 GB (48 x 32 GB 2Rx8 PC4-3200AA-R, running at 2933)
Storage: 1 x 960 GB SATA SSD
Other: None

Software

OS: Red Hat Enterprise Linux release 8.2 (Ootpa)
4.18.0-193.el8.x86_64
Compiler: C/C++: Version 2022.1 of Intel oneAPI DPC++/C++ Compiler for Linux;
Fortran: Version 2022.1 of Intel Fortran Compiler for Linux;
Parallel: Yes
Firmware: Version 1.05 Released Jan-2023
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other: jemalloc memory allocator V5.0.1
Power Management: OS set to prefer performance at the cost of additional power usage

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Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	96	64.9	910	64.2	919	**64.6**	**913**							
607.cactuBSSN_s	96	**60.1**	**277**	60.8	274	59.9	278							
619.lbm_s	96	**25.7**	**204**	28.4	184	24.9	210							
621.wrf_s	96	82.6	160	82.3	161	**82.3**	**161**							
627.cam4_s	96	54.2	163	**54.2**	**163**	54.1	164							
628.pop2_s	96	171	69.2	175	67.7	**174**	**68.2**							
638.imagick_s	96	**27.7**	**521**	27.6	523	27.7	521							
644.nab_s	96	33.3	524	33.2	527	**33.3**	**525**							
649.fotonik3d_s	96	**70.4**	**129**	70.6	129	69.9	130							
654.roms_s	96	44.3	356	46.0	342	**45.6**	**345**							
SPECspeed®2017_fp_base =		**254**												
SPECspeed®2017_fp_peak =		**Not Run**												

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH =
     "/home/Uniautos/cpu2017/lib/intel64:/home/Uniautos/cpu2017/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"
```

General Notes

```
Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>        /proc/sys/vm/drop_caches
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation
```

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Test Date: Jan-2023
Hardware Availability: Jan-2023
Software Availability: May-2022

General Notes (Continued)

```
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases
```

Platform Notes

```
BIOS configuration:
Performance Profile Set to Load Balance
Hyper-Threading Set to Disabled

Sysinfo program /home/Uniautos/cpu2017/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d
running on localhost.localdomain Thu Jan 12 00:43:15 2023

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
   https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) Gold 6348H CPU @ 2.30GHz
      4  "physical id"s (chips)
      96 "processors"
   cores, siblings (Caution: counting these is hw and system dependent. The following
   excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
      cpu cores : 24
      siblings  : 24
      physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29
      physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29
      physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29
      physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29

From lscpu from util-linux 2.32.1:
      Architecture:        x86_64
      CPU op-mode(s):      32-bit, 64-bit
      Byte Order:          Little Endian
      CPU(s):              96
      On-line CPU(s) list: 0-95
      Thread(s) per core:  1
      Core(s) per socket:  24
      Socket(s):           4
      NUMA node(s):        4
      Vendor ID:           GenuineIntel
      CPU family:          6
      Model:               85
      Model name:          Intel(R) Xeon(R) Gold 6348H CPU @ 2.30GHz
      Stepping:            11
      CPU MHz:             1000.098
```

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Test Date: Jan-2023
Hardware Availability: Jan-2023
Software Availability: May-2022

Platform Notes (Continued)

```
        CPU max MHz:           4200.0000
        CPU min MHz:           1000.0000
        BogoMIPS:              4600.00
        Virtualization:        VT-x
        L1d cache:             32K
        L1i cache:             32K
        L2 cache:              1024K
        L3 cache:              33792K
        NUMA node0 CPU(s):     0-23
        NUMA node1 CPU(s):     24-47
        NUMA node2 CPU(s):     48-71
        NUMA node3 CPU(s):     72-95
        Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
    pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
    lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
    aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
    xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
    avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
    invpcid_single ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority
    ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a
    avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl
    xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
    avx512_bf16 dtherm ida arat pln pts pku ospke avx512_vnni md_clear flush_l1d
    arch_capabilities

 /proc/cpuinfo cache data
   cache size : 33792 KB

 From numactl --hardware
 WARNING: a numactl 'node' might or might not correspond to a physical chip.
   available: 4 nodes (0-3)
   node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23
   node 0 size: 385164 MB
   node 0 free: 384712 MB
   node 1 cpus: 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47
   node 1 size: 387038 MB
   node 1 free: 386848 MB
   node 2 cpus: 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71
   node 2 size: 387066 MB
   node 2 free: 386879 MB
   node 3 cpus: 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95
   node 3 size: 387065 MB
   node 3 free: 382734 MB
   node distances:
   node   0   1   2   3
     0:  10  20  20  20
     1:  20  10  20  20
```

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xFusion

SPECspeed®2017_fp_base = 254

FusionServer 5885H V6 (Intel Xeon Gold 6348H)

SPECspeed®2017_fp_peak = Not Run

CPU2017 License: 6488
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Hardware Availability: Jan-2023
Software Availability: May-2022

Platform Notes (Continued)

```
      2:  20  20  10  20
      3:  20  20  20  10

 From /proc/meminfo
    MemTotal:       1583447864 kB
    HugePages_Total:        0
    Hugepagesize:        2048 kB

 /sbin/tuned-adm active
     Current active profile: balanced

 /sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has
    powersave

 From /etc/*release* /etc/*version*
    os-release:
       NAME="Red Hat Enterprise Linux"
       VERSION="8.2 (Ootpa)"
       ID="rhel"
       ID_LIKE="fedora"
       VERSION_ID="8.2"
       PLATFORM_ID="platform:el8"
       PRETTY_NAME="Red Hat Enterprise Linux 8.2 (Ootpa)"
       ANSI_COLOR="0;31"
    redhat-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
    system-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
    system-release-cpe: cpe:/o:redhat:enterprise_linux:8.2:ga

 uname -a:
    Linux localhost.localdomain 4.18.0-193.el8.x86_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020
    x86_64 x86_64 x86_64 GNU/Linux

 Kernel self-reported vulnerability status:

 CVE-2018-12207 (iTLB Multihit):                   Not affected
 CVE-2018-3620 (L1 Terminal Fault):                Not affected
 Microarchitectural Data Sampling:                 Not affected
 CVE-2017-5754 (Meltdown):                         Not affected
 CVE-2018-3639 (Speculative Store Bypass):         Mitigation: Speculative Store
                                                   Bypass disabled via prctl and
                                                   seccomp
 CVE-2017-5753 (Spectre variant 1):                Mitigation: usercopy/swapgs
                                                   barriers and __user pointer
                                                   sanitization
 CVE-2017-5715 (Spectre variant 2):                Mitigation: Enhanced IBRS, IBPB:
                                                   conditional, RSB filling
 CVE-2020-0543 (Special Register Buffer Data Sampling): No status reported
```

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Platform Notes (Continued)

```
 CVE-2019-11135 (TSX Asynchronous Abort):              Not affected

 run-level 3 Jan 11 22:48

 SPEC is set to: /home/Uniautos/cpu2017
    Filesystem             Type  Size  Used Avail Use% Mounted on
    /dev/mapper/rhel-home xfs   292G   27G  266G   9% /home

 From /sys/devices/virtual/dmi/id
    Vendor:         Huawei
    Product:        5885H V6
    Product Family: Cedar Island

 Additional information from dmidecode 3.2 follows.  WARNING: Use caution when you
 interpret this section. The 'dmidecode' program reads system data which is "intended to
 allow hardware to be accurately determined", but the intent may not be met, as there are
 frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
   Memory:
     8x Hynix HMAA4GR7AJR8N-XN 32 GB 2 rank 3200, configured at 2933
     5x Micron 18ASF4G72PDZ-3G2E1 32 GB 2 rank 3200, configured at 2933
     35x Samsung M393A4G43AB3-CWE 32 GB 2 rank 3200, configured at 2933

 BIOS:
    BIOS Vendor:       Byosoft Corporation
    BIOS Version:      1.05
    BIOS Date:         01/05/2023

 (End of data from sysinfo program)
```

Compiler Version Notes

```
==============================================================================
C                 | 619.lbm_s(base) 638.imagick_s(base) 644.nab_s(base)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------


==============================================================================
C++, C, Fortran | 607.cactuBSSN_s(base)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
```

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Test Date: Jan-2023
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Software Availability: May-2022

Compiler Version Notes (Continued)

```
  Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version
  2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
------------------------------------------------------------------------


==============================================================================
Fortran        | 603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)
------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version
  2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
------------------------------------------------------------------------


==============================================================================
Fortran, C     | 621.wrf_s(base) 627.cam4_s(base) 628.pop2_s(base)
------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version
  2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
`icx`

Fortran benchmarks:
`ifx`

Benchmarks using both Fortran and C:
`ifx icx`

Benchmarks using Fortran, C, and C++:
`icpx icx ifx`

Base Portability Flags

603.bwaves_s: `-DSPEC_LP64`

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Base Portability Flags (Continued)

607.cactuBSSN_s: `-DSPEC_LP64`
619.lbm_s: `-DSPEC_LP64`
621.wrf_s: `-DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian`
627.cam4_s: `-DSPEC_LP64 -DSPEC_CASE_FLAG`
628.pop2_s: `-DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian`
`-assume byterecl`
638.imagick_s: `-DSPEC_LP64`
644.nab_s: `-DSPEC_LP64`
649.fotonik3d_s: `-DSPEC_LP64`
654.roms_s: `-DSPEC_LP64`

Base Optimization Flags

C benchmarks:
`-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto`
`-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp`
`-DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

Fortran benchmarks:
`-m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512 -Ofast -ffast-math`
`-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp`
`-nostandard-realloc-lhs -align array32byte -auto`
`-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

Benchmarks using both Fortran and C:
`-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto`
`-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp`
`-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte -auto`
`-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

Benchmarks using Fortran, C, and C++:
`-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto`
`-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp`
`-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte -auto`
`-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2017/flags/Intel-ic2022-official-linux64_revA.html`
`http://www.spec.org/cpu2017/flags/xFusion-Platform-Settings-CPX-V1.4.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2017/flags/Intel-ic2022-official-linux64_revA.xml`
`http://www.spec.org/cpu2017/flags/xFusion-Platform-Settings-CPX-V1.4.xml`

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Test Sponsor: xFusion		**Hardware Availability:**	Jan-2023
Tested by: xFusion		**Software Availability:**	May-2022

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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