



# SPEC CPU®2017 Floating Point Rate Result

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## Tyrone Systems

(Test Sponsor: Netweb Pte Ltd)

Tyrone Camarero IDI100C2R-28  
(2.00 GHz, Intel Xeon Gold 6330)

**SPECrate®2017\_fp\_base = 406**

**SPECrate®2017\_fp\_peak = 423**

CPU2017 License: 006042

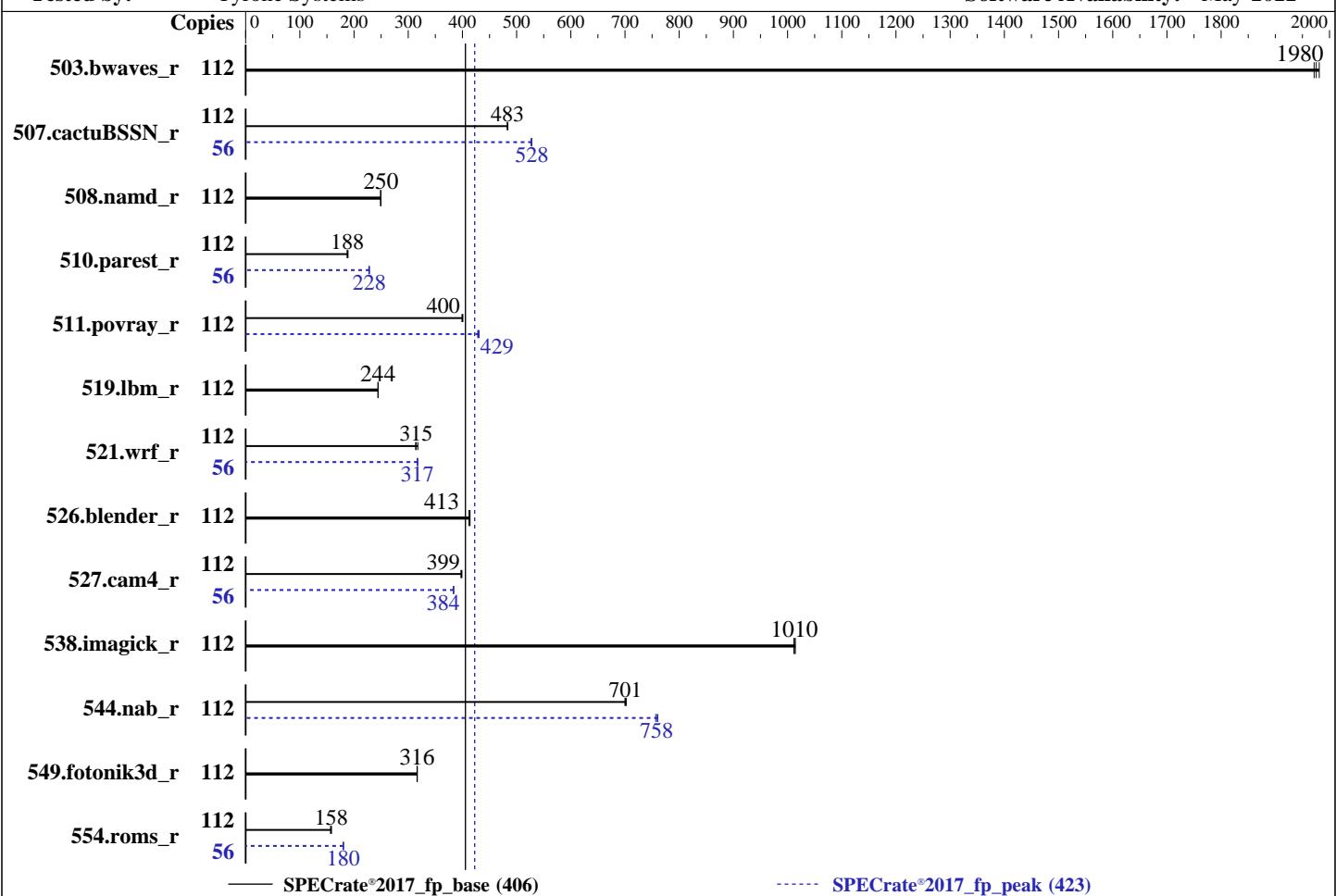
**Test Date:** Mar-2023

**Test Sponsor:** Netweb Pte Ltd

**Hardware Availability:** Apr-2021

**Tested by:** Tyrone Systems

**Software Availability:** May-2022



## Hardware

CPU Name: Intel Xeon Gold 6330  
Max MHz: 3100  
Nominal: 2000  
Enabled: 56 cores, 2 chips, 2 threads/core  
Orderable: 1,2 Chips  
Cache L1: 32 KB I + 48 KB D on chip per core  
L2: 1.25 MB I+D on chip per core  
L3: 42 MB I+D on chip per chip  
Other: None  
Memory: 1 TB (32 x 32 GB 2Rx4 PC4-3200AA-R, running at 2933)  
Storage: 1 x 512 GB NVMe SSD  
Other: None

## OS:

Red Hat Enterprise Linux release 8.5 (Ootpa)  
Kernel 4.18.0-348.el8.x86\_64

## Compiler:

C/C++: Version 2022.1 of Intel oneAPI DPC++/C++ Compiler for Linux;  
Fortran: Version 2022.1 of Intel Fortran Compiler for Linux;

## Parallel:

No  
Version SE5C620.86B.01.01.0004.2110190142 released Oct-2021

## Firmware:

xfs  
Run level 3 (multi-user)

## File System:

System State:  
Base Pointers:  
Peak Pointers:  
Other:  
Power Management:

64-bit  
64-bit  
jemalloc memory allocator V5.0.1  
BIOS set to prefer performance at the cost of additional power usage.

## Software



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	112	567	1980	<b>569</b>	<b>1980</b>	570	1970	112	567	1980	<b>569</b>	<b>1980</b>	570	1970
507.cactusBSSN_r	112	<b>293</b>	<b>483</b>	294	483	293	484	56	134	528	135	526	<b>134</b>	<b>528</b>
508.namd_r	112	426	250	428	249	<b>426</b>	<b>250</b>	112	426	250	428	249	<b>426</b>	<b>250</b>
510.parest_r	112	<b>1560</b>	<b>188</b>	1551	189	1564	187	56	<b>642</b>	<b>228</b>	645	227	641	228
511.povray_r	112	<b>654</b>	<b>400</b>	655	399	652	401	112	607	431	<b>609</b>	<b>429</b>	611	428
519.lbm_r	112	483	244	<b>483</b>	<b>244</b>	483	244	112	483	244	<b>483</b>	<b>244</b>	483	244
521.wrf_r	112	<b>797</b>	<b>315</b>	790	318	800	314	56	396	317	395	317	<b>396</b>	<b>317</b>
526.blender_r	112	412	414	<b>413</b>	<b>413</b>	414	412	112	412	414	<b>413</b>	<b>413</b>	414	412
527.cam4_r	112	491	399	<b>491</b>	<b>399</b>	493	397	56	<b>255</b>	<b>384</b>	255	384	256	383
538.imagick_r	112	275	1010	<b>275</b>	<b>1010</b>	275	1010	112	275	1010	<b>275</b>	<b>1010</b>	275	1010
544.nab_r	112	268	702	<b>269</b>	<b>701</b>	269	700	112	<b>249</b>	<b>758</b>	249	757	248	761
549.fotonik3d_r	112	<b>1379</b>	<b>316</b>	1380	316	1378	317	112	<b>1379</b>	<b>316</b>	1380	316	1378	317
554.roms_r	112	1125	158	<b>1128</b>	<b>158</b>	1136	157	56	494	180	<b>493</b>	<b>180</b>	493	180

**SPECrate®2017\_fp\_base = 406**

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
**LD\_LIBRARY\_PATH** = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"  
**MALLOC\_CONF** = "retain:true"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

(Continued on next page)



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## General Notes (Continued)

```
sync; echo 1> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
```

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

```
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases
```

## Platform Notes

BIOS Settings:

ENERGY\_PERF\_BIAS\_CFG mode = Maximum Performance

KTI Prefetch = Enable

LLC Dead Line Alloc = Disable

```
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on icelake2 Tue Mar 21 20:52:00 2023
```

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 239 (239-51.el8)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent\_hugepage

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## Platform Notes (Continued)

18. /sys/kernel/mm/transparent\_hugepage/khugepaged  
19. OS release  
20. Kernel self-reported vulnerability status, from /sys/devices/system/cpu/vulnerabilities  
21. Disk information  
22. /sys/devices/virtual/dmi/id  
23. dmidecode  
24. BIOS

---

---

1. uname -a  
Linux icelake2 4.18.0-348.el8.x86\_64 #1 SMP Mon Oct 4 12:17:22 EDT 2021 x86\_64 x86\_64 x86\_64 GNU/Linux

---

2. w  
20:52:00 up 8:20, 2 users, load average: 67.01, 100.60, 106.87  
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT  
root tty1 - 12:31 8:19m 1.36s 0.02s -bash  
root tty2 - 12:32 8:18m 0.03s 0.03s -bash

---

3. Username  
From environment variable \$USER: root

---

4. ulimit -a  
core file size (blocks, -c) 0  
data seg size (kbytes, -d) unlimited  
scheduling priority (-e) 0  
file size (blocks, -f) unlimited  
pending signals (-i) 4126770  
max locked memory (kbytes, -l) 64  
max memory size (kbytes, -m) unlimited  
open files (-n) 1024  
pipe size (512 bytes, -p) 8  
POSIX message queues (bytes, -q) 819200  
real-time priority (-r) 0  
stack size (kbytes, -s) unlimited  
cpu time (seconds, -t) unlimited  
max user processes (-u) 4126770  
virtual memory (kbytes, -v) unlimited  
file locks (-x) unlimited

---

5. sysinfo process ancestry  
/usr/lib/systemd/systemd --switched-root --system --deserialize 18  
login -- root

(Continued on next page)



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## Platform Notes (Continued)

```
-bash
-runcpu --nobuild --action validate --define default-platform-flags --define numcopies=112 -c
  ic2022.1-lin-core-avx512-rate-20220316.cfg --define smt-on --define cores=56 --define physicalfirst
  --define invoke_with_interleave --define drop_caches --tune base,peak -o all fprate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=112 --configfile
  ic2022.1-lin-core-avx512-rate-20220316.cfg --define smt-on --define cores=56 --define physicalfirst
  --define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --nopower
  --runmode rate --tune base:peak --size refrate fprate --nopreenv --note-preenv --logfile
  $SPEC/tmp/CPU2017.002/templogs/preenv.fprate.002.0.log --lognum 002.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

---

### 6. /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) Gold 6330 CPU @ 2.00GHz
vendor_id       : GenuineIntel
cpu family     : 6
model          : 106
stepping        : 6
microcode       : 0xd000311
bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs
cpu cores      : 28
siblings        : 56
2 physical ids (chips)
112 processors (hardware threads)
physical id 0: core ids 0-27
physical id 1: core ids 0-27
physical id 0: apicids 0-55
physical id 1: apicids 128-183
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

---

### 7. lscpu

From lscpu from util-linux 2.32.1:

```
Architecture:      x86_64
CPU op-mode(s):    32-bit, 64-bit
Byte Order:        Little Endian
CPU(s):           112
On-line CPU(s) list: 0-111
Thread(s) per core: 2
Core(s) per socket: 28
Socket(s):         2
NUMA node(s):      4
Vendor ID:         GenuineIntel
```

(Continued on next page)



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## Platform Notes (Continued)

BIOS Vendor ID:	Intel(R) Corporation
CPU family:	6
Model:	106
Model name:	Intel(R) Xeon(R) Gold 6330 CPU @ 2.00GHz
BIOS Model name:	Intel(R) Xeon(R) Gold 6330 CPU @ 2.00GHz
Stepping:	6
CPU MHz:	2000.000
CPU max MHz:	3100.0000
CPU min MHz:	800.0000
BogoMIPS:	4000.00
Virtualization:	VT-x
L1d cache:	48K
L1i cache:	32K
L2 cache:	1280K
L3 cache:	43008K
NUMA node0 CPU(s):	0-13,56-69
NUMA node1 CPU(s):	14-27,70-83
NUMA node2 CPU(s):	28-41,84-97
NUMA node3 CPU(s):	42-55,98-111
Flags:	fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmpfperf pnipclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single ssbd mba ibrs ibpb stibp ibrs_enhanced fsbsbase tsc_adjust sgx bmil hle avx2 smep bmi2 erms invpcid cqmmrdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local split_lock_detect wbnoinvd dtherm ida arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid sgx_lc fsrm md_clear pconfig flush_lld arch_capabilities

---

### 8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 4 nodes (0-3)

node 0 cpus: 0-13,56-69

node 0 size: 257666 MB

node 0 free: 243051 MB

node 1 cpus: 14-27,70-83

node 1 size: 258041 MB

node 1 free: 246510 MB

node 2 cpus: 28-41,84-97

node 2 size: 258041 MB

node 2 free: 246546 MB

node 3 cpus: 42-55,98-111

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## Platform Notes (Continued)

```
node 3 size: 258001 MB
node 3 free: 246501 MB
node distances:
node  0   1   2   3
 0: 10  11  20  20
 1: 11  10  20  20
 2: 20  20  10  11
 3: 20  20  11  10
```

```
-----9. /proc/meminfo
      MemTotal:       1056514228 kB
```

```
-----10. who -r
      run-level 3 Mar 21 12:31
```

```
-----11. Systemd service manager version: systemd 239 (239-51.el8)
      Default Target  Status
      multi-user     running
```

```
-----12. Services, from systemctl list-unit-files
      STATE      UNIT FILES
      enabled    ModemManager NetworkManager NetworkManager-dispatcher NetworkManager-wait-online accounts-daemon
                  atd auditd autovt@ avahi-daemon bluetooth chronyd crond cups display-manager firewalld gdm
                  getty@ import-state insights-client-boot irqbalance iscsi iscsi-onboot kdump ksm ksmtuned
                  libstoragemgmt libvirtd loadmodules lvm2-monitor mcelog mdmonitor microcode multipathd
                  nis-domainname nvmefc-boot-connections ostree-remount qemu-guest-agent rhsmcertd rpcbind rsyslog
                  rtkit-daemon selinux-autorelabel-mark smartd sshd sssd syslog timedatectl tuned udisks2 vdo
                  vgauthd vmtoolsd
      disabled   arp-ethers blk-availability brlty canberra-system-bootup canberra-system-shutdown
                  canberra-system-shutdown-reboot chrony-wait console-getty cpupower cups-browsed debug-shell
                  dnsmasq ebttables gssproxy httpd httpd@ initial-setup initial-setup-reconfiguration iprdump
                  iprinit iprupdate ipsec iscsid iscsiuiio kpatch kvm_stat ledmon libvirt-guests
                  man-db-restart-cache-update ndctl-monitor netcf-transaction nfs-blkmap nfs-convert nfs-server
                  nftables numad nvmf-autoconnect oddjobd podman podman-auto-update podman-restart psacct radvd
                  ras-mc-ctl rasdaemon rdisc rhcd rhsm rhsm-facts saslauthd serial-getty@ speech-dispatcherd
                  sshd-keygen@ switcheroo-control systemd-nspawn@ systemd-resolved tcsd upower virtinterfaced
                  virtnetworkd virtnodedeved virtnwfilterd virtproxyd virtqemud virtsecretd virtstoraged
                  wpa_supplicant
      generated  SystemTap compile-server gcc-toolset-10-stap-server gcc-toolset-10-systemtap
                  gcc-toolset-11-stap-server gcc-toolset-11-systemtap gcc-toolset-9-stap-server
                  gcc-toolset-9-systemtap scripts startup
      indirect   spice-vdagentd sssd-automofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo virtlockd
                  virtlogd
```

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## Platform Notes (Continued)

masked systemd-timedated

-----  
13. Linux kernel boot-time arguments, from /proc/cmdline  
BOOT\_IMAGE=(hd0,gpt2)/vmlinuz-4.18.0-348.el8.x86\_64  
root=/dev/mapper/rhel-root  
ro  
resume=/dev/mapper/rhel-swap  
rd.lvm.lv=rhel/root  
rd.lvm.lv=rhel/swap  
rhgb  
quiet

-----  
14. cpupower frequency-info  
analyzing CPU 0:  
current policy: frequency should be within 800 MHz and 3.10 GHz.  
The governor "performance" may decide which speed to use  
within this range.  
boost state support:  
Supported: yes  
Active: yes

-----  
15. tuned-adm active  
Current active profile: throughput-performance

-----  
16. sysctl  

kernel.numa_balancing	1
kernel.randomize_va_space	2
vm.compaction_proactiveness	0
vm.dirty_background_bytes	0
vm.dirty_background_ratio	10
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	40
vm.dirty_writeback_centisecs	500
vm.dirtytime_expire_seconds	43200
vm.extfrag_threshold	500
vm.min_unmapped_ratio	1
vm.nr_hugepages	0
vm.nr_hugepages_mempolicy	0
vm.nr_overcommit_hugepages	0
vm.swappiness	10
vm.watermark_boost_factor	15000
vm.watermark_scale_factor	10

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## Platform Notes (Continued)

vm.zone\_reclaim\_mode 0

-----  
17. /sys/kernel/mm/transparent\_hugepage  
defrag always defer defer+madvise [madvise] never  
enabled [always] madvise never  
hpage\_pmd\_size 2097152  
shmem\_enabled always within\_size advise [never] deny force

-----  
18. /sys/kernel/mm/transparent\_hugepage/khugepaged  
alloc\_sleep\_millisecs 60000  
defrag 1  
max\_ptes\_none 511  
max\_ptes\_swap 64  
pages\_to\_scan 4096  
scan\_sleep\_millisecs 10000

-----  
19. OS release  
From /etc/\*-release /etc/\*-version  
os-release Red Hat Enterprise Linux 8.5 (Ootpa)  
redhat-release Red Hat Enterprise Linux release 8.5 (Ootpa)  
system-release Red Hat Enterprise Linux release 8.5 (Ootpa)

-----  
20. Kernel self-reported vulnerability status, from /sys/devices/system/cpu/vulnerabilities  
itlb\_multihit Not affected  
l1tf Not affected  
mds Not affected  
meltdown Not affected  
spec\_store\_bypass Mitigation: Speculative Store Bypass disabled via prctl and seccomp  
spectre\_v1 Mitigation: usercopy/swapgs barriers and \_\_user pointer sanitization  
spectre\_v2 Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling  
srbds Not affected  
tsx\_async\_abort Not affected  
For more information, see the Linux documentation on hardware vulnerabilities, for example  
<https://www.kernel.org/doc/html/latest/admin-guide/hw-vuln/index.html>

-----  
21. Disk information

SPEC is set to: /home/cpu2017

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/mapper/rhel-home	xfs	402G	286G	116G	72%	/home

-----  
22. /sys/devices/virtual/dmi/id

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## Platform Notes (Continued)

Vendor: Tyrone\_Systems  
Product: Tyrone\_Camarero\_IDI100C2R-28  
Product Family: Family  
Serial: 2X22462203

### 23. dmidecode

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

#### Memory:

6x Samsung M393A4K40DB3-CWE 32 GB 2 rank 3200, configured at 2933  
26x Samsung M393A4K40EB3-CWE 32 GB 2 rank 3200, configured at 2933

### 24. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Intel Corporation  
BIOS Version: SE5C620.86B.01.01.0004.2110190142  
BIOS Date: 10/19/2021

## Compiler Version Notes

=====

C | 519.lbm\_r(base, peak) 538.imagick\_r(base, peak)  
| 544.nab\_r(base, peak)

=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2022.1.0 Build 20220316  
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=====

=====

C++ | 508.namd\_r(base, peak) 510.parest\_r(base, peak)

=====

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=====

=====

C++, C | 511.povray\_r(base, peak) 526.blender\_r(base, peak)

=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,

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# SPEC CPU®2017 Floating Point Rate Result

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Tyrone Systems

(Test Sponsor: Netweb Pte Ltd)

Tyrone Camarero IDI100C2R-28  
(2.00 GHz, Intel Xeon Gold 6330)

**SPECrate®2017\_fp\_base = 406**

**SPECrate®2017\_fp\_peak = 423**

CPU2017 License: 006042

Test Date: Mar-2023

Test Sponsor: Netweb Pte Ltd

Hardware Availability: Apr-2021

Tested by: Tyrone Systems

Software Availability: May-2022

## Compiler Version Notes (Continued)

Version 2022.1.0 Build 20220316

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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,

Version 2022.1.0 Build 20220316

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=====

C++, C, Fortran | 507.cactuBSSN\_r(base, peak)

=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,

Version 2022.1.0 Build 20220316

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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,

Version 2022.1.0 Build 20220316

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Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version

2022.1.0 Build 20220316

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=====

Fortran | 503.bwaves\_r(base, peak) 549.fotonik3d\_r(base, peak)  
| 554.roms\_r(base, peak)

=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version

2022.1.0 Build 20220316

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=====

Fortran, C | 521.wrf\_r(base, peak) 527.cam4\_r(base, peak)

=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version  
2022.1.0 Build 20220316

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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,

Version 2022.1.0 Build 20220316

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## Base Compiler Invocation

C benchmarks:

icx

(Continued on next page)



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Software Availability: May-2022

## Base Compiler Invocation (Continued)

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

## Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64  
507.cactuBSSN\_r: -DSPEC\_LP64  
508.namd\_r: -DSPEC\_LP64  
510.parest\_r: -DSPEC\_LP64  
511.povray\_r: -DSPEC\_LP64  
519.lbm\_r: -DSPEC\_LP64  
521.wrf\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian  
526.blender\_r: -DSPEC\_LP64 -DSPEC\_LINUX -funsigned-char  
527.cam4\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG  
538.imagick\_r: -DSPEC\_LP64  
544.nab\_r: -DSPEC\_LP64  
549.fotonik3d\_r: -DSPEC\_LP64  
554.roms\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib

C++ benchmarks:

-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib

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CPU2017 License: 006042

Test Date: Mar-2023

Test Sponsor: Netweb Pte Ltd

Hardware Availability: Apr-2021

Tested by: Tyrone Systems

Software Availability: May-2022

## Base Optimization Flags (Continued)

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -fno-math-errno  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math  
-fno-math-errno -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both C and C++:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math  
-fno-math-errno -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math  
-fno-math-errno -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

## Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx



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## Tyrone Systems

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Tyrone Camarero IDI100C2R-28  
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**SPECrate®2017\_fp\_base = 406**

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CPU2017 License: 006042

Test Date: Mar-2023

Test Sponsor: Netweb Pte Ltd

Hardware Availability: Apr-2021

Tested by: Tyrone Systems

Software Availability: May-2022

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

519.lbm\_r: basepeak = yes

538.imagick\_r: basepeak = yes

544.nab\_r: -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -qopt-zmm-usage=high -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib

C++ benchmarks:

508.namd\_r: basepeak = yes

510.parest\_r: -w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:

503.bwaves\_r: basepeak = yes

549.fotonik3d\_r: basepeak = yes

554.roms\_r: -w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs  
-align array32byte -auto -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both Fortran and C:

-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both C and C++:

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Test Sponsor: Netweb Pte Ltd

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Test Date: Mar-2023

Hardware Availability: Apr-2021

Software Availability: May-2022

## Peak Optimization Flags (Continued)

```
511.povray_r: -w -m64 -std=c11 -Wl,-z,muldefs  
-fprofile-generate(pass 1)  
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512  
-Ofast -ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

```
526.blender_r: basepeak = yes
```

Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

The flags files that were used to format this result can be browsed at

[http://www.spec.org/cpu2017/flags/Intel-ic2022-official-linux64\\_revA.html](http://www.spec.org/cpu2017/flags/Intel-ic2022-official-linux64_revA.html)  
<http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-ICX-revA.html>

You can also download the XML flags sources by saving the following links:

[http://www.spec.org/cpu2017/flags/Intel-ic2022-official-linux64\\_revA.xml](http://www.spec.org/cpu2017/flags/Intel-ic2022-official-linux64_revA.xml)  
<http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-ICX-revA.xml>

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