



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

## Dell Inc.

### SPECrate®2017\_fp\_base = 901

## PowerEdge R960 (Intel Xeon Gold 6416H)

### SPECrate®2017\_fp\_peak = 919

CPU2017 License: 6573

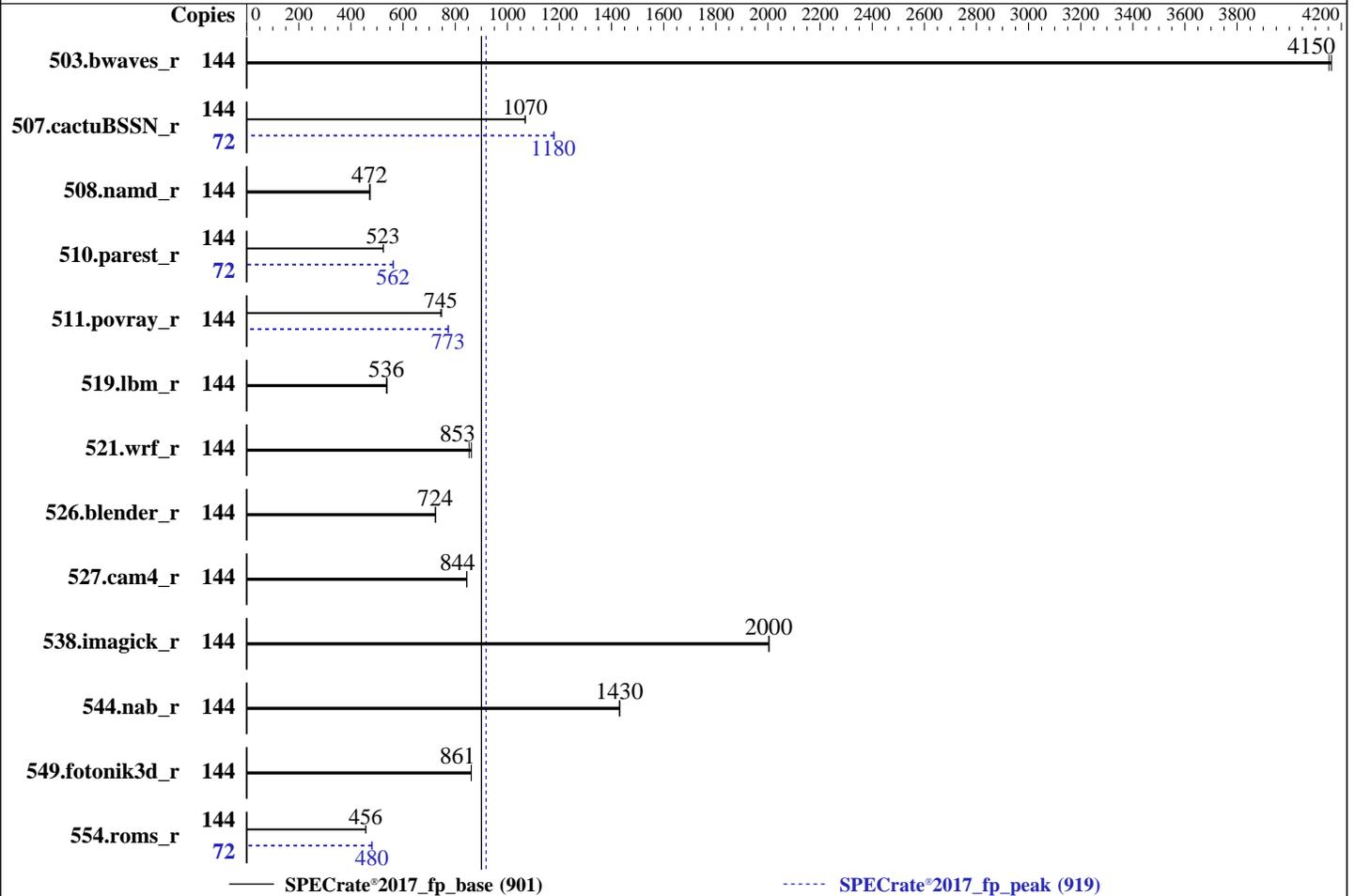
Test Date: Apr-2023

Test Sponsor: Dell Inc.

Hardware Availability: May-2023

Tested by: Dell Inc.

Software Availability: Dec-2022



### Hardware

CPU Name: Intel Xeon Gold 6416H  
 Max MHz: 4200  
 Nominal: 2200  
 Enabled: 72 cores, 4 chips, 2 threads/core  
 Orderable: 2,4 chips  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 2 MB I+D on chip per core  
 L3: 45 MB I+D on chip per chip  
 Other: None  
 Memory: 2 TB (32 x 64 GB 2Rx4 PC5-4800B-R)  
 Storage: 90 GB on tmpfs  
 Other: None

### Software

OS: Red Hat Enterprise Linux 9.0 (Plow)  
 5.14.0-70.13.1.el9\_0.x86\_64  
 Compiler: C/C++: Version 2023.0 of Intel oneAPI DPC++/C++ Compiler for Linux;  
 Fortran: Version 2023.0 of Intel Fortran Compiler for Linux;  
 Parallel: No  
 Firmware: Version 1.4.0 released Mar-2023  
 File System: tmpfs  
 System State: Run level 5 (graphical multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	144	<b>348</b>	<b>4150</b>	347	4160			144	<b>348</b>	<b>4150</b>	347	4160		
507.cactuBSSN_r	144	171	1070	<b>171</b>	<b>1070</b>			72	77.3	1180	<b>77.5</b>	<b>1180</b>		
508.namd_r	144	289	473	<b>290</b>	<b>472</b>			144	289	473	<b>290</b>	<b>472</b>		
510.parest_r	144	<b>720</b>	<b>523</b>	718	524			72	<b>335</b>	<b>562</b>	335	562		
511.povray_r	144	449	748	<b>452</b>	<b>745</b>			144	<b>435</b>	<b>773</b>	434	774		
519.lbm_r	144	<b>283</b>	<b>536</b>	282	538			144	<b>283</b>	<b>536</b>	282	538		
521.wrf_r	144	374	862	<b>378</b>	<b>853</b>			144	374	862	<b>378</b>	<b>853</b>		
526.blender_r	144	303	724	<b>303</b>	<b>724</b>			144	303	724	<b>303</b>	<b>724</b>		
527.cam4_r	144	298	845	<b>298</b>	<b>844</b>			144	298	845	<b>298</b>	<b>844</b>		
538.imagick_r	144	<b>179</b>	<b>2000</b>	179	2000			144	<b>179</b>	<b>2000</b>	179	2000		
544.nab_r	144	<b>169</b>	<b>1430</b>	169	1430			144	<b>169</b>	<b>1430</b>	169	1430		
549.fotonik3d_r	144	651	862	<b>652</b>	<b>861</b>			144	651	862	<b>652</b>	<b>861</b>		
554.roms_r	144	<b>501</b>	<b>456</b>	500	457			72	<b>238</b>	<b>480</b>	238	481		

SPECrate®2017\_fp\_base = **901**

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =  
    "/mnt/ramdisk/cpu2017-1.1.9-ic2023.0/lib/intel64:/mnt/ramdisk/cpu2017-1.1.9-ic2023.0/je5.0.1-64"  
MALLOC_CONF = "retain:true"
```

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM  
memory using Red Hat Enterprise Linux 8.4  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>  
jemalloc, a general purpose malloc implementation

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built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 90 GB ramdisk created with the cmd: "mount -t tmpfs -o size=90G tmpfs /mnt/ramdisk"

## Platform Notes

BIOS settings:

```

    ADDDC Setting : Disabled
    DIMM Self Healing on
    Uncorrectable Memory Error : Disabled
    DCU Streamer Prefetcher : Disabled
    Sub NUMA Cluster : 2-way Clustering
    LLC Prefetch : Disabled
    Dead Line LLC Alloc : Disabled
    Optimizer Mode : Enabled

    System Profile : Custom
    CPU Power Management : Maximum Performance
    CIE : Disabled
    C States : Autonomous
    Memory Patrol Scrub : Disabled
    Energy Efficiency Policy : Performance
    PCI ASPM L1 Link
    Power Management : Disabled

```

Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-ic2023.0/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost.localdomain Sat Apr 1 03:32:22 2023

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 250 (250-6.e19\_0)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent\_hugepage

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## Platform Notes (Continued)

```

17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

```

```

1. uname -a
Linux localhost.localdomain 5.14.0-70.13.1.el9_0.x86_64 #1 SMP PREEMPT Thu Apr 14 12:42:38 EDT 2022 x86_64
x86_64 x86_64 GNU/Linux

```

```

2. w
 03:32:22 up 4:07, 1 user, load average: 90.79, 130.76, 138.71
USER      TTY      LOGIN@  IDLE   JCPU   PCPU   WHAT
root      :l       23:25  ?xdm?  8:21   0.00s /usr/libexec/gdm-x-session --register-session --run-script
gnome-session

```

```

3. Username
From environment variable $USER: root

```

```

4. ulimit -a
real-time non-blocking time (microseconds, -R) unlimited
core file size (blocks, -c) 0
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 8254745
max locked memory (kbytes, -l) 64
max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 8254745
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited

```

```

5. sysinfo process ancestry
/usr/lib/systemd/systemd rhgb --switched-root --system --deserialize 31
/usr/lib/systemd/systemd --user
/usr/libexec/gnome-terminal-server
bash
/bin/bash ./DELL_rate.sh
/bin/bash ./dell-run-main.sh rate
/bin/bash ./dell-run-main.sh rate
/bin/bash ./dell-run-speccpu.sh rate --define DL-BIOSinc=Dell-BIOS_Xeon-4.inc --define DL-BIOS-adddcD=1
--define DL-BIOS-SNC=2 --output_format csv,html,pdf,txt
/bin/bash ./dell-run-speccpu.sh rate --define DL-BIOSinc=Dell-BIOS_Xeon-4.inc --define DL-BIOS-adddcD=1
--define DL-BIOS-SNC=2 --output_format csv,html,pdf,txt
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=144 -c
ic2023.0-lin-sapphirerapids-rate-20221201.cfg --define smt-on --define cores=72 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak -o all --iterations 2 --define

```

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## Platform Notes (Continued)

```
DL-BIOSinc=Dell-BIOS_Xeon-4.inc --define DL-BIOS-adddcD=1 --define DL-BIOS-SNC=2 --output_format
csv,html,pdf,txt fprate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=144 --configfile
ic2023.0-lin-sapphirerapids-rate-20221201.cfg --define smt-on --define cores=72 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --iterations 2
--define DL-BIOSinc=Dell-BIOS_Xeon-4.inc --define DL-BIOS-adddcD=1 --define DL-BIOS-SNC=2 --output_format
csv,html,pdf,txt --nopower --runmode rate --tune base:peak --size refrate fprate --nopreenv --note-preenv
--logfile $SPEC/tmp/CPU2017.002/temlogs/preenv.fprate.002.0.log --lognum 002.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-ic2023.0
```

```
6. /proc/cpuinfo
model name      : Intel(R) Xeon(R) Gold 6416H
vendor_id      : GenuineIntel
cpu family     : 6
model          : 143
stepping       : 7
microcode      : 0x2b0001b0
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs
cpu cores      : 18
siblings       : 36
4 physical ids (chips)
144 processors (hardware threads)
physical id 0: core ids 0-17
physical id 1: core ids 0-17
physical id 2: core ids 0-17
physical id 3: core ids 0-17
physical id 0: apicids 0-35
physical id 1: apicids 128-163
physical id 2: apicids 256-291
physical id 3: apicids 384-419
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

### 7. lscpu

From lscpu from util-linux 2.37.4:

```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Address sizes: 46 bits physical, 57 bits virtual
Byte Order: Little Endian
CPU(s): 144
On-line CPU(s) list: 0-143
Vendor ID: GenuineIntel
BIOS Vendor ID: Intel
Model name: Intel(R) Xeon(R) Gold 6416H
BIOS Model name: Intel(R) Xeon(R) Gold 6416H
CPU family: 6
Model: 143
Thread(s) per core: 2
Core(s) per socket: 18
Socket(s): 4
Stepping: 7
BogoMIPS: 4400.00
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
```

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### Platform Notes (Continued)

```
ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtrp pdcm pcid dca sse4_1
sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3
invpcid_single cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow
vmni flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 avx2 smep bmi2
erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma
clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec
xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts
avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq
avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid bus_lock_detect
cldemote movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk pconfig
arch_lbr avx512_fp16 amx_tile flush_lld arch_capabilities
```

#### Virtualization:

```
VT-x
L1d cache: 3.4 MiB (72 instances)
L1i cache: 2.3 MiB (72 instances)
L2 cache: 144 MiB (72 instances)
L3 cache: 180 MiB (4 instances)
```

#### NUMA node(s):

```
8
NUMA node0 CPU(s): 0,8,16,24,32,40,48,56,64,72,80,88,96,104,112,120,128,136
NUMA node1 CPU(s): 4,12,20,28,36,44,52,60,68,76,84,92,100,108,116,124,132,140
NUMA node2 CPU(s): 1,9,17,25,33,41,49,57,65,73,81,89,97,105,113,121,129,137
NUMA node3 CPU(s): 5,13,21,29,37,45,53,61,69,77,85,93,101,109,117,125,133,141
NUMA node4 CPU(s): 2,10,18,26,34,42,50,58,66,74,82,90,98,106,114,122,130,138
NUMA node5 CPU(s): 6,14,22,30,38,46,54,62,70,78,86,94,102,110,118,126,134,142
NUMA node6 CPU(s): 3,11,19,27,35,43,51,59,67,75,83,91,99,107,115,123,131,139
NUMA node7 CPU(s): 7,15,23,31,39,47,55,63,71,79,87,95,103,111,119,127,135,143
```

#### Vulnerability Itlb multihit:

```
Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected
```

#### From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	3.4M	12	Data	1	64	1	64
L1i	32K	2.3M	8	Instruction	1	64	1	64
L2	2M	144M	16	Unified	2	2048	1	64
L3	45M	180M	15	Unified	3	49152	1	64

#### 8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 8 nodes (0-7)
node 0 cpus: 0,8,16,24,32,40,48,56,64,72,80,88,96,104,112,120,128,136
node 0 size: 257496 MB
node 0 free: 256481 MB
node 1 cpus: 4,12,20,28,36,44,52,60,68,76,84,92,100,108,116,124,132,140
node 1 size: 258043 MB
node 1 free: 257181 MB
node 2 cpus: 1,9,17,25,33,41,49,57,65,73,81,89,97,105,113,121,129,137
node 2 size: 258043 MB
node 2 free: 257169 MB
node 3 cpus: 5,13,21,29,37,45,53,61,69,77,85,93,101,109,117,125,133,141
node 3 size: 258043 MB
node 3 free: 257174 MB
```

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```

node 4 cpus: 2,10,18,26,34,42,50,58,66,74,82,90,98,106,114,122,130,138
node 4 size: 258043 MB
node 4 free: 247601 MB
node 5 cpus: 6,14,22,30,38,46,54,62,70,78,86,94,102,110,118,126,134,142
node 5 size: 258043 MB
node 5 free: 256702 MB
node 6 cpus: 3,11,19,27,35,43,51,59,67,75,83,91,99,107,115,123,131,139
node 6 size: 258007 MB
node 6 free: 257150 MB
node 7 cpus: 7,15,23,31,39,47,55,63,71,79,87,95,103,111,119,127,135,143
node 7 size: 258024 MB
node 7 free: 257176 MB
node distances:
node  0  1  2  3  4  5  6  7
0:  10 12 21 21 21 21 21 21
1:  12 10 21 21 21 21 21 21
2:  21 21 10 12 21 21 21 21
3:  21 21 12 10 21 21 21 21
4:  21 21 21 21 10 12 21 21
5:  21 21 21 21 12 10 21 21
6:  21 21 21 21 21 21 10 12
7:  21 21 21 21 21 21 12 10

```

```

9. /proc/meminfo
MemTotal:      2113277832 kB

```

```

10. who -r
run-level 5 Mar 31 23:25

```

```

11. Systemd service manager version: systemd 250 (250-6.el9_0)
Default Target Status
graphical          running

```

```

12. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled ModemManager NetworkManager NetworkManager-dispatcher NetworkManager-wait-online
accounts-daemon atd auditd avahi-daemon bluetooth crond cups dbus-broker gdm getty@
insights-client-boot irqbalance iscsi iscsi-onboot kdump libstoragemgmt lm_sensors
low-memory-monitor lvm2-monitor mcelog mdmonitor microcode multipathd nis-domainname
nvme-fc-boot-connections ostree-remount pmcd pmie pmlonger power-profiles-daemon
qemu-guest-agent rshmcertd rpcbind rsyslog rtkit-daemon selinux-autorelabel-mark smartd
sshd sssd switcheroo-control sysstat systemd-network-generator udisks2 upower vgauthd
virtgemud vmttoolsd
enabled-runtime systemd-remount-fs
disabled arp-ethers autofs blk-availability brltty canberra-system-bootup canberra-system-shutdown
canberra-system-shutdown-reboot chrony-wait chronyd cni-dhcp console-getty cpupower
cups-browsed dbus-daemon debug-shell dnsmasq dovecot fancontrol fcoe firewalld
grafana-server gssproxy httpd httpd@ ibacm iprdump iprinit iprupdate ipsec iscsid iscsiui
kpatch kvm_stat ledmon libvirt-guests libvirt lldpad man-db-restart-cache-update named
named-chroot nfs-blkmap nfs-server nftables nmb numad nvme-autoconnect pmfind pmie_farm
pmlonger_farm pmproxy podman podman-auto-update podman-restart postfix powertop psacct
ras-mc-ctl rasdaemon rdisc rhcd rhsm rhsm-facts rpmdb-rebuild rrdcached saslauthd
serial-getty@ smb snmpd snmptrapd spamassassin speech-dispatcherd srp_daemon
srp_daemon_port@ sshd-keygen@ systemd-boot-check-no-failures systemd-nspawn@
systemd-pstore systemd-sysextd target targetclid tog-pegasus trace-cmd virtinterfaced
virtnetworkd virtnodevdev virtnwfilterd virtproxyd virtsecret d virtstaged vsftpd

```

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```

indirect          wpa_supplicant
                  pcsd spice-vdagentd sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo
                  virtlockd virtlogd vsftpd@

```

```

-----
13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=(hd0,gpt2)/vmlinuz-5.14.0-70.13.1.el9_0.x86_64
root=/dev/mapper/rhel-root
ro
resume=/dev/mapper/rhel-swap
rd.lvm.lv=rhel/root
rd.lvm.lv=rhel/swap
rhgb
quiet

```

```

-----
14. cpupower frequency-info
analyzing CPU 0:
  Unable to determine current policy
  boost state support:
    Supported: yes
    Active: yes

```

```

-----
15. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space      2
vm.compaction_proactiveness     20
vm.dirty_background_bytes       0
vm.dirty_background_ratio      10
vm.dirty_bytes                  0
vm.dirty_expire_centisecs      3000
vm.dirty_ratio                  20
vm.dirty_writeback_centisecs    500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold            500
vm.min_unmapped_ratio           1
vm.nr_hugepages                 0
vm.nr_hugepages_mempolicy       0
vm.nr_overcommit_hugepages     0
vm.swappiness                    60
vm.watermark_boost_factor      15000
vm.watermark_scale_factor       10
vm.zone_reclaim_mode            0

```

```

-----
16. /sys/kernel/mm/transparent_hugepage
defrag          always defer defer+madvice [madvice] never
enabled         [always] madvice never
hpage_pmd_size  2097152
shmem_enabled   always within_size advise [never] deny force

```

```

-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                  1
max_ptes_none           511
max_ptes_shared         256
max_ptes_swap           64
pages_to_scan           4096

```

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# SPEC CPU®2017 Floating Point Rate Result

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Dell Inc.

SPECrate®2017\_fp\_base = 901

PowerEdge R960 (Intel Xeon Gold 6416H)

SPECrate®2017\_fp\_peak = 919

CPU2017 License: 6573  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Apr-2023  
Hardware Availability: May-2023  
Software Availability: Dec-2022

## Platform Notes (Continued)

scan\_sleep\_millisecs 10000

### 18. OS release

From /etc/\*-release /etc/\*-version  
os-release Red Hat Enterprise Linux 9.0 (Plow)  
redhat-release Red Hat Enterprise Linux release 9.0 (Plow)  
system-release Red Hat Enterprise Linux release 9.0 (Plow)

### 19. Disk information

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-ic2023.0  
Filesystem Type Size Used Avail Use% Mounted on  
tmpfs tmpfs 90G 4.2G 86G 5% /mnt/ramdisk

### 20. /sys/devices/virtual/dmi/id

Vendor: Dell Inc.  
Product: PowerEdge R960  
Product Family: PowerEdge

### 21. dmidecode

Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:  
2x 00AD00B300AD HMC94MEBRA121N 64 GB 2 rank 4800  
30x 00AD063200AD HMC94MEBRA109N 64 GB 2 rank 4800

### 22. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Dell Inc.  
BIOS Version: 1.4.0  
BIOS Date: 03/15/2023  
BIOS Revision: 1.4

## Compiler Version Notes

C | 519.lbm\_r(base, peak) 538.imagick\_r(base, peak) 544.nab\_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

C++ | 508.namd\_r(base, peak) 510.parest\_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
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C++, C | 511.povray\_r(base, peak) 526.blender\_r(base, peak)

(Continued on next page)



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## Compiler Version Notes (Continued)

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=====  
C++, C, Fortran | 507.cactuBSSN\_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
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=====  
Fortran | 503.bwaves\_r(base, peak) 549.fotonik3d\_r(base, peak) 554.roms\_r(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
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=====  
Fortran, C | 521.wrf\_r(base, peak) 527.cam4\_r(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
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## Base Compiler Invocation

C benchmarks:  
icx

C++ benchmarks:  
icpx

Fortran benchmarks:  
ifx

Benchmarks using both Fortran and C:  
ifx icx

Benchmarks using both C and C++:  
icpx icx

Benchmarks using Fortran, C, and C++:  
icpx icx ifx



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**Test Date:** Apr-2023  
**Hardware Availability:** May-2023  
**Software Availability:** Dec-2022

## Base Portability Flags

```
503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64
```

## Base Optimization Flags

### C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

### C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

### Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

### Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

### Benchmarks using both C and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
```

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Tested by: Dell Inc.

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Hardware Availability: May-2023

Software Availability: Dec-2022

## Base Optimization Flags (Continued)

Benchmarks using both C and C++ (continued):

```
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsaphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

## Peak Compiler Invocation

C benchmarks:

```
icx
```

C++ benchmarks:

```
icpx
```

Fortran benchmarks:

```
ifx
```

Benchmarks using both Fortran and C:

```
ifx icx
```

Benchmarks using both C and C++:

```
icpx icx
```

Benchmarks using Fortran, C, and C++:

```
icpx icx ifx
```

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
519.lbm_r: basepeak = yes
```

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Software Availability: Dec-2022

## Peak Optimization Flags (Continued)

538.imagick\_r: basepeak = yes

544.nab\_r: basepeak = yes

C++ benchmarks:

508.namd\_r: basepeak = yes

510.parest\_r: -w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids  
-Ofast -ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -mprefer-vector-width=512  
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:

503.bwaves\_r: basepeak = yes

549.fotonik3d\_r: basepeak = yes

554.roms\_r: -w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs  
-align array32byte -auto -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both Fortran and C:

521.wrf\_r: basepeak = yes

527.cam4\_r: basepeak = yes

Benchmarks using both C and C++:

511.povray\_r: -w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs  
-fprofile-generate(pass 1)  
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)  
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse  
-funroll-loops -qopt-mem-layout-trans=4 -Wno-implicit-int  
-mprefer-vector-width=512 -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib

526.blender\_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops

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## Peak Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):

```
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.3.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.3.xml>

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