



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380p Gen8  
(2.50 GHz, Intel Xeon E5-2670 v2)

**SPECrate®2017\_fp\_base = 83.5**

**SPECrate®2017\_fp\_peak = Not Run**

CPU2017 License: 3

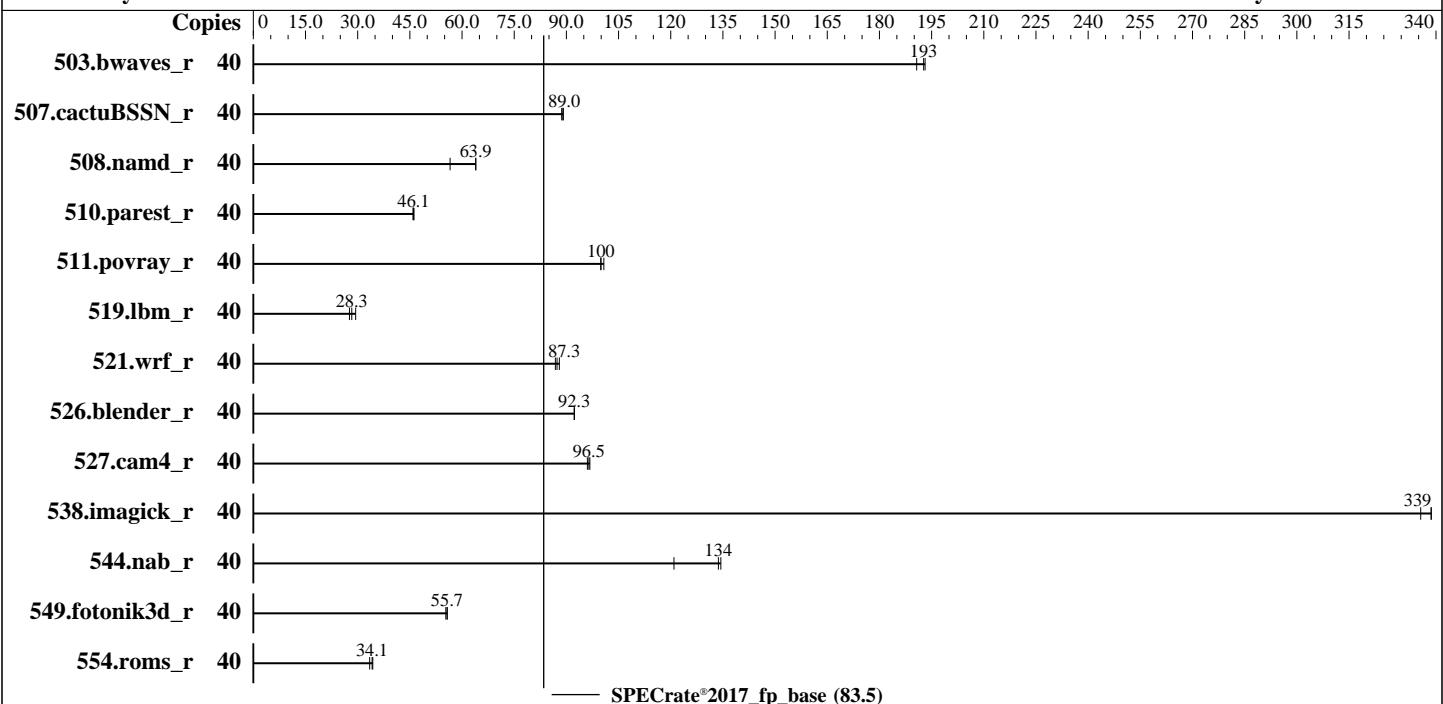
Test Sponsor: HPE

Tested by: HPE

**Test Date:** May-2023

**Hardware Availability:** May-2019

**Software Availability:** Dec-2022



## Hardware

CPU Name: Intel Xeon E5-2670 v2  
Max MHz: 3300  
Nominal: 2500  
Enabled: 20 cores, 2 chips, 2 threads/core  
Orderable: 1, 2 chip(s)  
Cache L1: 32 KB I + 32 KB D on chip per core  
L2: 256 KB I+D on chip per core  
L3: 25 MB I+D on chip per chip  
Other: None  
Memory: 128 GB (8 x 16 GB 2Rx4 PC3-14900R-13, ECC)  
Storage: 5 x 600 GB SAS 10K HDD, RAID 5  
Other: None

## OS:

Red Hat Enterprise Linux 9.0 (Plow)

Kernel 5.14.0-70.13.1.el9\_0.x86\_64

C/C++: Version 2023.0 of Intel oneAPI DPC++/C++ Compiler for Linux;

Fortran: Version 2023.0 of Intel Fortran Compiler for Linux;

No

HPE BIOS Version P70 05/24/2019 released May-2019

xfs

System State: Run level 3 (multi-user)

64-bit

Peak Pointers: Not Applicable

Other: jemalloc memory allocator V5.0.1

Power Management: BIOS set to prefer performance at the cost of additional power usage

## Software



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380p Gen8

(2.50 GHz, Intel Xeon E5-2670 v2)

SPECrate®2017\_fp\_base = 83.5

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: May-2023

Hardware Availability: May-2019

Software Availability: Dec-2022

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	40	<b><u>2082</u></b>	<b><u>193</u></b>	2077	193	2103	191							
507.cactubSSN_r	40	571	88.6	<b><u>569</u></b>	<b><u>89.0</u></b>	568	89.1							
508.namd_r	40	<b><u>594</u></b>	<b><u>63.9</u></b>	594	64.0	672	56.6							
510.parest_r	40	2280	45.9	2266	46.2	<b><u>2271</u></b>	<b><u>46.1</u></b>							
511.povray_r	40	<b><u>934</u></b>	<b><u>100</u></b>	927	101	935	99.9							
519.lbm_r	40	1433	29.4	<b><u>1490</u></b>	<b><u>28.3</u></b>	1525	27.6							
521.wrf_r	40	1018	88.0	1032	86.8	<b><u>1026</u></b>	<b><u>87.3</u></b>							
526.blender_r	40	660	92.3	660	92.3	<b><u>660</u></b>	<b><u>92.3</u></b>							
527.cam4_r	40	<b><u>725</u></b>	<b><u>96.5</u></b>	729	96.0	723	96.7							
538.imagick_r	40	294	339	<b><u>294</u></b>	<b><u>339</u></b>	296	336							
544.nab_r	40	557	121	<b><u>504</u></b>	<b><u>134</u></b>	501	134							
549.fotonik3d_r	40	<b><u>2799</u></b>	<b><u>55.7</u></b>	2796	55.7	2817	55.3							
554.roms_r	40	<b><u>1861</u></b>	<b><u>34.1</u></b>	1900	33.4	1851	34.3							

SPECrate®2017\_fp\_base = 83.5

SPECrate®2017\_fp\_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor.

For details, please see the config file.

This benchmark result is intended to provide perspective on past performance using the historical hardware and/or software described on this result page.

The system as described on this result page was formerly generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the SPEC OSG Policy document, <http://www.spec.org/osg/policy.html>. This measured result may not be representative of the result that would be measured were this benchmark run with hardware and software available as of the publication date.

This benchmark run is conducted using the latest binaries based on IC23 and to suffice the minimum software requirement, the Operating System used is RHEL9.0

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
```



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

## Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380p Gen8  
(2.50 GHz, Intel Xeon E5-2670 v2)

SPECrate®2017\_fp\_base = 83.5

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: May-2023

Hardware Availability: May-2019

Software Availability: Dec-2022

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

LD\_LIBRARY\_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"

MALLOC\_CONF = "retain:true"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8480+ CPU + 512GB RAM memory using Red Hat Enterprise Linux 9.0

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5 sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

## Platform Notes

The system ROM used for this result contains Intel microcode version 0x42e for the Intel Xeon E5-2670 v2 processor.

BIOS Configuration:

HP Power Profile set to Custom

Energy/Performance Bias set to Maximum Performance

Thermal Configuration set to Maximum Cooling

Collaborative Power Control set to Disabled

Processor Power and Utilization Monitoring set to Disabled

Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost.localdomain Sat May 6 03:09:51 2023

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 250 (250-6.el9\_0)
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. cpupower frequency-info
16. sysctl
17. /sys/kernel/mm/transparent\_hugepage

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380p Gen8  
(2.50 GHz, Intel Xeon E5-2670 v2)

SPECrate®2017\_fp\_base = 83.5

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: May-2023

Hardware Availability: May-2019

Software Availability: Dec-2022

## Platform Notes (Continued)

18. /sys/kernel/mm/transparent\_hugepage/khugepaged

19. OS release

20. Disk information

21. /sys/devices/virtual/dmi/id

22. dmidecode

23. BIOS

1. uname -a  
Linux localhost.localdomain 5.14.0-70.13.1.el9\_0.x86\_64 #1 SMP PREEMPT Thu Apr 14 12:42:38 EDT 2022 x86\_64 x86\_64 GNU/Linux

2. w  
03:09:51 up 1 day, 2:17, 2 users, load average: 0.00, 0.00, 0.00  
USER TTY LOGIN@ IDLE JCPU PCPU WHAT  
root ttym1 06Apr22 394days 0.02s 0.02s -bash  
root pts/0 06Apr22 15.00s 2.44s 0.01s -bash

3. Username  
From environment variable \$USER: root

4. ulimit -a  
real-time non-blocking time (microseconds, -R) unlimited  
core file size (blocks, -c) 0  
data seg size (kbytes, -d) unlimited  
scheduling priority (-e) 0  
file size (blocks, -f) unlimited  
pending signals (-i) 513255  
max locked memory (kbytes, -l) 64  
max memory size (kbytes, -m) unlimited  
open files (-n) 1024  
pipe size (512 bytes, -p) 8  
POSIX message queues (bytes, -q) 819200  
real-time priority (-r) 0  
stack size (kbytes, -s) unlimited  
cpu time (seconds, -t) unlimited  
max user processes (-u) 513255  
virtual memory (kbytes, -v) unlimited  
file locks (-x) unlimited

5. sysinfo process ancestry  
/usr/lib/systemd/systemd rhgb --system --deserialize 70  
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups  
sshd: root [priv]  
sshd: root@pts/0  
-bash  
-bash  
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=40 -c  
HPE-ic2023.0-lin-core-avx-rate-20221201.cfg --define smt-on --define cores=20 --define physicalfirst  
--define invoke\_with\_interleave --define drop\_caches --tune base -o all fprate  
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=40 --configfile  
HPE-ic2023.0-lin-core-avx-rate-20221201.cfg --define smt-on --define cores=20 --define physicalfirst  
--define invoke\_with\_interleave --define drop\_caches --tune base --output\_format all --nopower --runmode  
rate --tune base --size reframe fprate --nopreenv --note-preenv --logfile  
\$SPEC/tmp/CPU2017.005/templogs/preenv.fprate.005.0.log --lognum 005.0 --from\_runcpu 2

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380p Gen8  
(2.50 GHz, Intel Xeon E5-2670 v2)

SPECrate®2017\_fp\_base = 83.5

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: May-2023

Hardware Availability: May-2019

Software Availability: Dec-2022

## Platform Notes (Continued)

```
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

```
-----  
6. /proc/cpuinfo  
    model name      : Intel(R) Xeon(R) CPU E5-2670 v2 @ 2.50GHz  
    vendor_id       : GenuineIntel  
    cpu family     : 6  
    model          : 62  
    stepping       : 4  
    microcode      : 0x42e  
    bugs           : cpu_meltdown spectre_v1 spectre_v2 spec_store_bypass l1tf mds swapgs itlb_multihit  
    cpu cores      : 10  
    siblings       : 20  
    2 physical ids (chips)  
    40 processors (hardware threads)  
    physical id 0: core ids 0-4,8-12  
    physical id 1: core ids 0-4,8-12  
    physical id 0: apicids 0-9,16-25  
    physical id 1: apicids 32-41,48-57
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

```
-----  
7. lscpu
```

From lscpu from util-linux 2.37.4:

```
Architecture:                  x86_64  
CPU op-mode(s):                32-bit, 64-bit  
Address sizes:                 46 bits physical, 48 bits virtual  
Byte Order:                   Little Endian  
CPU(s):                      40  
On-line CPU(s) list:          0-39  
Vendor ID:                    GenuineIntel  
BIOS Vendor ID:               Intel  
Model name:                   Intel(R) Xeon(R) CPU E5-2670 v2 @ 2.50GHz  
BIOS Model name:              Intel(R) Xeon(R) CPU E5-2670 v2 @ 2.50GHz  
CPU family:                   6  
Model:                        62  
Thread(s) per core:           2  
Core(s) per socket:           10  
Socket(s):                   2  
Stepping:                     4  
BogoMIPS:                     4987.79  
Flags:                         fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36  
                                clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp  
                                lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc  
                                cpuid aperf mperf pn1 pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3  
                                cx16 xtrpr pdcm pcid dca sse4_1 sse4_2 x2apic popcnt tsc_deadline_timer aes  
                                xsave avx f16c rdrand lahf_lm cpuid_fault epb pt1 intel_ppin ssbd ibrs  
                                ibpb stibp tpr_shadow vnmi flexpriority ept vpid fsgsbase smep erms  
                                xsaveopt dtherm ida arat pln pts md_clear flush_lld  
Virtualization:               VT-x  
L1d cache:                    640 KiB (20 instances)  
L1i cache:                    640 KiB (20 instances)  
L2 cache:                     5 MiB (20 instances)  
L3 cache:                     50 MiB (2 instances)  
NUMA node(s):                 2  
NUMA node0 CPU(s):            0-9,20-29  
NUMA node1 CPU(s):            10-19,30-39
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380p Gen8  
(2.50 GHz, Intel Xeon E5-2670 v2)

**SPECrate®2017\_fp\_base = 83.5**

**SPECrate®2017\_fp\_peak = Not Run**

CPU2017 License: 3

**Test Date:** May-2023

Test Sponsor: HPE

**Hardware Availability:** May-2019

Tested by: HPE

**Software Availability:** Dec-2022

## Platform Notes (Continued)

Vulnerability Itlb multihit:

KVM: Mitigation: VMX disabled

Vulnerability Lltf:

Mitigation: PTE Inversion; VMX conditional cache flushes, SMT vulnerable

Vulnerability Mds:

Mitigation: Clear CPU buffers; SMT vulnerable

Vulnerability Meltdown:

Mitigation: PTI

Vulnerability Spec store bypass:

Mitigation: Speculative Store Bypass disabled via prctl

Vulnerability Spectre v1:

Mitigation: usercopy/swapgs barriers and \_\_user pointer sanitization

Vulnerability Spectre v2:

Mitigation: Retpolines, IBPB conditional, IBRS\_FW, STIBP conditional, RSB filling

Vulnerability Srbds:

Not affected

Vulnerability Tsx async abort:

Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	32K	640K	8	Data	1	64	1	64
L1i	32K	640K	8	Instruction	1	64	1	64
L2	256K	5M	8	Unified	2	512	1	64
L3	25M	50M	20	Unified	3	20480	1	64

-----  
8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 2 nodes (0-1)

node 0 cpus: 0-9,20-29

node 0 size: 63893 MB

node 0 free: 37457 MB

node 1 cpus: 10-19,30-39

node 1 size: 64459 MB

node 1 free: 59077 MB

node distances:

node 0 1

  0: 10 20

  1: 20 10

-----  
9. /proc/meminfo

MemTotal: 131434248 kB

-----  
10. who -r

run-level 3 Apr 6 20:00

-----  
11. Systemd service manager version: systemd 250 (250-6.el9\_0)

Default	Target	Status
multi-user		degraded

-----  
12. Failed units, from systemctl list-units --state=failed

UNIT	LOAD	ACTIVE	SUB	DESCRIPTION
* run-r3487d2c7195e446ca2414f089c797bd3.service	loaded	failed	failed	/usr/bin/systemctl start man-db-cache-update

-----  
13. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	NetworkManager NetworkManager-dispatcher NetworkManager-wait-online atd auditd bluetooth chronyd crond dbus-broker firewalld getty@ insights-client-boot irqbalance kdump libstoragemgmt lvm2-monitor mcelog mdmonitor microcode nis-domainname nvmefc-boot-connections rhsmcertd rsyslog selinux-autorelabel-mark smartd sshd sssd systemd-network-generator upower

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380p Gen8  
(2.50 GHz, Intel Xeon E5-2670 v2)

SPECrate®2017\_fp\_base = 83.5

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: May-2023

Hardware Availability: May-2019

Software Availability: Dec-2022

## Platform Notes (Continued)

```
enabled-runtime    systemd-remount-fs
disabled          arp-ethers blk-availability canberra-system-bootup canberra-system-shutdown
                  canberra-system-shutdown-reboot chrony-wait console-getty cpupower debug-shell iprdump
                  iprinit ipruleupdate kpatch kvm_stat ledmon man-db-restart-cache-update nftables
                  nvmf-autoconnect psacct rdisc rhcd rhsm rhsm-facts rpmbuild serial-getty@
                  sshd-keygen@ systemd-boot-check-no-failures systemd-pstore systemd-sysext
indirect          sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo
transient         run-r3487d2c7195e446ca2414f089c797bd3

-----
14. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=(hd0,gpt2)/vmlinuz-5.14.0-70.13.1.el9_0.x86_64
root=/dev/mapper/rhel-root
ro
crashkernel=1G-4G:192M,4G-64G:256M,64G-:512M
resume=/dev/mapper/rhel-swap
rd.lvm.lv=rhel/root
rd.lvm.lv=rhel/swap
rhgb
quiet

-----
15. cpupower frequency-info
analyzing CPU 0:
  Unable to determine current policy
  boost state support:
    Supported: yes
    Active: yes

-----
16. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space       2
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio      10
vm.dirty_bytes                 0
vm.dirty_expire_centisecs     3000
vm.dirty_ratio                 20
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                   60
vm.watermark_boost_factor      15000
vm.watermark_scale_factor       10
vm.zone_reclaim_mode            0

-----
17. /sys/kernel/mm/transparent_hugepage
defrag           always defer defer+madvise [madvise] never
enabled          [always] madvise never
hpage_pmd_size  2097152
shmem_enabled    always within_size advise [never] deny force

-----
18. /sys/kernel/mm/transparent_hugepage/khugepaged
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380p Gen8  
(2.50 GHz, Intel Xeon E5-2670 v2)

SPECrate®2017\_fp\_base = 83.5

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: May-2023

Hardware Availability: May-2019

Software Availability: Dec-2022

## Platform Notes (Continued)

```
alloc_sleep_millisecs    60000
defrag                  1
max_ptes_none           511
max_ptes_shared          256
max_ptes_swap            64
pages_to_scan            4096
scan_sleep_millisecs    10000
```

---

```
19. OS release
From /etc/*-release /etc/*-version
os-release      Red Hat Enterprise Linux 9.0 (Plow)
redhat-release Red Hat Enterprise Linux release 9.0 (Plow)
system-release Red Hat Enterprise Linux release 9.0 (Plow)
```

---

```
20. Disk information
SPEC is set to: /home/cpu2017
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs   2.2T   44G  2.1T   3% /home
```

---

```
21. /sys/devices/virtual/dmi/id
Vendor:          HP
Product:         ProLiant DL380p Gen8
Product Family: ProLiant
Serial:          USE22897AP
```

---

```
22. dmidecode
Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
 8x HP 712383-081 16 GB 2 rank 1866
```

---

```
23. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor:      HP
BIOS Version:     P70
BIOS Date:        05/24/2019
```

## Compiler Version Notes

---

```
=====
C          | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
=====
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.1.0 Build 20230320
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
```

---

```
=====
C++         | 508.namd_r(base) 510.parest_r(base)
=====
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.1.0 Build 20230320
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380p Gen8  
(2.50 GHz, Intel Xeon E5-2670 v2)

SPECrate®2017\_fp\_base = 83.5

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: May-2023

Hardware Availability: May-2019

Software Availability: Dec-2022

## Compiler Version Notes (Continued)

=====

C++, C | 511.povray\_r(base) 526.blender\_r(base)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.1.0 Build 20230320  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.1.0 Build 20230320  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====

C++, C, Fortran | 507.cactusBSSN\_r(base)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.1.0 Build 20230320  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.1.0 Build 20230320  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.  
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.1.0 Build 20230320  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====

Fortran | 503.bwaves\_r(base) 549.fotonik3d\_r(base) 554.roms\_r(base)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.1.0 Build 20230320  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====

Fortran, C | 521.wrf\_r(base) 527.cam4\_r(base)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.1.0 Build 20230320  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.1.0 Build 20230320  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====

## Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380p Gen8  
(2.50 GHz, Intel Xeon E5-2670 v2)

SPECrate®2017\_fp\_base = 83.5

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: May-2023

Hardware Availability: May-2019

Software Availability: Dec-2022

## Base Compiler Invocation (Continued)

Benchmarks using Fortran, C, and C++:

icpx icx ifx

## Base Portability Flags

```
503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xAVX -Ofast -ffast-math -fsto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -Wno-implicit-int
-ljemalloc -L/home/cpu2017/je5.0.1-64/
```

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xAVX -Ofast -ffast-math -fsto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -ljemalloc
-L/home/cpu2017/je5.0.1-64/
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xAVX -Ofast -ffast-math -fsto -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/home/cpu2017/je5.0.1-64/
```

Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xAVX -Ofast -ffast-math -fsto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -Wno-implicit-int
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380p Gen8  
(2.50 GHz, Intel Xeon E5-2670 v2)

SPECrate®2017\_fp\_base = 83.5

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: May-2023

Hardware Availability: May-2019

Software Availability: Dec-2022

## Base Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):

-L/home/cpu2017/je5.0.1-64/

Benchmarks using both C and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xAVX -Ofast
-ffast-math -futto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -ljemalloc
-L/home/cpu2017/je5.0.1-64/
```

Benchmarks using Fortran, C, and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xAVX -Ofast
-ffast-math -futto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/home/cpu2017/je5.0.1-64/
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.0-revA.html>  
<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.2023-06-06.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.0-revA.xml>  
<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.2023-06-06.xml>

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.9 on 2023-05-06 03:09:50-0400.

Report generated on 2023-06-06 19:14:20 by CPU2017 PDF formatter v6716.

Originally published on 2023-06-06.