



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

H3C UniServer R4900 G6 Ultra (Intel Xeon Silver 4416+)

SPECspeed®2017_fp_base = 238

SPECspeed®2017_fp_peak = Not Run

CPU2017 License: 9066

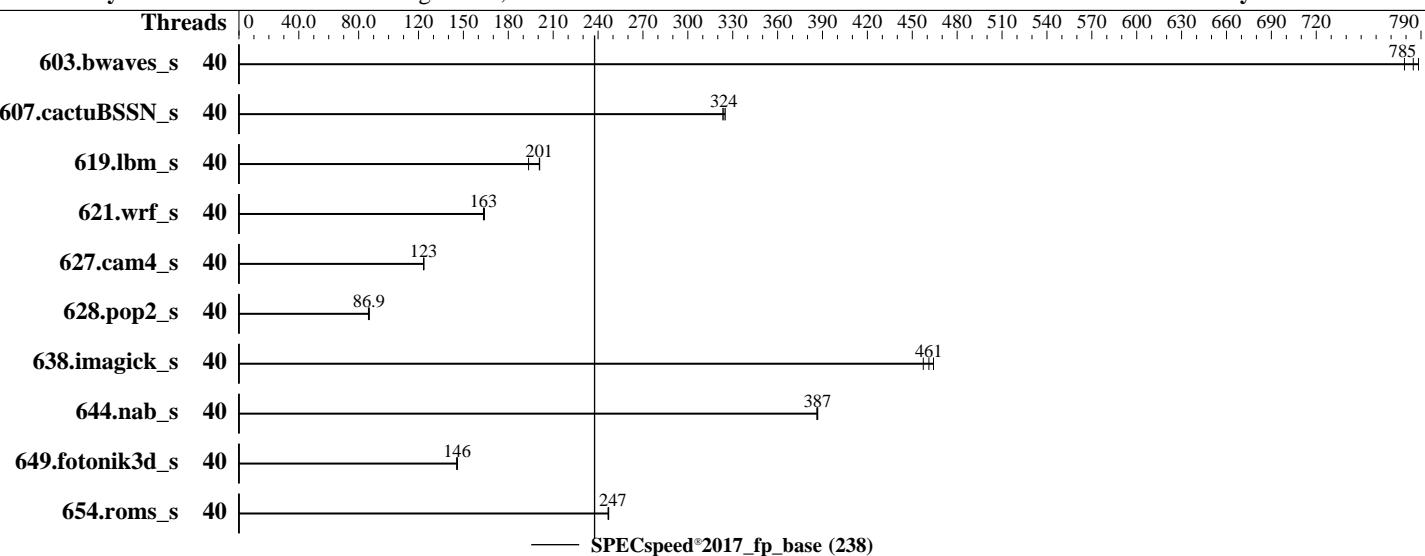
Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: Apr-2023

Hardware Availability: Jan-2023

Software Availability: Dec-2022



Hardware

CPU Name: Intel Xeon Silver 4416+
 Max MHz: 3900
 Nominal: 2000
 Enabled: 40 cores, 2 chips
 Orderable: 1,2 chips
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 2 MB I+D on chip per core
 L3: 37.5 MB I+D on chip per chip
 Other: None
 Memory: 512 GB (16 x 32 GB 2Rx8 PC5-4800B-R, running at 4000)
 Storage: 1 x 3.2 TB NVME SSD
 Other: None

Software

OS: Red Hat Enterprise Linux 9.0 (Plow)
 5.14.0-70.22.1.el9_0.x86_64
 Compiler: C/C++: Version 2023.0 of Intel oneAPI DPC++/C++ Compiler for Linux;
 Fortran: Version 2023.0 of Intel Fortran Compiler for Linux;
 Parallel: Yes
 Firmware: Version 5.29 released Mar-2023 BIOS
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: Not Applicable
 Other: jemalloc memory allocator V5.0.1
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds
603.bwaves_s	40	75.2	785	74.8	788	75.7	779							
607.cactuBSSN_s	40	51.4	324	51.5	323	51.3	325							
619.lbm_s	40	26.1	201	27.1	194	26.1	201							
621.wrf_s	40	80.9	163	80.6	164	80.9	163							
627.cam4_s	40	71.8	123	71.7	124	71.8	123							
628.pop2_s	40	137	86.9	137	86.5	136	87.1							
638.imagick_s	40	31.5	457	31.3	461	31.1	464							
644.nab_s	40	45.2	387	45.2	386	45.2	387							
649.fotonik3d_s	40	62.5	146	62.5	146	62.6	146							
654.roms_s	40	63.8	247	63.8	247	63.7	247							

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

tuned-adm profile was set to Throughput-Performance using "tuned-adm profile throughput-performance"
cpupower was set to performance using "cpupower frequency-set -g performance"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"

LD_LIBRARY_PATH = "/home/speccpu2/lib/intel64:/home/speccpu2/je5.0.1-64"

MALLOC_CONF = "retain:true"

OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

sync; echo 3> /proc/sys/vm/drop_caches

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases



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Platform Notes

BIOS Settings:

```
Set Enable LP [Global] to Single LP
Set Patrol Scrub to Disabled
Set Power Performance Tuning to BIOS Controls EPB
Set ENERGY_PERF_BIAS_CFG mode to Performance
Set LLC Prefetch to Disabled
Set FB Thread Slicing to Enabled
```

```
Sysinfo program /home/speccpu2/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost.localdomain Tue Apr 25 22:26:08 2023
```

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 250 (250-6.el9_0)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

```
1. uname -a
Linux localhost.localdomain 5.14.0-70.22.1.el9_0.x86_64 #1 SMP PREEMPT Tue Aug 2 10:02:12 EDT 2022 x86_64
x86_64 x86_64 GNU/Linux
```

```
2. w
22:26:08 up 5 min, 2 users, load average: 0.00, 0.00, 0.00
USER     TTY      LOGIN@    IDLE   JCPU   PCPU WHAT
root     tty1     22:21   16.00s  0.77s  0.00s -bash
root     pts/0     22:21    4:14   0.02s  0.02s -bash
```

```
3. Username
From environment variable $USER: root
```

```
4. ulimit -a
```

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Platform Notes (Continued)

```
real-time non-blocking time  (microseconds, -R) unlimited
core file size              (blocks, -c) 0
data seg size               (kbytes, -d) unlimited
scheduling priority         (-e) 0
file size                   (blocks, -f) unlimited
pending signals             (-i) 2060502
max locked memory           (kbytes, -l) 64
max memory size             (kbytes, -m) unlimited
open files                  (-n) 1024
pipe size                   (512 bytes, -p) 8
POSIX message queues        (bytes, -q) 819200
real-time priority          (-r) 0
stack size                  (kbytes, -s) unlimited
cpu time                    (seconds, -t) unlimited
max user processes           (-u) 2060502
virtual memory               (kbytes, -v) unlimited
file locks                  (-x) unlimited
```

```
-----  
5. sysinfo process ancestry  
/usr/lib/systemd/systemd rhgb --switched-root --system --deserialize 31  
login -- root  
-bash  
-bash  
runcpu --nobuild --action validate --define default-platform-flags -c  
  ic2023.0-lin-sapphirerapids-speed-20221201.cfg --define cores=40 --tune base -o all --define drop_caches  
  fpspeed  
runcpu --nobuild --action validate --define default-platform-flags --configfile  
  ic2023.0-lin-sapphirerapids-speed-20221201.cfg --define cores=40 --tune base --output_format all --define  
  drop_caches --nopower --runmode speed --tune base --size refspeed fpspeed --nopreenv --note-preenv  
  --logfile $SPEC/tmp/CPU2017.001/templogs/preenv.fpspeed.001.0.log --lognum 001.0 --from_runcpu 2  
  specperl $SPEC/bin/sysinfo  
$SPEC = /home/speccpu2
```

```
-----  
6. /proc/cpuinfo  
model name      : Intel(R) Xeon(R) Silver 4416+
vendor_id       : GenuineIntel
cpu family      : 6
model          : 143
stepping        : 8
microcode       : 0x2b000181
bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs
cpu cores       : 20
siblings        : 20
2 physical ids (chips)
40 processors (hardware threads)
physical id 0: core ids 0-19
physical id 1: core ids 0-19
physical id 0: apicids 0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38
physical id 1: apicids 128,130,132,134,136,138,140,142,144,146,148,150,152,154,156,158,160,162,164,166
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.
```

```
-----  
7. lscpu
```

```
From lscpu from util-linux 2.37.4:
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
```

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Platform Notes (Continued)

Address sizes:	52 bits physical, 57 bits virtual
Byte Order:	Little Endian
CPU(s):	40
On-line CPU(s) list:	0-39
Vendor ID:	GenuineIntel
BIOS Vendor ID:	Intel(R) Corporation
Model name:	Intel(R) Xeon(R) Silver 4416+
BIOS Model name:	Intel(R) Xeon(R) Silver 4416+
CPU family:	6
Model:	143
Thread(s) per core:	1
Core(s) per socket:	20
Socket(s):	2
Stepping:	8
CPU max MHz:	3900.0000
CPU min MHz:	800.0000
BogoMIPS:	4000.00
Flags:	fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmon tsc_known_freq pn1 pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtrp pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrandlahf_lm abm 3dnowprefetch cpuid_fault epb cat_13 cat_12 cdp_13 invpcid_single intel_ppin cdp_12 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnni flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqmm_llc cqmm_occu11c cqmm_mbmm_total cqmm_mbmm_local split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpocntdq la57 rdpid bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear serialize tsxlptrk pconfig arch_lbr avx512_fp16 amx_tile flush_ll1d arch_capabilities
Virtualization:	VT-x
L1d cache:	1.9 MiB (40 instances)
L1i cache:	1.3 MiB (40 instances)
L2 cache:	80 MiB (40 instances)
L3 cache:	75 MiB (2 instances)
NUMA node(s):	2
NUMA node0 CPU(s):	0-19
NUMA node1 CPU(s):	20-39
Vulnerability Itlb multihit:	Not affected
Vulnerability Llft:	Not affected
Vulnerability Mds:	Not affected
Vulnerability Meltdown:	Not affected
Vulnerability Spec store bypass:	Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1:	Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2:	Mitigation; Enhanced IBRS, IBPB conditional, RSB filling
Vulnerability Srbds:	Not affected
Vulnerability Tsx async abort:	Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	1.9M	12	Data	1	64	1	64
L1i	32K	1.3M	8	Instruction	1	64	1	64
L2	2M	80M	16	Unified	2	2048	1	64
L3	37.5M	75M	15	Unified	3	40960	1	64

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Platform Notes (Continued)

8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0-19
node 0 size: 257169 MB
node 0 free: 256425 MB
node 1 cpus: 20-39
node 1 size: 257996 MB
node 1 free: 256851 MB
node distances:
node 0 1
0: 10 21
1: 21 10

9. /proc/meminfo
MemTotal: 527530004 kB

10. who -r
run-level 3 Apr 25 22:21

11. Systemd service manager version: systemd 250 (250-6.el9_0)
Default Target Status
multi-user running

12. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled NetworkManager NetworkManager-dispatcher NetworkManager-wait-online atd auditd bluetooth
chrony crond dbus-broker firewalld getty@ insights-client-boot irqbalance iscsi
iscsi-onboot kdump libstoragemgmt lvm2-monitor mcelog mdmonitor microcode multipathd
nis-domainname nvmefc-boot-connections rhsmcertd rsyslog selinux-autorelabel-mark smartd
sshd sssd systemd-network-generator tuned udisks2
enabled-runtime systemd-remount-fs
disabled arp-ethers blk-availability chrony-wait cni-dhcp console-getty cpupower debug-shell
iprdump iprinit iprupdate iscsid iscsiui0 kpatch kvm_stat ledmon
man-db-restart-cache-update nftables nvmf-autoconnect podman podman-auto-update
podman-restart psacct rdisc rhcd rhsm rhsm-facts rpmbuild serial-getty@ sshd-keygen@
systemd-boot-check-no-failures systemd-pstore systemd-sysext
indirect sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo

13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=(hd1,gpt2)/vmlinuz-5.14.0-70.22.1.el9_0.x86_64
root=/dev/mapper/rhel-root
ro
crashkernel=1G-4G:192M,4G-64G:256M,64G-:512M
resume=/dev/mapper/rhel-swap
rd.lvm.lv=rhel/root
rd.lvm.lv=rhel/swap
rhgb
quiet

14. cpupower frequency-info
analyzing CPU 0:
current policy: frequency should be within 800 MHz and 3.90 GHz.

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Platform Notes (Continued)

The governor "performance" may decide which speed to use within this range.

boost state support:

Supported: yes

Active: yes

15. tuned-adm active

Current active profile: throughput-performance

16. sysctl

kernel.numa_balancing	1
kernel.randomize_va_space	2
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	10
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	40
vm.dirty_writeback_centisecs	500
vm.dirtytime_expire_seconds	43200
vm.extfrag_threshold	500
vm.min_unmapped_ratio	1
vm.nr_hugepages	0
vm.nr_hugepages_mempolicy	0
vm.nr_overcommit_hugepages	0
vm.swappiness	10
vm.watermark_boost_factor	15000
vm.watermark_scale_factor	10
vm.zone_reclaim_mode	0

17. /sys/kernel/mm/transparent_hugepage

defrag	always defer defer+madvise [madvise] never
enabled	[always] madvise never
hpage_pmd_size	2097152
shmem_enabled	always within_size advise [never] deny force

18. /sys/kernel/mm/transparent_hugepage/khugepaged

alloc_sleep_millisecs	60000
defrag	1
max_ptes_none	511
max_ptes_shared	256
max_ptes_swap	64
pages_to_scan	4096
scan_sleep_millisecs	10000

19. OS release

From /etc/*-release /etc/*-version
os-release Red Hat Enterprise Linux 9.0 (Plow)
redhat-release Red Hat Enterprise Linux release 9.0 (Plow)
system-release Red Hat Enterprise Linux release 9.0 (Plow)

20. Disk information

SPEC is set to: /home/speccpu2
Filesystem Type Size Used Avail Use% Mounted on

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Platform Notes (Continued)

```
/dev/mapper/rhel-home xfs 2.9T 72G 2.8T 3% /home
```

```
21. /sys/devices/virtual/dmi/id  
Product Family: Rack
```

```
22. dmidecode  
Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section.  
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately  
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the  
"DMTF SMBIOS" standard.
```

```
Memory:  
16x Micron MTC20F2085S1RC48BA1 32 GB 2 rank 4800, configured at 4000
```

```
23. BIOS  
(This section combines info from /sys/devices and dmidecode.)  
BIOS Vendor: American Megatrends International, LLC.  
BIOS Version: 6.00.17  
BIOS Date: 03/30/2023  
BIOS Revision: 5.29
```

Compiler Version Notes

```
=====  
C | 619.lbm_s(base) 638.imagick_s(base) 644.nab_s(base)
```

```
=====  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
```

```
=====  
C++, C, Fortran | 607.cactubssn_s(base)
```

```
=====  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
```

```
=====  
Fortran | 603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)
```

```
=====  
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
```

```
=====  
Fortran, C | 621.wrf_s(base) 627.cam4_s(base) 628.pop2_s(base)
```

```
=====  
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
```



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Base Compiler Invocation

C benchmarks:

icx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fopenmp
-DSPEC_OPENMP -Wno-implicit-int -mprefer-vector-width=512
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Fortran benchmarks:

-m64 -Wl,-z,muldefs -DSPEC_OPENMP -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fopenmp -nostandard-realloc-lhs
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using both Fortran and C:

-m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math

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H3C UniServer R4900 G6 Ultra (Intel Xeon Silver 4416+)

SPECspeed®2017_fp_base = 238

SPECspeed®2017_fp_peak = Not Run

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Hardware Availability: Jan-2023

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Software Availability: Dec-2022

Base Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):

```
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fopenmp
-DSPEC_OPENMP -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fopenmp -DSPEC_OPENMP -Wno-implicit-int
-mprefer-vector-width=512 -nostandard-realloc-lhs -align array32byte
-auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.html>

http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.0-SPR-RevC.html

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.xml>

http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.0-SPR-RevC.xml

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