



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Supermicro

Hyper A+ Server AS -2025HS-TNR
(H13DSH , AMD EPYC 9224)

SPECrate®2017_int_base = 500

SPECrate®2017_int_peak = 524

CPU2017 License: 001176

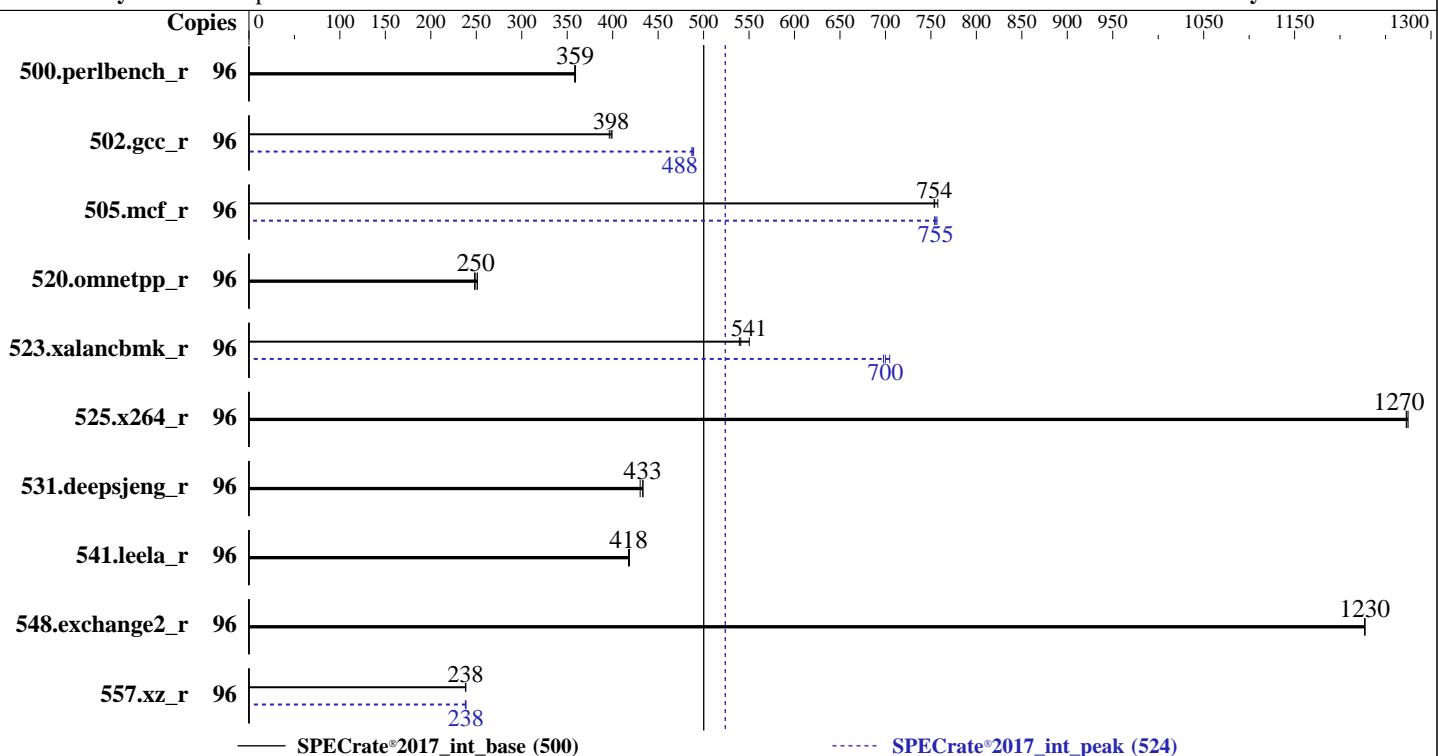
Test Date: May-2023

Test Sponsor: Supermicro

Hardware Availability: Nov-2022

Tested by: Supermicro

Software Availability: Feb-2023



Hardware		Software	
CPU Name:	AMD EPYC 9224	OS:	Ubuntu 22.04.2 LTS
Max MHz:	3700	Compiler:	Kernel 5.15.0-72-generic
Nominal:	2500	Parallel:	C/C++/Fortran: Version 4.0.0 of AOCC
Enabled:	48 cores, 2 chips, 2 threads/core	Firmware:	No
Orderable:	1,2 chips	File System:	Version 1.4 released Apr-2023
Cache L1:	32 KB I + 32 KB D on chip per core	System State:	ext4
L2:	1 MB I+D on chip per core	Base Pointers:	Run level 3 (multi-user)
L3:	64 MB I+D on chip per chip, 16 MB shared / 6 cores	Peak Pointers:	64-bit
Other:	None	Other:	32/64-bit
Memory:	384 GB (24 x 16 GB 1Rx8 PC5-4800B-R)	Power Management:	None
Storage:	1 x 14 TB NVMe SSD	BIOS and OS set to prefer performance at the cost of additional power usage.	
Other:	None		



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Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	96	425	359	426	359	427	358	96	425	359	426	359	427	358		
502.gcc_r	96	340	399	341	398	343	396	96	279	487	278	489	278	488		
505.mcf_r	96	206	754	205	758	206	754	96	206	754	205	755	205	757		
520.omnetpp_r	96	501	251	505	250	508	248	96	501	251	505	250	508	248		
523.xalancbmk_r	96	188	539	187	541	184	550	96	145	698	144	705	145	700		
525.x264_r	96	132	1270	132	1270	132	1270	96	132	1270	132	1270	132	1270		
531.deepsjeng_r	96	254	433	254	433	256	430	96	254	433	254	433	256	430		
541.leela_r	96	381	418	381	418	380	418	96	381	418	381	418	380	418		
548.exchange2_r	96	205	1230	205	1230	205	1230	96	205	1230	205	1230	205	1230		
557.xz_r	96	435	238	435	238	435	239	96	436	238	435	239	435	238		

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) only on request for base runs,
'echo madvise > /sys/kernel/mm/transparent_hugepage/enabled' run as root.
To enable THP for all allocations for peak runs,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.



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Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =
    "/home/cpu2017/amd_rate_aocc400_genoa_B_lib/lib:/home/cpu2017/amd_rate_aocc400_genoa_B_lib/lib32:"
MALLOC_CONF = "retain:true"
```

Environment variables set by runcpu during the 523.xalancbmk_r peak run:

```
MALLOC_CONF = "thp:never"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Settings:
NUMA Nodes Per Socket = NPS4
TSME = Disabled
Determinism Control = Manual
Determinism Enable = Disable Performance Determinism
cTDP Control = Manual
cTDP = 240
Package Power Limit Control = Manual
Package Power Limit = 240

```
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on as-2025hs-tnr-9224 Fri May 19 03:09:28 2023
```

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.11-0ubuntu3.9)
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. cpupower frequency-info
16. sysctl

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Platform Notes (Continued)

```
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS
```

```
1. uname -a
Linux as-2025hs-tnr-9224 5.15.0-72-generic #79-Ubuntu SMP Wed Apr 19 08:22:18 UTC 2023 x86_64 x86_64 x86_64
GNU/Linux
```

```
2. w
03:09:28 up 3 min, 2 users, load average: 0.22, 0.63, 0.33
USER      TTY      FROM          LOGIN@    IDLE     JCPU     PCPU WHAT
lab        tty1     -           03:08   32.00s  0.14s  0.00s -bash
lab        pts/0     -           03:08   16.00s  1.12s  0.11s sudo su -
```

```
3. Username
From environment variable $USER: root
From the command 'logname': lab
```

```
4. ulimit -a
time(seconds)          unlimited
file(blocks)           unlimited
data(kbytes)           unlimited
stack(kbytes)          unlimited
coredump(blocks)       0
memory(kbytes)         unlimited
locked memory(kbytes) 2097152
process                1545875
nofiles               1024
vmemory(kbytes)        unlimited
locks                 unlimited
rtprio                 0
```

```
5. sysinfo process ancestry
/sbin/init
/bin/login -p --
-bash
sudo su -
sudo su -
su -
-bash
python3 ./run_amd_rate_aocc400_genoa_B1.py
/bin/bash ./amd_rate_aocc400_genoa_B1.sh
runcpu --config amd_rate_aocc400_genoa_B1.cfg --tune all --reportable --iterations 3 intrate
runcpu --configfile amd_rate_aocc400_genoa_B1.cfg --tune all --reportable --iterations 3 --nopower --runmode
  rate --tune base:peak --size test:train:refrate intrate --nopreenv --note-preenv --logfile
    $SPEC/tmp/CPU2017.001/templogs/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

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Platform Notes (Continued)

6. /proc/cpuinfo

```
model name      : AMD EPYC 9224 24-Core Processor
vendor_id       : AuthenticAMD
cpu family     : 25
model          : 17
stepping        : 1
microcode       : 0xa101116
bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size        : 3584 4K pages
cpu cores       : 24
siblings        : 48
2 physical ids (chips)
96 processors (hardware threads)
physical id 0: core ids 0-5,8-13,16-21,24-29
physical id 1: core ids 0-5,8-13,16-21,24-29
physical id 0: apicids 0-11,16-27,32-43,48-59
physical id 1: apicids 64-75,80-91,96-107,112-123
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.
```

7. lscpu

From lscpu from util-linux 2.37.2:

```
Architecture:           x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         52 bits physical, 57 bits virtual
Byte Order:             Little Endian
CPU(s):                96
On-line CPU(s) list:   0-95
Vendor ID:              AuthenticAMD
Model name:             AMD EPYC 9224 24-Core Processor
CPU family:             25
Model:                 17
Thread(s) per core:    2
Core(s) per socket:    24
Socket(s):              2
Stepping:               1
Frequency boost:       enabled
CPU max MHz:           3706.0540
CPU min MHz:           1500.0000
BogoMIPS:               4999.76
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                        clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
                        constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmpfperf rapl
                        pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe
                        popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy
                        abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext
                        perfctr_core perfctr_nb bpxt perfctr_llc mwaitx cpb cat_13 cdp_13
                        invpcid_single hw_pstate ssbd mba ibrs ibpb stibp vmmcall fsgsbase bmil
                        avx2 smep bmi2 erms invpcid cqmq rdt_a avx512f avx512dq rdseed adx smap
                        avx512fimf clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
                        xsavenc xgetbv1 xsaves cqmq_llc cqmq_occup_llc cqmq_mbm_total cqmq_mbm_local
                        avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd amd_ppin cppo arat npt
                        lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists
                        pausefilter pfthreshold avic v_vmsave_vmlload vgif v_spec_ctrl avx512vbmi
                        umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg
                        avx512_vpocntdq la57 rdpid overflow_recov succor smca fsrm flush_l1d
AMD-V
L1d cache:             1.5 MiB (48 instances)
```

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Platform Notes (Continued)

L1i cache:	1.5 MiB (48 instances)
L2 cache:	48 MiB (48 instances)
L3 cache:	128 MiB (8 instances)
NUMA node(s):	8
NUMA node0 CPU(s):	0-5,48-53
NUMA node1 CPU(s):	6-11,54-59
NUMA node2 CPU(s):	12-17,60-65
NUMA node3 CPU(s):	18-23,66-71
NUMA node4 CPU(s):	24-29,72-77
NUMA node5 CPU(s):	30-35,78-83
NUMA node6 CPU(s):	36-41,84-89
NUMA node7 CPU(s):	42-47,90-95
Vulnerability Itlb multihit:	Not affected
Vulnerability Llft:	Not affected
Vulnerability Mds:	Not affected
Vulnerability Meltdown:	Not affected
Vulnerability Mmio stale data:	Not affected
Vulnerability Retbleed:	Not affected
Vulnerability Spec store bypass:	Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1:	Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2:	Mitigation; Retpolines, IBPB conditional, IBRS_FW, STIBP always-on, RSB filling, PBRSB-eIBRS Not affected
Vulnerability Srbds:	Not affected
Vulnerability Tsx async abort:	Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	32K	1.5M	8	Data	1	64	1	64
L1i	32K	1.5M	8	Instruction	1	64	1	64
L2	1M	48M	8	Unified	2	2048	1	64
L3	16M	128M	16	Unified	3	16384	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 8 nodes (0-7)

node 0 cpus: 0-5,48-53

node 0 size: 48095 MB

node 0 free: 47538 MB

node 1 cpus: 6-11,54-59

node 1 size: 48368 MB

node 1 free: 47669 MB

node 2 cpus: 12-17,60-65

node 2 size: 48368 MB

node 2 free: 47853 MB

node 3 cpus: 18-23,66-71

node 3 size: 48368 MB

node 3 free: 47838 MB

node 4 cpus: 24-29,72-77

node 4 size: 48368 MB

node 4 free: 47907 MB

node 5 cpus: 30-35,78-83

node 5 size: 48368 MB

node 5 free: 47907 MB

node 6 cpus: 36-41,84-89

node 6 size: 48309 MB

node 6 free: 47859 MB

node 7 cpus: 42-47,90-95

node 7 size: 48333 MB

node 7 free: 47851 MB

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Platform Notes (Continued)

```
node distances:  
node  0   1   2   3   4   5   6   7  
0: 10 11 11 11 32 32 32 32  
1: 11 10 11 11 32 32 32 32  
2: 11 11 10 11 32 32 32 32  
3: 11 11 11 10 32 32 32 32  
4: 32 32 32 32 10 11 11 11  
5: 32 32 32 32 11 10 11 11  
6: 32 32 32 32 11 11 10 11  
7: 32 32 32 32 11 11 11 10
```

```
-----  
9. /proc/meminfo  
MemTotal:      395860884 kB
```

```
-----  
10. who -r  
run-level 3 May 19 03:07
```

```
-----  
11. Systemd service manager version: systemd 249 (249.11-0ubuntu3.9)  
Default Target  Status  
multi-user     degraded
```

```
-----  
12. Failed units, from systemctl list-units --state=failed  
UNIT          LOAD ACTIVE SUB   DESCRIPTION  
* systemd-networkd-wait-online.service loaded failed failed Wait for Network to be Configured
```

```
-----  
13. Services, from systemctl list-unit-files  
STATE          UNIT FILES  
enabled        ModemManager apparmor blk-availability cloud-config cloud-final cloud-init  
                cloud-init-local console-setup cron dmesg e2scrub_reap finalrd getty@ gpu-manager  
                grub-common grub-initrd-fallback irqbalance keyboard-setup lm-sensors lvm2-monitor  
                lxd-agent multipathd networkd-dispatcher open-iscsi open-vm-tools pollinate rsyslog  
                secureboot-db setvtrgb ssh systemd-networkd systemd-networkd-wait-online systemd-pstore  
                systemd-resolved systemd-timesyncd thermald ua-reboot-cmds ubuntu-advantage udisks2 ufw  
                vgaauth  
enabled-runtime netplan-ovs-cleanupsystemd-fsck-root systemd-remount-fs  
disabled       console-getty debug-shell iscsid nftables rsync serial-getty@  
                systemd-boot-check-no-failures systemd-network-generator systemd-sysext  
                systemd-time-wait-sync upower  
generated      apport  
indirect       uuid  
masked         cryptdisks cryptdisks-early hwclock lvm2 multipath-tools-boot rc rcS screen-cleanup sudo  
                x11-common
```

```
-----  
14. Linux kernel boot-time arguments, from /proc/cmdline  
BOOT_IMAGE=/boot/vmlinuz-5.15.0-72-generic  
root=UUID=8eea9839-275b-42c2-ae30-6471898efd3d  
ro
```

```
-----  
15. cpupower frequency-info  
analyzing CPU 0:  
    current policy: frequency should be within 1.50 GHz and 2.50 GHz.  
    The governor "performance" may decide which speed to use  
    within this range.
```

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Platform Notes (Continued)

```
boost state support:  
Supported: yes  
Active: yes  
Boost States: 0  
Total States: 3  
Pstate-P0: 2500MHz
```

```
-----  
16. sysctl  
kernel.numa_balancing          1  
kernel.randomize_va_space       0  
vm.compaction_proactiveness    20  
vm.dirty_background_bytes       0  
vm.dirty_background_ratio      10  
vm.dirty_bytes                 0  
vm.dirty_expire_centisecs     3000  
vm.dirty_ratio                 8  
vm.dirty_writeback_centisecs   500  
vm.dirtytime_expire_seconds    43200  
vm.extfrag_threshold           500  
vm.min_unmapped_ratio          1  
vm.nr_hugepages                0  
vm.nr_hugepages_mempolicy      0  
vm.nr_overcommit_hugepages     0  
vm.swappiness                  1  
vm.watermark_boost_factor      15000  
vm.watermark_scale_factor      10  
vm.zone_reclaim_mode           1
```

```
-----  
17. /sys/kernel/mm/transparent_hugepage  
defrag           [always] defer defer+madvise madvise never  
enabled          [always] madvise never  
hpage_pmd_size  2097152  
shmem_enabled    always within_size advise [never] deny force
```

```
-----  
18. /sys/kernel/mm/transparent_hugepage/khugepaged  
alloc_sleep_millisecs        60000  
defrag                      1  
max_ptes_none                511  
max_ptes_shared              256  
max_ptes_swap                64  
pages_to_scan                4096  
scan_sleep_millisecs         10000
```

```
-----  
19. OS release  
From /etc/*-release /etc/*-version  
os-release Ubuntu 22.04.2 LTS
```

```
-----  
20. Disk information  
SPEC is set to: /home/cpu2017  
Filesystem  Type  Size  Used  Avail Use% Mounted on  
/dev/nvme0n1p2  ext4  14T   17G  14T   1%  /
```

```
-----  
21. /sys/devices/virtual/dmi/id  
Vendor:      Supermicro
```

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Platform Notes (Continued)

Product: Super Server
Product Family: SMC H13
Serial: 123456789

22. dmidecode

Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

24x SK Hynix HMCG78MEBRA107N 16 GB 1 rank 4800

23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: American Megatrends International, LLC.
BIOS Version: 1.4
BIOS Date: 04/19/2023
BIOS Revision: 5.27

Compiler Version Notes

=====

C | 502.gcc_r(peak)

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on LLVM Mirror.Version.14.0.6)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

=====

C | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
| 557.xz_r(base, peak)

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

=====

C | 502.gcc_r(peak)

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on LLVM Mirror.Version.14.0.6)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

=====

C | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
| 557.xz_r(base, peak)

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Target: x86_64-unknown-linux-gnu
Thread model: posix

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Hardware Availability: Nov-2022

Tested by: Supermicro

Software Availability: Feb-2023

Compiler Version Notes (Continued)

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

=====

C++ | 523.xalancbmk_r(peak)

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on LLVM Mirror.Version.14.0.6)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

=====

C++ | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base) 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

=====

C++ | 523.xalancbmk_r(peak)

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on LLVM Mirror.Version.14.0.6)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

=====

C++ | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base) 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

=====

Fortran | 548.exchange2_r(base, peak)

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

Base Compiler Invocation

C benchmarks:

clang

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SPEC CPU®2017 Integer Rate Result

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Supermicro

Hyper A+ Server AS -2025HS-TNR
(H13DSH , AMD EPYC 9224)

SPECrate®2017_int_base = 500

SPECrate®2017_int_peak = 524

CPU2017 License: 001176

Test Date: May-2023

Test Sponsor: Supermicro

Hardware Availability: Nov-2022

Tested by: Supermicro

Software Availability: Feb-2023

Base Compiler Invocation (Continued)

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Base Portability Flags

500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LINUX -DSPEC_LP64
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-m64 -fno -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather
-z muldefs -O3 -march=znver4 -fveclib=AMDLIBM -ffast-math
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lflang
-lamdalloc

C++ benchmarks:

-m64 -fno -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -z muldefs -O3
-march=znver4 -fveclib=AMDLIBM -ffast-math
-mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt
-fvirtual-function-elimination -fvisibility=hidden -lamdlibm -lflang
-lamdalloc-ext

(Continued on next page)



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CPU2017 License: 001176

Test Sponsor: Supermicro

Tested by: Supermicro

Test Date: May-2023

Hardware Availability: Nov-2022

Software Availability: Feb-2023

Base Optimization Flags (Continued)

Fortran benchmarks:

```
-m64 -futto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop
-Wl,-mllvm -Wl,-enable-iv-split -z muldefs -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fepilog-vectorization-of-inductions
-mllvm -optimize-strided-mem-cost -floop-transform
-mllvm -unroll-aggressive -mllvm -unroll-threshold=500 -lamdlibm
-lflang -lamdalloc
```

Base Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

Peak Compiler Invocation

C benchmarks:

```
clang
```

C++ benchmarks:

```
clang++
```

Fortran benchmarks:

```
flang
```

Peak Portability Flags

```
500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LINUX -DSPEC_LP64
```

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Tested by: Supermicro

Software Availability: Feb-2023

Peak Portability Flags (Continued)

525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

500.perlbench_r: basepeak = yes

502.gcc_r: -m32 -flto -z muldefs -Ofast -march=znver4
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -fgnu89-inline
-lamdalloc

505.mcf_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math
-fstruct-layout=7 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lflang -lamdalloc

525.x264_r: basepeak = yes

557.xz_r: Same as 505.mcf_r

C++ benchmarks:

520.omnetpp_r: basepeak = yes

523.xalancbmk_r: -m32 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=aggressive
-fno-loop-reroll -Ofast -march=znver4 -fveclib=AMDLIBM
-ffast-math -finline-aggressive
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt
-mllvm -do-block-reorder=aggressive

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Peak Optimization Flags (Continued)

523.xalancbmk_r (continued):

```
-fvirtual-function-elimination -fvisibility=hidden  
-lamdalloc-ext
```

531.deepsjeng_r: basepeak = yes

541.leela_r: basepeak = yes

Fortran benchmarks:

548.exchange2_r: basepeak = yes

Peak Other Flags

C benchmarks (except as noted below):

```
-Wno-unused-command-line-argument
```

502.gcc_r: -L/usr/lib32 -Wno-unused-command-line-argument

```
-L/home/work/cpu2017/v118/aocc4/b1/rate/amd_rate_aocc400_genoa_B_lib/lib32
```

C++ benchmarks (except as noted below):

```
-Wno-unused-command-line-argument
```

523.xalancbmk_r: -L/usr/lib32 -Wno-unused-command-line-argument

```
-L/home/work/cpu2017/v118/aocc4/b1/rate/amd_rate_aocc400_genoa_B_lib/lib32
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc400-flags.html>

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-Genoa-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc400-flags.xml>

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-Genoa-revC.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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