



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR675 V3
(2.75 GHz, AMD EPYC 9454)

SPECrate®2017_fp_base = 1060

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 9017

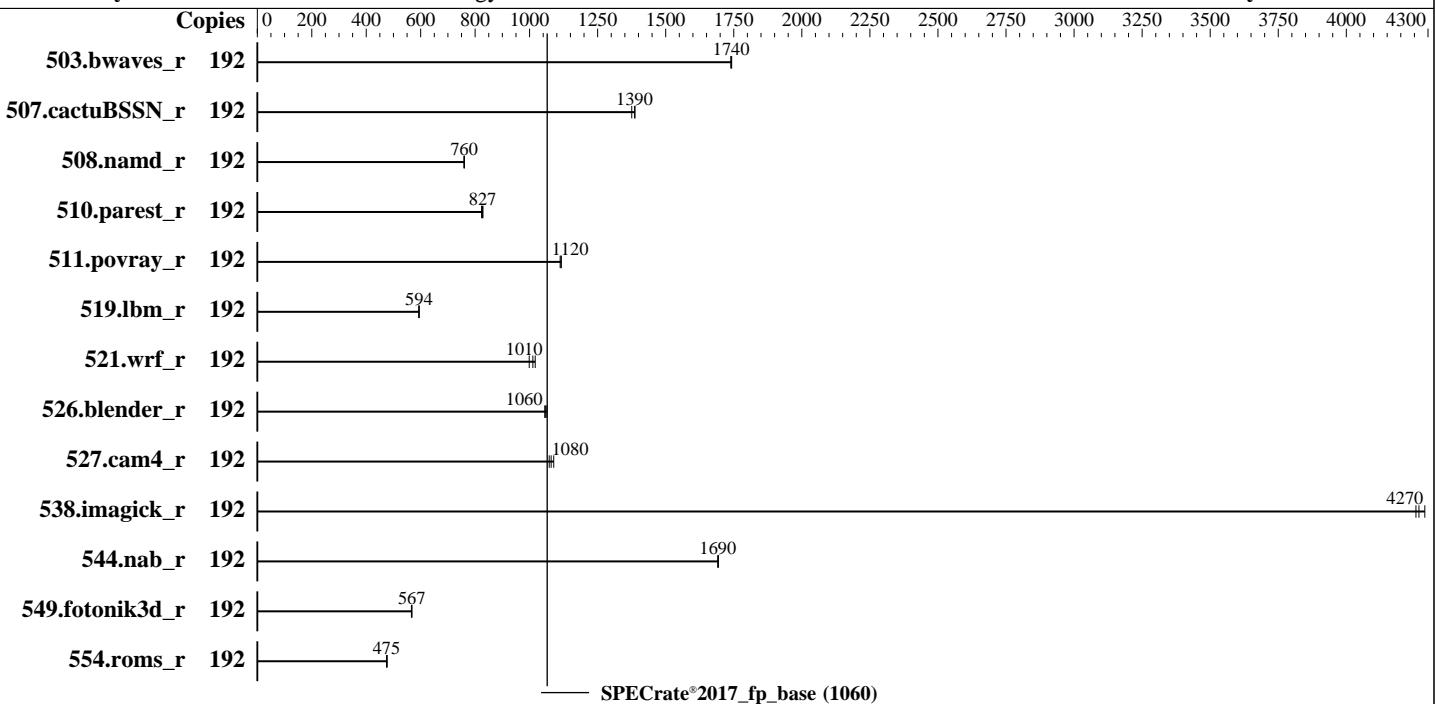
Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Nov-2023

Hardware Availability: Jul-2023

Software Availability: Nov-2022



Hardware

CPU Name: AMD EPYC 9454
Max MHz: 3800
Nominal: 2750
Enabled: 96 cores, 2 chips, 2 threads/core
Orderable: 1,2 chips
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 256 MB I+D on chip per chip,
32 MB shared / 6 cores
Other: None
Memory: 1536 GB (24 x 64 GB 2Rx4 PC5-4800B-R)
Storage: 1 x 3.84 TB NVME SSD
Other: None

Software

OS: SUSE Linux Enterprise Server 15 SP4
Compiler: Kernel 5.14.21-150400.22-default
Parallel: C/C++/Fortran: Version 4.0.0 of AOCC
Firmware: No
File System: Lenovo BIOS Version QGE115H 3.10 released Sep-2023
System State: btrfs
Base Pointers: Run level 3 (multi-user)
Peak Pointers: 64-bit
Other: Not Applicable
Power Management: None
BIOS and OS set to prefer performance at the cost of additional power usage



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Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	192	1106	1740	1107	1740	1105	1740									
507.cactuBSSN_r	192	177	1380	175	1390	175	1390									
508.namd_r	192	240	759	240	761	240	760									
510.parest_r	192	606	829	607	827	610	824									
511.povray_r	192	403	1110	402	1120	402	1120									
519.lbm_r	192	341	594	341	594	341	593									
521.wrf_r	192	431	999	421	1020	425	1010									
526.blender_r	192	277	1060	275	1060	276	1060									
527.cam4_r	192	308	1090	313	1070	311	1080									
538.imagick_r	192	112	4270	112	4260	111	4290									
544.nab_r	192	191	1690	191	1690	191	1690									
549.fotonik3d_r	192	1318	568	1322	566	1319	567									
554.roms_r	192	639	477	643	475	643	475									

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,

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Operating System Notes (Continued)

```
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
```

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =  
    "/home/cpu2017-1.1.9-amd-aocc400-znver4-A1.2/amd_rate_aocc400_znver4_A_lib/lib:/home/cpu2017-1.1.9-amd  
    -aocc400-znver4-A1.2/amd_rate_aocc400_znver4_A_lib/lib32:  
MALLOC_CONF = "retain:true"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:

Choose Operating Mode set to Maximum Performance and then set it to Custom Mode

NUMA Nodes per Socket set to NPS4

L2 Stream HW Prefetcher set to Disabled

```
Sysinfo program /home/cpu2017-1.1.9-amd-aocc400-znver4-A1.2/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost Sun Nov 19 03:14:47 2023
```

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl

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Platform Notes (Continued)

```
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS
-----
-----
1. uname -a
Linux localhost 5.14.21-150400.22-default #1 SMP PREEMPT_DYNAMIC Wed May 11 06:57:18 UTC 2022 (49db222)
x86_64 x86_64 x86_64 GNU/Linux
-----
2. w
03:14:47 up 1 min, 1 user, load average: 0.27, 0.09, 0.03
USER      TTY      FROM             LOGIN@    IDLE     JCPU    PCPU WHAT
root      tty1          -           03:14    23.00s   1.25s   0.16s /bin/bash ./amd_rate_aocc400_znver4_A1.sh
-----
3. Username
From environment variable $USER: root
-----
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size            (kbytes, -d) unlimited
scheduling priority      (-e) 0
file size                (blocks, -f) unlimited
pending signals          (-i) 6190578
max locked memory        (kbytes, -l) 2097152
max memory size          (kbytes, -m) unlimited
open files               (-n) 1024
pipe size                (512 bytes, -p) 8
POSIX message queues     (bytes, -q) 819200
real-time priority       (-r) 0
stack size               (kbytes, -s) unlimited
cpu time                 (seconds, -t) unlimited
max user processes        (-u) 6190578
virtual memory            (kbytes, -v) unlimited
file locks               (-x) unlimited
-----
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 30
login -- root
-bash
/bin/bash ./Run023-compliant-amd-ratefp_base.sh
python3 ./run_amd_rate_aocc400_znver4_A1.py
/bin/bash ./amd_rate_aocc400_znver4_A1.sh
runcpu --config amd_rate_aocc400_znver4_A1.cfg --tune base --reportable --iterations 3 fprate
runcpu --configfile amd_rate_aocc400_znver4_A1.cfg --tune base --reportable --iterations 3 --nopower
--runmode rate --tune base --size test:train:refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.203/templogs/preenv.fprate.203.0.log --lognum 203.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017-1.1.9-amd-aocc400-znver4-A1.2
-----
6. /proc/cpuinfo
```

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Platform Notes (Continued)

```
model name      : AMD EPYC 9454 48-Core Processor
vendor_id       : AuthenticAMD
cpu family     : 25
model          : 17
stepping        : 1
microcode       : 0xa10113b
bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size        : 3584 4K pages
cpu cores       : 48
siblings        : 96
2 physical ids (chips)
192 processors (hardware threads)
physical id 0: core ids 0-5,8-13,16-21,24-29,32-37,40-45,48-53,56-61
physical id 1: core ids 0-5,8-13,16-21,24-29,32-37,40-45,48-53,56-61
physical id 0: apicids 0-11,16-27,32-43,48-59,64-75,80-91,96-107,112-123
physical id 1: apicids 128-139,144-155,160-171,176-187,192-203,208-219,224-235,240-251
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.
```

7. lscpu

```
From lscpu from util-linux 2.37.2:
Architecture:           x86_64
CPU op-mode(s):         32-bit, 64-bit
Address sizes:          52 bits physical, 57 bits virtual
Byte Order:              Little Endian
CPU(s):                 192
On-line CPU(s) list:    0-191
Vendor ID:              AuthenticAMD
Model name:             AMD EPYC 9454 48-Core Processor
CPU family:             25
Model:                  17
Thread(s) per core:     2
Core(s) per socket:     48
Socket(s):              2
Stepping:               1
Frequency boost:        enabled
CPU max MHz:            3810.7910
CPU min MHz:            1500.0000
BogoMIPS:                5492.02
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                        clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
                        constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmpfperf rapl
                        pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe
                        popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy
                        abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext
                        perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_13 cdp_13
                        invpcid_single hw_pstate ssbd mba ibrs ibpb stibp vmmcall fsgsbase bmi1
                        avx2 smep bmi2 erms invpcid cqmq rdt_a avx512f avx512dq rdseed adx smap
                        avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
                        xsavec xgetbv1 xsaves cqmq_llc cqmq_occup_llc cqmq_mbmb_total cqmq_mbmb_local
                        avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd amd_ppin arat npt lbrv
                        svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists
                        pausefilter pfthreshold avic v_vmsave_vmload vgif v_spec_ctrl avx512vbmi
                        umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg
                        avx512_vpocntdq la57 rdpid overflow_recov succor smca fsrm flush_lld
                        AMD-V
Virtualization:          3 MiB (96 instances)
L1d cache:               3 MiB (96 instances)
L1i cache:
```

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Platform Notes (Continued)

L2 cache:	96 MiB (96 instances)
L3 cache:	512 MiB (16 instances)
NUMA node(s):	8
NUMA node0 CPU(s):	0-11,96-107
NUMA node1 CPU(s):	12-23,108-119
NUMA node2 CPU(s):	24-35,120-131
NUMA node3 CPU(s):	36-47,132-143
NUMA node4 CPU(s):	48-59,144-155
NUMA node5 CPU(s):	60-71,156-167
NUMA node6 CPU(s):	72-83,168-179
NUMA node7 CPU(s):	84-95,180-191
Vulnerability Itlb multihit:	Not affected
Vulnerability Llrf:	Not affected
Vulnerability Mds:	Not affected
Vulnerability Meltdown:	Not affected
Vulnerability Spec store bypass:	Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1:	Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2:	Mitigation; Retpolines, IBPB conditional, IBRS_FW, STIBP always-on, RSB filling
Vulnerability Srbds:	Not affected
Vulnerability Tsx async abort:	Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	32K	3M	8	Data	1	64	1	64
L1i	32K	3M	8	Instruction	1	64	1	64
L2	1M	96M	8	Unified	2	2048	1	64
L3	32M	512M	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 8 nodes (0-7)
node 0 cpus: 0-11,96-107
node 0 size: 193262 MB
node 0 free: 191658 MB
node 1 cpus: 12-23,108-119
node 1 size: 193519 MB
node 1 free: 193100 MB
node 2 cpus: 24-35,120-131
node 2 size: 193519 MB
node 2 free: 193118 MB
node 3 cpus: 36-47,132-143
node 3 size: 193485 MB
node 3 free: 193103 MB
node 4 cpus: 48-59,144-155
node 4 size: 193519 MB
node 4 free: 193174 MB
node 5 cpus: 60-71,156-167
node 5 size: 193519 MB
node 5 free: 193140 MB
node 6 cpus: 72-83,168-179
node 6 size: 193519 MB
node 6 free: 192962 MB
node 7 cpus: 84-95,180-191
node 7 size: 193321 MB
node 7 free: 192959 MB
node distances:
node 0 1 2 3 4 5 6 7
 0: 10 12 12 12 32 32 32 32

```

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Platform Notes (Continued)

```
1: 12 10 12 12 32 32 32 32
2: 12 12 10 12 32 32 32 32
3: 12 12 12 10 32 32 32 32
4: 32 32 32 32 10 12 12 12
5: 32 32 32 32 12 10 12 12
6: 32 32 32 32 12 12 10 12
7: 32 32 32 32 12 12 12 10
```

```
-----  
9. /proc/meminfo  
MemTotal: 1584813468 kB
```

```
-----  
10. who -r  
run-level 3 Nov 19 03:13
```

```
-----  
11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)  
Default Target Status  
multi-user running
```

```
-----  
12. Services, from systemctl list-unit-files  
STATE UNIT FILES  
enabled YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ haveged irqbalance  
issue-generator kbdsettings klog lvm2-monitor nsqd nvmefc-boot-connections postfix  
purge-kernels rollback rsyslog smartd sshd wickedd wickedd-auto4 wickedd-dhcp4  
wickedd-dhcp6 wickedd-nanny  
enabled-runtime systemd-remount-fs  
disabled autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait  
chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info  
firewalld gpm grub2-once haveged-switch-root hwloc-dump-hwdata ipmi ipmiev  
issue-add-ssh-keys kexec-load lummask man-db-create multipathd nfs nfs-blkmap ntp-wait  
ntpd nvmf-autoconnect rdisc rpcbind rpmconfigcheck rsyncd serial-getty@  
smartd_generate_opts snmpd snmptrapd svnservice systemd-boot-check-no-failures  
systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd  
indirect wickedd
```

```
-----  
13. Linux kernel boot-time arguments, from /proc/cmdline  
BOOT_IMAGE=/boot/vmlinuz-5.14.21-150400.22-default  
root=UUID=57007a51-1b8d-4733-95f6-d7d0c84f41c9  
splash=silent  
quiet  
security=apparmor  
mitigations=auto
```

```
-----  
14. cpupower frequency-info  
analyzing CPU 0:  
    current policy: frequency should be within 1.50 GHz and 2.75 GHz.  
    The governor "performance" may decide which speed to use  
    within this range.  
boost state support:  
    Supported: yes  
    Active: yes
```

```
-----  
15. sysctl  
kernel.numa_balancing
```

1

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Platform Notes (Continued)

```
kernel.randomize_va_space          0
vm.compaction_proactiveness       20
vm.dirty_background_bytes          0
vm.dirty_background_ratio         10
vm.dirty_bytes                    0
vm.dirty_expire_centisecs        3000
vm.dirty_ratio                   8
vm.dirty_writeback_centisecs     500
vm.dirtytime_expire_seconds      43200
vm.extfrag_threshold             500
vm.min_unmapped_ratio            1
vm.nr_hugepages                  0
vm.nr_hugepages_mempolicy        0
vm.nr_overcommit_hugepages       0
vm.swappiness                     1
vm.watermark_boost_factor        15000
vm.watermark_scale_factor        10
vm.zone_reclaim_mode              1

-----
16. /sys/kernel/mm/transparent_hugepage
    defrag           [always] defer defer+madvise madvise never
    enabled          [always] madvise never
    hpage_pmd_size  2097152
    shmem_enabled   always within_size advise [never] deny force

-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
    alloc_sleep_millisecs  60000
    defrag                1
    max_ptes_none         511
    max_ptes_shared       256
    max_ptes_swap         64
    pages_to_scan         4096
    scan_sleep_millisecs 10000

-----
18. OS release
    From /etc/*-release /etc/*-version
    os-release SUSE Linux Enterprise Server 15 SP4

-----
19. Disk information
    SPEC is set to: /home/cpu2017-1.1.9-amd-aocc400-znver4-A1.2
    Filesystem  Type  Size  Used  Avail Use% Mounted on
    /dev/nvme0n1p2  btrfs  3.5T  189G  3.4T   6%  /home

-----
20. /sys/devices/virtual/dmi/id
    Vendor:          Lenovo
    Product:         ThinkSystem SR675 V3 System Board
    Product Family:  ThinkSystem
    Serial:          None

-----
21. dmidecode
    Additional information from dmidecode 3.2 follows.  WARNING: Use caution when you interpret this section.
    The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
    determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
    "DMTF SMBIOS" standard.
```

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Platform Notes (Continued)

Memory:

24x SK Hynix HMCG94MEBRA123N 64 GB 2 rank 4800

22. BIOS

(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor: Lenovo
BIOS Version: QGE115H-3.10
BIOS Date: 09/07/2023
BIOS Revision: 3.10
Firmware Revision: 3.10

Compiler Version Notes

=====

C | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)

=====

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aoxx-compiler-4.0.0/bin

=====

C++ | 508.namd_r(base) 510.parest_r(base)

=====

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aoxx-compiler-4.0.0/bin

=====

C++, C | 511.povray_r(base) 526.blender_r(base)

=====

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aoxx-compiler-4.0.0/bin
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aoxx-compiler-4.0.0/bin

=====

C++, C, Fortran | 507.cactusBSSN_r(base)

=====

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aoxx-compiler-4.0.0/bin
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aoxx-compiler-4.0.0/bin
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu

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SPEC CPU®2017 Floating Point Rate Result

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Lenovo Global Technology

ThinkSystem SR675 V3
(2.75 GHz, AMD EPYC 9454)

SPECrate®2017_fp_base = 1060

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 9017

Test Date: Nov-2023

Test Sponsor: Lenovo Global Technology

Hardware Availability: Jul-2023

Tested by: Lenovo Global Technology

Software Availability: Nov-2022

Compiler Version Notes (Continued)

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

=====
Fortran | 503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
=====

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

=====
Fortran, C | 521.wrf_r(base) 527.cam4_r(base)
=====

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang



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Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactubSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
526.blender_r: -funsigned-char -DSPEC_LP64
527.cam4_r: -DSPEC_CASE_FLAG -DSPEC_LP64
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

```
-m64 -fno -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather -O3
-march=znver4 -fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-freemap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
-zopt -lamdlibm -lamdaloc -lflang
```

C++ benchmarks:

```
-m64 -fno -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -mllvm -unroll-threshold=100
-finline-aggressive -mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdaloc
-lflang
```

Fortran benchmarks:

```
-m64 -fno -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -Kieee -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm -lamdaloc
-lflang
```

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Software Availability: Nov-2022

Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

```
-m64 -fsto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-femap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
-zopt -Kieee -Mrecursive -funroll-loops -mllvm -lsr-in-nested-loop
-fepilog-vectorization-of-inductions -lamdlibm -lamdaloc -lflang
```

Benchmarks using both C and C++:

```
-m64 -fsto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-femap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
-zopt -mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000 -lamdlibm -lamdaloc -lflang
```

Benchmarks using Fortran, C, and C++:

```
-m64 -fsto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-femap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
-zopt -mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000 -Kieee -Mrecursive
-funroll-loops -mllvm -lsr-in-nested-loop
-fepilog-vectorization-of-inductions -lamdlibm -lamdaloc -lflang
```

Base Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

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Base Other Flags (Continued)

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECCpu2017-Flags-V1.2-Genoa-T.html>
<http://www.spec.org/cpu2017/flags/aocc400-flags.2023-09-13.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECCpu2017-Flags-V1.2-Genoa-T.xml>
<http://www.spec.org/cpu2017/flags/aocc400-flags.2023-09-13.xml>

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