



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

IEIT Systems Co., Ltd.

SPECSpeed®2017_fp_base = 428

NF5280A7 (AMD EPYC 9654)

SPECSpeed®2017_fp_peak = 450

CPU2017 License: 3358

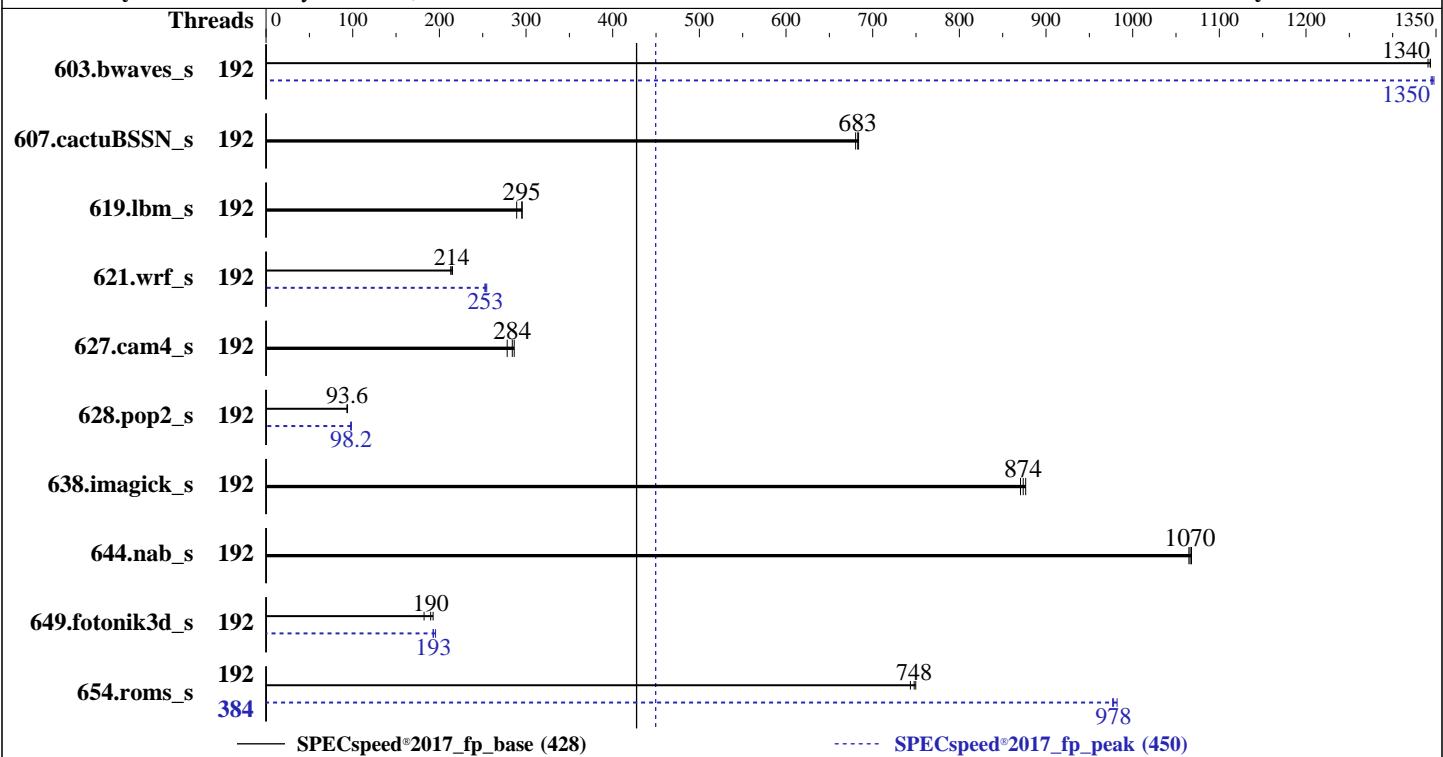
Test Date: Nov-2023

Test Sponsor: IEIT Systems Co., Ltd.

Hardware Availability: Sep-2023

Tested by: IEIT Systems Co., Ltd.

Software Availability: Nov-2022



— SPECSpeed®2017_fp_base (428)

----- SPECSpeed®2017_fp_peak (450)

Hardware

CPU Name: AMD EPYC 9654
 Max MHz: 3700
 Nominal: 2400
 Enabled: 192 cores, 2 chips, 2 threads/core
 Orderable: 1,2 chips
 Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 384 MB I+D on chip per chip, 32 MB shared / 8 cores
 Other: None
 Memory: 1536 GB (24 x 64 GB 2Rx4 PC5-4800B-R)
 Storage: 1 x 1.92 TB NVME SSD
 Other: None

Software

OS: Red Hat Enterprise Linux 9.0 (Plow)
 5.14.0-70.22.1.el9_0.x86_64
 Compiler: C/C++/Fortran: Version 4.0.0 of AOCC
 Parallel: Yes
 Firmware: Version 04.02.19 released Mar-2023
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other: None
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

IEIT Systems Co., Ltd.

SPECSpeed®2017_fp_base = 428

NF5280A7 (AMD EPYC 9654)

SPECSpeed®2017_fp_peak = 450

CPU2017 License: 3358

Test Date: Nov-2023

Test Sponsor: IEIT Systems Co., Ltd.

Hardware Availability: Sep-2023

Tested by: IEIT Systems Co., Ltd.

Software Availability: Nov-2022

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	192	43.9	1340	44.0	1340	43.9	1340	192	43.8	1350	43.9	1340	43.8	1350
607.cactuBSSN_s	192	24.4	683	24.5	680	24.4	684	192	24.4	683	24.5	680	24.4	684
619.lbm_s	192	17.7	296	18.1	289	17.8	295	192	17.7	296	18.1	289	17.8	295
621.wrf_s	192	62.1	213	61.5	215	61.7	214	192	52.4	252	52.2	253	51.9	255
627.cam4_s	192	31.2	284	31.0	286	31.9	278	192	31.2	284	31.0	286	31.9	278
628.pop2_s	192	127	93.6	127	93.6	126	94.0	192	122	97.7	121	98.2	121	98.4
638.imagick_s	192	16.5	874	16.5	877	16.6	871	192	16.5	874	16.5	877	16.6	871
644.nab_s	192	16.4	1070	16.4	1060	16.4	1070	192	16.4	1070	16.4	1060	16.4	1070
649.fotonik3d_s	192	48.0	190	47.3	193	50.0	182	192	47.3	193	46.6	196	47.2	193
654.roms_s	192	21.2	744	21.1	748	21.0	750	384	16.1	977	16.0	982	16.1	978

SPECSpeed®2017_fp_base = 428

SPECSpeed®2017_fp_peak = 450

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
 'numactl' was used to bind copies to the cores.
 See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
 'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
 numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
 To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
 To free node-local memory and avoid remote memory usage,
 'sysctl -w vm.zone_reclaim_mode=1' run as root.
 To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
 To disable address space layout randomization (ASLR) to reduce run-to-run
 variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
 'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
 'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
 To always enable THP for peak runs of:
 603.bwaves_s, 607.cactuBSSN_s, 619.lbm_s, 627.cam4_s, 628.pop2_s, 638.imagick_s, 644.nab_s, 649.fotonik3d_s:
 'echo madvise > /sys/kernel/mm/transparent_hugepage/enabled; echo always > /sys/kernel/mm/transparent_hugepage/defrag'
 run as root.
 To disable THP for peak runs of 621.wrf_s:

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

IEIT Systems Co., Ltd.

SPECspeed®2017_fp_base = 428

NF5280A7 (AMD EPYC 9654)

SPECspeed®2017_fp_peak = 450

CPU2017 License: 3358

Test Date: Nov-2023

Test Sponsor: IEIT Systems Co., Ltd.

Hardware Availability: Sep-2023

Tested by: IEIT Systems Co., Ltd.

Software Availability: Nov-2022

Operating System Notes (Continued)

```
'echo never > /sys/kernel/mm/transparent_hugepage/enabled; echo always > /sys/kernel/mm/transparent_hugepage/defrag'
run as root.
```

To enable THP only on request for peak runs of 654.roms_s:

```
'echo madvise > /sys/kernel/mm/transparent_hugepage/enabled; echo madvise > /sys/kernel/mm/transparent_hugepage/defrag'
run as root.
```

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
GOMP_CPU_AFFINITY = "0-383"
LD_LIBRARY_PATH = "/home/cpu2017/amd_speed_aocc400_genoa_B_lib/lib:"
LIBOMP_NUM_HIDDEN_HELPER_THREADS = "0"
MALLOC_CONF = "oversize_threshold:0,retain:true"
OMP_DYNAMIC = "false"
OMP_SCHEDULE = "static"
OMP_STACKSIZE = "128M"
OMP_THREAD_LIMIT = "384"
```

Environment variables set by runcpu during the 603.bwaves_s peak run:

```
GOMP_CPU_AFFINITY = "0-191"
```

Environment variables set by runcpu during the 621.wrf_s peak run:

```
GOMP_CPU_AFFINITY = "0-191"
```

Environment variables set by runcpu during the 628.pop2_s peak run:

```
GOMP_CPU_AFFINITY = "0-191"
```

Environment variables set by runcpu during the 649.fotonik3d_s peak run:

```
GOMP_CPU_AFFINITY = "0-191"
```

```
PGHPF_ZMEM = "yes"
```

Environment variables set by runcpu during the 654.roms_s peak run:

```
GOMP_CPU_AFFINITY = "0 192 1 193 2 194 3 195 4 196 5 197 6 198 7 199 8 200 9 201 10 202 11 203 12 204 13
205 14 206 15 207 16 208 17 209 18 210 19 211 20 212 21 213 22 214 23 215 24 216 25 217 26 218 27 219
28 220 29 221 30 222 31 223 32 224 33 225 34 226 35 227 36 228 37 229 38 230 39 231 40 232 41 233 42
234 43 235 44 236 45 237 46 238 47 239 48 240 49 241 50 242 51 243 52 244 53 245 54 246 55 247 56 248
57 249 58 250 59 251 60 252 61 253 62 254 63 255 64 256 65 257 66 258 67 259 68 260 69 261 70 262 71
263 72 264 73 265 74 266 75 267 76 268 77 269 78 270 79 271 80 272 81 273 82 274 83 275 84 276 85 277
86 278 87 279 88 280 89 281 90 282 91 283 92 284 93 285 94 286 95 287 96 288 97 289 98 290 99 291 100
292 101 293 102 294 103 295 104 296 105 297 106 298 107 299 108 300 109 301 110 302 111 303 112 304
113 305 114 306 115 307 116 308 117 309 118 310 119 311 120 312 121 313 122 314 123 315 124 316 125
317 126 318 127 319 128 320 129 321 130 322 131 323 132 324 133 325 134 326 135 327 136 328 137 329
138 330 139 331 140 332 141 333 142 334 143 335 144 336 145 337 146 338 147 339 148 340 149 341 150
342 151 343 152 344 153 345 154 346 155 347 156 348 157 349 158 350 159 351 160 352 161 353 162 354
163 355 164 356 165 357 166 358 167 359 168 360 169 361 170 362 171 363 172 364 173 365 174 366 175
367 176 368 177 369 178 370 179 371 180 372 181 373 182 374 183 375 184 376 185 377 186 378 187 379
188 380 189 381 190 382 191 383"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

IEIT Systems Co., Ltd.

SPECSpeed®2017_fp_base = 428

NF5280A7 (AMD EPYC 9654)

SPECSpeed®2017_fp_peak = 450

CPU2017 License: 3358

Test Date: Nov-2023

Test Sponsor: IEIT Systems Co., Ltd.

Hardware Availability: Sep-2023

Tested by: IEIT Systems Co., Ltd.

Software Availability: Nov-2022

General Notes (Continued)

is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:

SVM Mode = disable

DRAM Scrub time = disable

NUMA nodes per socket = NPS1

Determinism Slider = Power

cTDP = 400

Package Power Limit = 400

Sysinfo program /home/cpu2017/bin/sysinfo

Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Wed Nov 8 16:00:54 2023

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
 2. w
 3. Username
 4. ulimit -a
 5. sysinfo process ancestry
 6. /proc/cpuinfo
 7. lscpu
 8. numactl --hardware
 9. /proc/meminfo
 10. who -r
 11. Systemd service manager version: systemd 250 (250-6.el9_0)
 12. Services, from systemctl list-unit-files
 13. Linux kernel boot-time arguments, from /proc/cmdline
 14. cpupower frequency-info
 15. tuned-adm active
 16. sysctl
 17. /sys/kernel/mm/transparent_hugepage
 18. /sys/kernel/mm/transparent_hugepage/khugepaged
 19. OS release
 20. Disk information
 21. /sys/devices/virtual/dmi/id
 22. dmidecode
 23. BIOS
-

1. uname -a
Linux localhost 5.14.0-70.22.1.el9_0.x86_64 #1 SMP PREEMPT Tue Aug 2 10:02:12 EDT 2022 x86_64 x86_64 x86_64
GNU/Linux

2. w
16:00:54 up 4:26, 1 user, load average: 6.18, 7.22, 4.31
USER TTY LOGIN@ IDLE JCPU PCPU WHAT
root ttym1 12:36 3:23m 1.41s 0.32s /bin/bash ./amd_speed_aocc400_genoa_B1.sh

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

IEIT Systems Co., Ltd.

SPECSpeed®2017_fp_base = 428

NF5280A7 (AMD EPYC 9654)

SPECSpeed®2017_fp_peak = 450

CPU2017 License: 3358

Test Date: Nov-2023

Test Sponsor: IEIT Systems Co., Ltd.

Hardware Availability: Sep-2023

Tested by: IEIT Systems Co., Ltd.

Software Availability: Nov-2022

Platform Notes (Continued)

3. Username

From environment variable \$USER: root

4. ulimit -a

```
real-time non-blocking time (microseconds, -R) unlimited
core file size (blocks, -c) 0
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 6191061
max locked memory (kbytes, -l) 2097152
max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 6191061
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited
```

5. sysinfo process ancestry

```
/usr/lib/systemd/systemd --switched-root --system --deserialize 18
login -- root
-bash
python3 ./run_amd_speed_aocc400_genoa_B1.py
/bin/bash ./amd_speed_aocc400_genoa_B1.sh
runcpu --config amd_speed_aocc400_genoa_B1.cfg --tune all --reportable --iterations 3 fpspeed
runcpu --configfile amd_speed_aocc400_genoa_B1.cfg --tune all --reportable --iterations 3 --nopower
--runmode speed --tune base:peak --size test:train:refspeed fpspeed --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.012/templogs/preenv.fpspeed.012.0.log --lognum 012.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

6. /proc/cpuinfo

```
model name      : AMD EPYC 9654 96-Core Processor
vendor_id       : AuthenticAMD
cpu family     : 25
model          : 17
stepping        : 1
microcode       : 0xa101116
bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size        : 3584 4K pages
cpu cores       : 96
siblings         : 192
2 physical ids (chips)
384 processors (hardware threads)
physical id 0: core ids 0-95
physical id 1: core ids 0-95
physical id 0: apicids 0-191
physical id 1: apicids 256-447
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

IEIT Systems Co., Ltd.

SPECspeed®2017_fp_base = 428

NF5280A7 (AMD EPYC 9654)

SPECspeed®2017_fp_peak = 450

CPU2017 License: 3358

Test Date: Nov-2023

Test Sponsor: IEIT Systems Co., Ltd.

Hardware Availability: Sep-2023

Tested by: IEIT Systems Co., Ltd.

Software Availability: Nov-2022

Platform Notes (Continued)

```
From lscpu from util-linux 2.37.4:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Address sizes: 52 bits physical, 57 bits virtual
Byte Order: Little Endian
CPU(s): 384
On-line CPU(s) list: 0-383
Vendor ID: AuthenticAMD
BIOS Vendor ID: Advanced Micro Devices, Inc.
Model name: AMD EPYC 9654 96-Core Processor
BIOS Model name: AMD EPYC 9654 96-Core Processor
CPU family: 25
Model: 17
Thread(s) per core: 2
Core(s) per socket: 96
Socket(s): 2
Stepping: 1
Frequency boost: enabled
CPU max MHz: 3707.8120
CPU min MHz: 1500.0000
BogoMIPS: 4800.29
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
       clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
       constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmpfperf rapl
       pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe
       popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy
       abm sse4a misalignsse 3dnowprefetch osw ibs skinit wdt tce topoext
       perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_13 cdp_13
       invpcid_single hw_pstate ssbd mba ibrs ibpb stibp vmmcall fsgsbase bmi1
       avx2 smep bmi2 erms invpcid cq_m rdt_a avx512f avx512dq rdseed adx smap
       avx512fma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
       xsaves xgetbv1 xsaves cq_m_llc cq_m_occup_llc cq_m_mb_m_total cq_m_mb_m_local
       avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd amd_ppin arat npt lbrv
       svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists
       pausefilter pfthreshold avic v_vmsave_vmload vgif v_spec_ctrl avx512vbmi
       umip pkru ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg
       avx512_vpocntdq la57 rdpid overflow_recov succor smca fsrm flush_l1d
Virtualization: AMD-V
L1d cache: 6 MiB (192 instances)
L1i cache: 6 MiB (192 instances)
L2 cache: 192 MiB (192 instances)
L3 cache: 768 MiB (24 instances)
NUMA node(s): 2
NUMA node0 CPU(s): 0-95,192-287
NUMA node1 CPU(s): 96-191,288-383
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Retpolines, IBPB conditional, IBRS_FW, STIBP always-on, RSB
filling
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected
```

```
From lscpu --cache:
      NAME ONE-SIZE ALL-SIZE WAYS TYPE      LEVEL    SETS PHY-LINE COHERENCY-SIZE
      L1d      32K        6M     8 Data          1      64           1            64
```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

IEIT Systems Co., Ltd.

SPECspeed®2017_fp_base = 428

NF5280A7 (AMD EPYC 9654)

SPECspeed®2017_fp_peak = 450

CPU2017 License: 3358

Test Date: Nov-2023

Test Sponsor: IEIT Systems Co., Ltd.

Hardware Availability: Sep-2023

Tested by: IEIT Systems Co., Ltd.

Software Availability: Nov-2022

Platform Notes (Continued)

L1i	32K	6M	8	Instruction	1	64	1	64
L2	1M	192M	8	Unified	2	2048	1	64
L3	32M	768M	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0-95,192-287
node 0 size: 773854 MB
node 0 free: 770429 MB
node 1 cpus: 96-191,288-383
node 1 size: 774014 MB
node 1 free: 771478 MB
node distances:
node 0 1
0: 10 32
1: 32 10

9. /proc/meminfo

MemTotal: 1585018720 kB

10. who -r
run-level 3 Nov 8 11:34

11. Systemd service manager version: systemd 250 (250-6.el9_0)

Default Target Status
multi-user running

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	dbus-broker getty@ lvm2-monitor mdmonitor microcode nis-domainname rhsmcertd tuned udisks2
enabled-runtime	systemd-remount-fs
disabled	NetworkManager NetworkManager-dispatcher NetworkManager-wait-online audited blk-availability chrony-wait chronynd console-getty cpupower crond debug-shell firewalld irqbalance kdump kvm_stat man-db-restart-cache-update nftables rdisc rhsmd facts rpmdb-rebuild rsyslog selinux-autorelabel-mark serial-getty@ sshd sshd-keygen@ sssd systemd-boot-check-no-failures systemd-network-generator systemd-pstore systemd-sysext
indirect	sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo

13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=(hd0,gpt2)/vmlinuz-5.14.0-70.22.1.el9_0.x86_64
root=/dev/mapper/rhel-root
ro
resume=/dev/mapper/rhel-swap
rd.lvm.lv=rhel/root
rd.lvm.lv=rhel/swap

14. cpupower frequency-info

analyzing CPU 0:

current policy: frequency should be within 1.50 GHz and 2.40 GHz.
The governor "performance" may decide which speed to use
within this range.

boost state support:

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

IEIT Systems Co., Ltd.

SPECSpeed®2017_fp_base = 428

NF5280A7 (AMD EPYC 9654)

SPECSpeed®2017_fp_peak = 450

CPU2017 License: 3358

Test Date: Nov-2023

Test Sponsor: IEIT Systems Co., Ltd.

Hardware Availability: Sep-2023

Tested by: IEIT Systems Co., Ltd.

Software Availability: Nov-2022

Platform Notes (Continued)

```
Supported: yes
Active: yes
Boost States: 0
Total States: 3
Pstate-P0: 2400MHz
```

15. tuned-adm active

Current active profile: throughput-performance

16. sysctl

kernel.numa_balancing	1
kernel.randomize_va_space	0
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	10
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	8
vm.dirty_writeback_centisecs	500
vm.dirtytime_expire_seconds	43200
vm.extfrag_threshold	500
vm.min_unmapped_ratio	1
vm.nr_hugepages	0
vm.nr_hugepages_mempolicy	0
vm.nr_overcommit_hugepages	0
vm.swappiness	1
vm.watermark_boost_factor	15000
vm.watermark_scale_factor	10
vm.zone_reclaim_mode	1

17. /sys/kernel/mm/transparent_hugepage

```
defrag      [always] defer defer+madvise madvise never
enabled     [always] madvise never
hpage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force
```

18. /sys/kernel/mm/transparent_hugepage/khugepaged

alloc_sleep_millisecs	60000
defrag	1
max_ptes_none	511
max_ptes_shared	256
max_ptes_swap	64
pages_to_scan	4096
scan_sleep_millisecs	10000

19. OS release

```
From /etc/*-release /etc/*-version
os-release Red Hat Enterprise Linux 9.0 (Plow)
redhat-release Red Hat Enterprise Linux release 9.0 (Plow)
system-release Red Hat Enterprise Linux release 9.0 (Plow)
```

20. Disk information

SPEC is set to: /home/cpu2017

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
------------	------	------	------	-------	------	------------

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

IEIT Systems Co., Ltd.

SPECspeed®2017_fp_base = 428

NF5280A7 (AMD EPYC 9654)

SPECspeed®2017_fp_peak = 450

CPU2017 License: 3358

Test Date: Nov-2023

Test Sponsor: IEIT Systems Co., Ltd.

Hardware Availability: Sep-2023

Tested by: IEIT Systems Co., Ltd.

Software Availability: Nov-2022

Platform Notes (Continued)

```
/dev/mapper/rhel-home xfs 1.7T 20G 1.7T 2% /home
```

```
-----  
21. /sys/devices/virtual/dmi/id
```

```
Vendor: IEI  
Product: NF5280-A7-A0-R0-00  
Product Family: Not specified  
Serial: 000000000
```

```
-----  
22. dmidecode
```

```
Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section.  
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately  
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the  
"DMTF SMBIOS" standard.
```

```
Memory:
```

```
24x Samsung M321R8GA0BB0-CQKZJ 64 GB 2 rank 4800
```

```
-----  
23. BIOS
```

```
(This section combines info from /sys/devices and dmidecode.)
```

```
BIOS Vendor: American Megatrends International, LLC.  
BIOS Version: 04.02.19  
BIOS Date: 03/27/2023
```

Compiler Version Notes

```
=====  
C | 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base, peak)  
-----  
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on LLVM Mirror.Version.14.0.6)  
Target: x86_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin  
-----  
=====  
C++, C, Fortran | 607.cactusBSSN_s(base, peak)  
-----  
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on LLVM Mirror.Version.14.0.6)  
Target: x86_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin  
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on LLVM Mirror.Version.14.0.6)  
Target: x86_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin  
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on LLVM Mirror.Version.14.0.6)  
Target: x86_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin  
-----  
=====  
Fortran | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)  
-----  
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on LLVM Mirror.Version.14.0.6)  
Target: x86_64-unknown-linux-gnu
```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

IEIT Systems Co., Ltd.

SPECSpeed®2017_fp_base = 428

NF5280A7 (AMD EPYC 9654)

SPECSpeed®2017_fp_peak = 450

CPU2017 License: 3358

Test Date: Nov-2023

Test Sponsor: IEIT Systems Co., Ltd.

Hardware Availability: Sep-2023

Tested by: IEIT Systems Co., Ltd.

Software Availability: Nov-2022

Compiler Version Notes (Continued)

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

=====
Fortran, C | 621.wrf_s(base, peak) 627.cam4_s(base, peak) 628.pop2_s(base, peak)
=====

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on LLVM Mirror.Version.14.0.6)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on LLVM Mirror.Version.14.0.6)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

Base Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactusBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
627.cam4_s: -DSPEC_CASE_FLAG -DSPEC_LP64
628.pop2_s: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

IEIT Systems Co., Ltd.

SPECSpeed®2017_fp_base = 428

NF5280A7 (AMD EPYC 9654)

SPECSpeed®2017_fp_peak = 450

CPU2017 License: 3358

Test Date: Nov-2023

Test Sponsor: IEIT Systems Co., Ltd.

Hardware Availability: Sep-2023

Tested by: IEIT Systems Co., Ltd.

Software Availability: Nov-2022

Base Optimization Flags

C benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver4  
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=7  
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000  
-fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3  
-DSPEC_OPENMP -zopt -fopenmp=libomp -lomp -lamdlibm -lamdalloc  
-lflang
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP -O3 -march=znver4  
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -Mrecursive  
-funroll-loops -mllvm -lsr-in-nested-loop  
-mllvm -reduce-array-computations=3 -zopt -fopenmp=libomp -lomp  
-lamdlibm -lamdalloc -lflang
```

Benchmarks using both Fortran and C:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver4  
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=7  
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000  
-fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3  
-DSPEC_OPENMP -zopt -Mrecursive -funroll-loops  
-mllvm -lsr-in-nested-loop -fopenmp=libomp -lomp -lamdlibm -lamdalloc  
-lflang
```

Benchmarks using Fortran, C, and C++:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver4  
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=7  
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000  
-fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3  
-DSPEC_OPENMP -zopt -mllvm -unroll-threshold=100 -finline-aggressive  
-mllvm -loop-unswitch-threshold=200000 -Mrecursive -funroll-loops  
-mllvm -lsr-in-nested-loop -fopenmp=libomp -lomp -lamdlibm -lamdalloc  
-lflang
```



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

IEIT Systems Co., Ltd.

SPECSpeed®2017_fp_base = 428

NF5280A7 (AMD EPYC 9654)

SPECSpeed®2017_fp_peak = 450

CPU2017 License: 3358

Test Date: Nov-2023

Test Sponsor: IEIT Systems Co., Ltd.

Hardware Availability: Sep-2023

Tested by: IEIT Systems Co., Ltd.

Software Availability: Nov-2022

Base Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-return-type -Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-return-type -Wno-unused-command-line-argument

Peak Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

619.lbm_s: basepeak = yes

638.imagick_s: basepeak = yes

644.nab_s: basepeak = yes

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

IEIT Systems Co., Ltd.

SPECspeed®2017_fp_base = 428

NF5280A7 (AMD EPYC 9654)

SPECspeed®2017_fp_peak = 450

CPU2017 License: 3358

Test Date: Nov-2023

Test Sponsor: IEIT Systems Co., Ltd.

Hardware Availability: Sep-2023

Tested by: IEIT Systems Co., Ltd.

Software Availability: Nov-2022

Peak Optimization Flags (Continued)

Fortran benchmarks:

```
603.bwaves_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP  
-Ofast -march=znver4 -fveclib=AMDLIBM -ffast-math  
-fopenmp -Mrecursive -mllvm -reduce-array-computations=3  
-fvector-transform -fscalar-transform -fopenmp=libomp  
-lomp -lamdlibm -lamdalloc -lflang
```

```
649.fotonik3d_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP  
-Ofast -march=znver4 -fveclib=AMDLIBM -ffast-math  
-fopenmp -flto -Mrecursive  
-mllvm -reduce-array-computations=3 -zopt -fopenmp=libomp  
-lomp -lamdlibm -lamdalloc -lflang
```

654.roms_s: Same as 603.bwaves_s

Benchmarks using both Fortran and C:

```
621.wrf_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast  
-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp  
-flto -fstruct-layout=9 -mllvm -unroll-threshold=50  
-fremap-arrays -fstrip-mining  
-mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt  
-O3 -Mrecursive -funroll-loops -mllvm -lsr-in-nested-loop  
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

627.cam4_s: basepeak = yes

```
628.pop2_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast  
-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp  
-flto -fstruct-layout=9 -mllvm -unroll-threshold=50  
-fremap-arrays -fstrip-mining  
-mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt  
-Mrecursive -fvector-transform -fscalar-transform  
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

IEIT Systems Co., Ltd.

SPECSpeed®2017_fp_base = 428

NF5280A7 (AMD EPYC 9654)

SPECSpeed®2017_fp_peak = 450

CPU2017 License: 3358

Test Date: Nov-2023

Test Sponsor: IEIT Systems Co., Ltd.

Hardware Availability: Sep-2023

Tested by: IEIT Systems Co., Ltd.

Software Availability: Nov-2022

Peak Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: basepeak = yes

Peak Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-return-type -Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-return-type -Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc400-flags.html>

<http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-amd-V3.1.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc400-flags.xml>

<http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-amd-V3.1.xml>

SPEC CPU and SPECSpeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2023-11-08 16:00:54-0500.

Report generated on 2023-12-06 20:27:51 by CPU2017 PDF formatter v6716.

Originally published on 2023-12-06.