



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Esconet Technologies Ltd.

Hexadata HDR-RM2386212I Ver: ILX-002  
(Intel Xeon Gold 6338N, 2.20 GHz)

SPECrate®2017\_fp\_base = 372

SPECrate®2017\_fp\_peak = 381

CPU2017 License: 6523

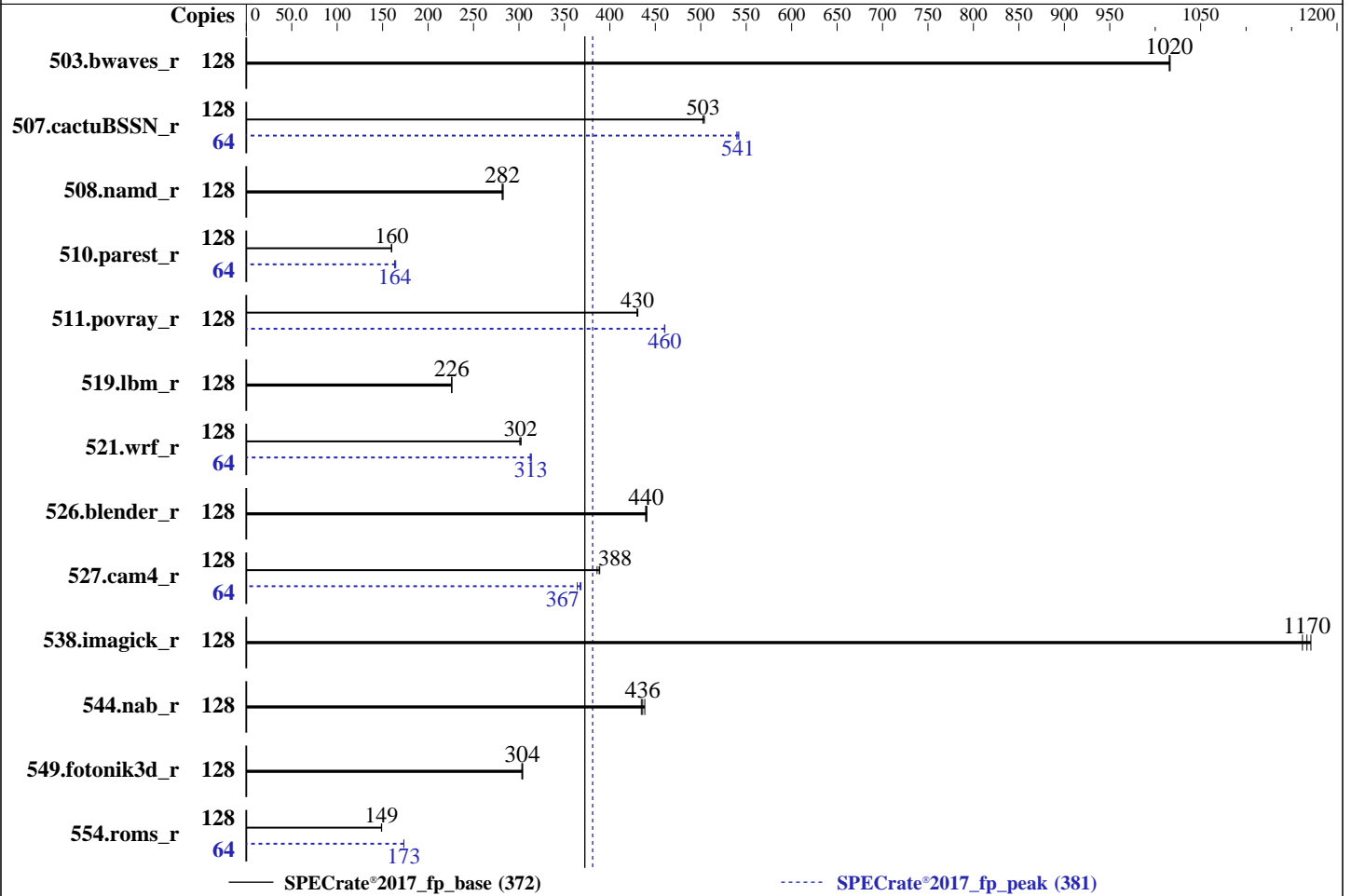
Test Sponsor: Esconet Technologies Ltd.

Tested by: Esconet Technologies Ltd.

Test Date: Dec-2023

Hardware Availability: May-2021

Software Availability: Dec-2023



### Hardware

CPU Name: Intel Xeon Gold 6338N  
 Max MHz: 3500  
 Nominal: 2200  
 Enabled: 64 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 1.25 MB I+D on chip per core  
 L3: 48 MB I+D on chip per chip  
 Other: None  
 Memory: 1 TB (16 x 64 GB 2Rx4 PC4-3200AA-R, running at 2666)  
 Storage: 960 GB SSD  
 Other: None

### Software

OS: CentOS Linux 8  
 4.18.0-348.7.1.el8\_5.x86\_64  
 Compiler: C/C++: Version 2023.2.3 of Intel oneAPI DPC++/C++ Compiler for Linux;  
 Fortran: Version 2023.2.3 of Intel Fortran Compiler for Linux;  
 Parallel: No  
 Firmware: Version F26 released May-2023  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: OS set to prefer performance at the cost of additional power usage.



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	128	1263	1020	<u>1264</u>	<u>1020</u>	1264	1020	128	1263	1020	<u>1264</u>	<u>1020</u>	1264	1020
507.cactuBSSN_r	128	321	504	323	502	<u>322</u>	<u>503</u>	64	150	542	150	540	<u>150</u>	<u>541</u>
508.namd_r	128	432	282	<u>432</u>	<u>282</u>	430	283	128	432	282	<u>432</u>	<u>282</u>	430	283
510.parest_r	128	2093	160	<u>2093</u>	<u>160</u>	2097	160	64	1018	164	1026	163	<u>1023</u>	<u>164</u>
511.povray_r	128	<u>695</u>	<u>430</u>	694	431	695	430	128	649	461	<u>649</u>	<u>460</u>	650	460
519.lbm_r	128	<u>597</u>	<u>226</u>	597	226	597	226	128	<u>597</u>	<u>226</u>	597	226	597	226
521.wrf_r	128	949	302	<u>950</u>	<u>302</u>	953	301	64	459	312	457	314	<u>458</u>	<u>313</u>
526.blender_r	128	444	439	<u>443</u>	<u>440</u>	442	441	128	444	439	<u>443</u>	<u>440</u>	442	441
527.cam4_r	128	580	386	576	389	<u>577</u>	<u>388</u>	64	<u>305</u>	<u>367</u>	307	364	304	368
538.imagick_r	128	<u>273</u>	<u>1170</u>	272	1170	274	1160	128	<u>273</u>	<u>1170</u>	272	1170	274	1160
544.nab_r	128	491	438	496	435	<u>494</u>	<u>436</u>	128	491	438	496	435	<u>494</u>	<u>436</u>
549.fotonik3d_r	128	1646	303	<u>1642</u>	<u>304</u>	1640	304	128	1646	303	<u>1642</u>	<u>304</u>	1640	304
554.roms_r	128	1366	149	1368	149	<u>1366</u>	<u>149</u>	64	586	173	587	173	<u>587</u>	<u>173</u>

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH = "/home/ub/cpul7/lib/intel64:/home/ub/cpul7/je5.0.1-64"  
MALLOC\_CONF = "retain:true"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>  
jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

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### General Notes (Continued)

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

### Platform Notes

BIOS settings: Default

Sysinfo program /home/ub/cpul7/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost.localdomain Thu Dec 28 09:37:11 2023

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 239 (239-51.e18\_5.2)
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. cpupower frequency-info
16. tuned-adm active
17. sysctl
18. /sys/kernel/mm/transparent\_hugepage
19. /sys/kernel/mm/transparent\_hugepage/khugepaged
20. OS release
21. Kernel self-reported vulnerability status, from /sys/devices/system/cpu/vulnerabilities
22. Disk information
23. /sys/devices/virtual/dmi/id
24. dmidecode
25. BIOS

-----  
1. uname -a  
Linux localhost.localdomain 4.18.0-348.7.1.e18\_5.x86\_64 #1 SMP Wed Dec 22 13:25:12 UTC 2021 x86\_64 x86\_64  
x86\_64 GNU/Linux  
-----

2. w  
09:37:11 up 8:56, 1 user, load average: 87.33, 117.88, 123.55  
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT

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### Platform Notes (Continued)

```
ub      tty1      -                00:43    8:53m  1.11s  0.01s  sh
reportable-ic2023.2.3-lin-core-avx512-rate-smt-on-20231121.sh
```

-----  
3. Username

From environment variable \$USER: ub

-----  
4. ulimit -a

```
core file size          (blocks, -c) 0
data seg size          (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size               (blocks, -f) unlimited
pending signals        (-i) 4125541
max locked memory      (kbytes, -l) 64
max memory size        (kbytes, -m) unlimited
open files             (-n) 1024
pipe size              (512 bytes, -p) 8
POSIX message queues   (bytes, -q) 819200
real-time priority     (-r) 0
stack size             (kbytes, -s) unlimited
cpu time               (seconds, -t) unlimited
max user processes     (-u) 4125541
virtual memory         (kbytes, -v) unlimited
file locks             (-x) unlimited
```

-----  
5. sysinfo process ancestry

```
/usr/lib/systemd/systemd --switched-root --system --deserialize 18
login -- ub
-bash
sh reportable-ic2023.2.3-lin-core-avx512-rate-smt-on-20231121.sh
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=128 -c
ic2023.2.3-lin-core-avx512-rate-20231121.cfg --define smt-on --define cores=64 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak -o all fprate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=128 --configfile
ic2023.2.3-lin-core-avx512-rate-20231121.cfg --define smt-on --define cores=64 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --nopower
--runmode rate --tune base:peak --size refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.002/templogs/preenv.fprate.002.0.log --lognum 002.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/ub/cpul7
```

-----  
6. /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) Gold 6338N CPU @ 2.20GHz
vendor_id      : GenuineIntel
cpu family     : 6
model          : 106
stepping      : 6
microcode     : 0xd0003a5
bugs           : spectre_v1 spectre_v2 spec_store_bypass swaps
cpu cores     : 32
siblings      : 64
2 physical ids (chips)
128 processors (hardware threads)
physical id 0: core ids 0-31
physical id 1: core ids 0-31
physical id 0: apicids 0-63
physical id 1: apicids 128-191
```

(Continued on next page)



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### Platform Notes (Continued)

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

#### 7. lscpu

From lscpu from util-linux 2.32.1:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Byte Order:             Little Endian
CPU(s):                 128
On-line CPU(s) list:   0-127
Thread(s) per core:    2
Core(s) per socket:    32
Socket(s):              2
NUMA node(s):          2
Vendor ID:              GenuineIntel
CPU family:             6
Model:                  106
Model name:             Intel(R) Xeon(R) Gold 6338N CPU @ 2.20GHz
Stepping:               6
CPU MHz:                2200.000
CPU max MHz:           3500.0000
CPU min MHz:           800.0000
BogoMIPS:               4400.00
Virtualization:        VT-x
L1d cache:              48K
L1i cache:              32K
L2 cache:               1280K
L3 cache:               49152K
NUMA node0 CPU(s):     0-31,64-95
NUMA node1 CPU(s):     32-63,96-127
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts
                        acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art
                        arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni
                        pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca
                        sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm
                        abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single intel_ppin ssbd mba ibrs ibpb
                        stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust
                        sgx bmi1 hle avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap
                        avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec
                        xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local split_lock_detect
                        wbnoinvd dtherm ida arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req avx512vbmi
                        umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme
                        avx512_vpoperndq la57 rdpid sgx_lc fsrm md_clear pconfig flush_l1d arch_capabilities

```

#### 8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 2 nodes (0-1)
node 0 cpus: 0-31,64-95
node 0 size: 515342 MB
node 0 free: 486548 MB
node 1 cpus: 32-63,96-127
node 1 size: 516079 MB
node 1 free: 490352 MB
node distances:
node  0  1
 0:  10  20
 1:  20  10

```

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### Platform Notes (Continued)

-----  
9. /proc/meminfo  
MemTotal: 1056175740 kB

-----  
10. who -r  
run-level 3 Dec 28 00:41

-----  
11. Systemd service manager version: systemd 239 (239-51.el8\_5.2)  
Default Target Status  
multi-user degraded

-----  
12. Failed units, from systemctl list-units --state=failed  
UNIT LOAD ACTIVE SUB DESCRIPTION  
\* sep5.service loaded failed failed systemd script to load sep5 driver at boot time  
\* systemd-sysctl.service loaded failed failed Apply Kernel Variables

-----  
13. Services, from systemctl list-unit-files  
STATE UNIT FILES  
enabled NetworkManager NetworkManager-dispatcher NetworkManager-wait-online auditd autovt@ crond  
firewalld getty@ import-state irqbalance kdump loadmodules lvm2-monitor mdmonitor microcode  
nis-domainname rsyslog selinux-autorelabel-mark sep5 sshd sssd syslog tuned udisks2  
disabled blk-availability console-getty cpupower debug-shell ebttables iprdump iprint iprupdate kvm\_stat  
man-db-restart-cache-update nftables rdisc serial-getty@ sshd-keygen@ systemd-resolved tcsd  
indirect sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo

-----  
14. Linux kernel boot-time arguments, from /proc/cmdline  
BOOT\_IMAGE=(hd1,gpt2)/vmlinuz-4.18.0-348.7.1.el8\_5.x86\_64  
root=/dev/mapper/cl-root  
ro  
crashkernel=auto  
resume=/dev/mapper/cl-swap  
rd.lvm.lv=cl/root  
rd.lvm.lv=cl/swap  
rhgb  
quiet

-----  
15. cpupower frequency-info  
analyzing CPU 0:  
current policy: frequency should be within 800 MHz and 3.50 GHz.  
The governor "performance" may decide which speed to use  
within this range.  
boost state support:  
Supported: yes  
Active: yes

-----  
16. tuned-adm active  
Current active profile: throughput-performance

-----  
17. sysctl  
kernel.numa\_balancing 1  
kernel.randomize\_va\_space 2

(Continued on next page)



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### Platform Notes (Continued)

```

vm.compaction_proactiveness      0
vm.dirty_background_bytes        0
vm.dirty_background_ratio        10
vm.dirty_bytes                   0
vm.dirty_expire_centisecs        3000
vm.dirty_ratio                   40
vm.dirty_writeback_centisecs     500
vm.dirtytime_expire_seconds      43200
vm.extfrag_threshold             500
vm.min_unmapped_ratio            1
vm.nr_hugepages                  0
vm.nr_hugepages_mempolicy        0
vm.nr_overcommit_hugepages       0
vm.swappiness                     10
vm.watermark_boost_factor        15000
vm.watermark_scale_factor        10
vm.zone_reclaim_mode             0

```

```

-----
18. /sys/kernel/mm/transparent_hugepage
defrag          always defer+madvice [madvice] never
enabled        [always] madvice never
hpage_pmd_size 2097152
shmem_enabled  always within_size advise [never] deny force

```

```

-----
19. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                 1
max_ptes_none          511
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs   10000

```

```

-----
20. OS release
From /etc/*-release /etc/*-version
os-release             CentOS Linux 8
redhat-release         CentOS Linux release 8.5.2111
system-release         CentOS Linux release 8.5.2111

```

```

-----
21. Kernel self-reported vulnerability status, from /sys/devices/system/cpu/vulnerabilities
itlb_multihit         Not affected
lltf                   Not affected
mds                    Not affected
meltdown              Not affected
spec_store_bypass     Mitigation: Speculative Store Bypass disabled via prctl and seccomp
spectre_v1             Mitigation: usercopy/swapgs barriers and __user pointer sanitization
spectre_v2            Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
srbds                  Not affected
tsx_async_abort       Not affected
For more information, see the Linux documentation on hardware vulnerabilities, for example
https://www.kernel.org/doc/html/latest/admin-guide/hw-vuln/index.html

```

```

-----
22. Disk information
SPEC is set to: /home/ub/cpu17
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/cl-home xfs   819G  160G  659G  20% /home

```

(Continued on next page)



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### Platform Notes (Continued)

```

-----
23. /sys/devices/virtual/dmi/id
Vendor:          ESCONET TECHNOLOGIES LTD.
Product:        HEXADATA
Product Family: Server

```

```

-----
24. dmidecode
Additional information from dmidecode 3.2 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
  16x Samsung M393A8G40AB2-CWE 64 GB 2 rank 3200, configured at 2666

```

```

-----
25. BIOS
(This section combines info from /sys/devices and dmidecode.)
  BIOS Vendor:      GIGABYTE
  BIOS Version:     F26
  BIOS Date:        05/29/2023
  BIOS Revision:    5.22

```

### Compiler Version Notes

```

=====
C          | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)
-----

```

```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
-----

```

```

=====
C++       | 508.namd_r(base, peak) 510.parest_r(base, peak)
-----

```

```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
-----

```

```

=====
C++, C    | 511.povray_r(base, peak) 526.blender_r(base, peak)
-----

```

```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
-----

```

```

=====
C++, C, Fortran | 507.cactuBSSN_r(base, peak)
-----

```

```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

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## Compiler Version Notes (Continued)

-----  
Fortran | 503.bwaves\_r(base, peak) 549.fotonik3d\_r(base, peak) 554.roms\_r(base, peak)  
-----

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.  
-----

-----  
Fortran, C | 521.wrf\_r(base, peak) 527.cam4\_r(base, peak)  
-----

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
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## Base Compiler Invocation

C benchmarks:  
icx

C++ benchmarks:  
icpx

Fortran benchmarks:  
ifx

Benchmarks using both Fortran and C:  
ifx icx

Benchmarks using both C and C++:  
icpx icx

Benchmarks using Fortran, C, and C++:  
icpx icx ifx

## Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64  
507.cactuBSSN\_r: -DSPEC\_LP64  
508.namd\_r: -DSPEC\_LP64  
510.parest\_r: -DSPEC\_LP64  
511.povray\_r: -DSPEC\_LP64  
519.lbm\_r: -DSPEC\_LP64

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# SPEC CPU®2017 Floating Point Rate Result

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Esconet Technologies Ltd.

SPECrate®2017\_fp\_base = 372

Hexadata HDR-RM2386212I Ver: ILX-002  
(Intel Xeon Gold 6338N, 2.20 GHz)

SPECrate®2017\_fp\_peak = 381

CPU2017 License: 6523

Test Date: Dec-2023

Test Sponsor: Esconet Technologies Ltd.

Hardware Availability: May-2021

Tested by: Esconet Technologies Ltd.

Software Availability: Dec-2023

## Base Portability Flags (Continued)

```
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64
```

## Base Optimization Flags

### C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

### C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

### Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

### Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -nostandard-realloc-lhs -align array32byte -auto
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

### Benchmarks using both C and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

### Benchmarks using Fortran, C, and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```



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**Test Date:** Dec-2023

**Hardware Availability:** May-2021

**Software Availability:** Dec-2023

## Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

519.lbm\_r: basepeak = yes

538.imagick\_r: basepeak = yes

544.nab\_r: basepeak = yes

C++ benchmarks:

508.namd\_r: basepeak = yes

510.parest\_r: -w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib

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## Peak Optimization Flags (Continued)

Fortran benchmarks:

503.bwaves\_r: basepeak = yes

549.fotonik3d\_r: basepeak = yes

```
554.roms_r: -w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -nostandard-realloc-lhs -align array32byte -auto
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both C and C++:

```
511.povray_r: -w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4 -Wno-implicit-int
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

526.blender\_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Hexadata-Default-Platform-Flags.html>

<http://www.spec.org/cpu2017/flags/Hexadata-Intel-ic2023p2-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Hexadata-Default-Platform-Flags.xml>

<http://www.spec.org/cpu2017/flags/Hexadata-Intel-ic2023p2-official-linux64.xml>



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