



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Cisco Systems

Cisco UCS C240 M7 (Intel Xeon Gold 6534,
3.90GHz)

SPECSpeed®2017_int_base = 14.0

SPECSpeed®2017_int_peak = 14.3

CPU2017 License: 9019

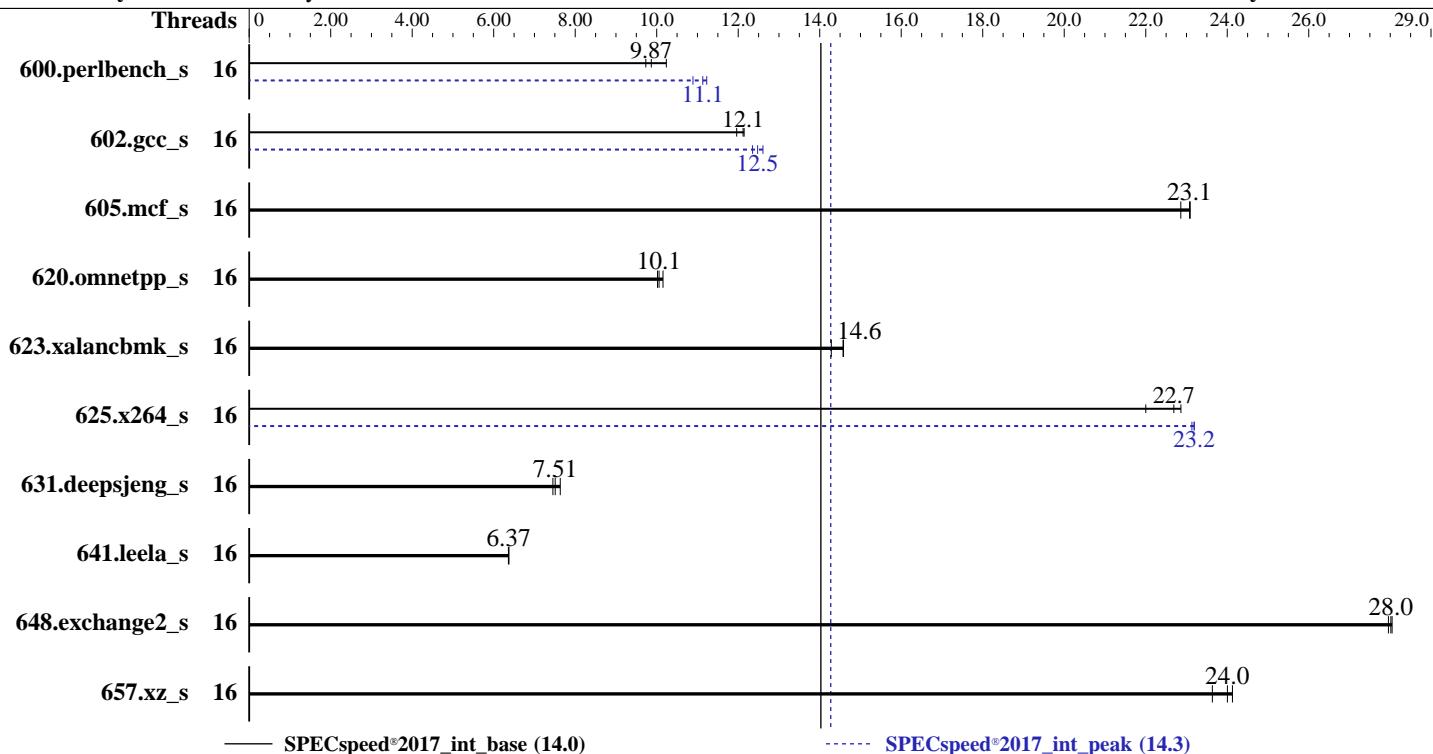
Test Date: Feb-2024

Test Sponsor: Cisco Systems

Hardware Availability: Feb-2024

Tested by: Cisco Systems

Software Availability: Jun-2023



Hardware

CPU Name: Intel Xeon Gold 6534
 Max MHz: 4200
 Nominal: 3900
 Enabled: 16 cores, 2 chips
 Orderable: 1,2 Chips
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 2 MB I+D on chip per core
 L3: 22.5 MB I+D on chip per chip
 Other: None
 Memory: 1 TB (16 x 64 GB 2Rx4 PC5-5600B-R,
 running at 4800)
 Storage: 1 x 960 GB M.2 SSD SATA
 Other: None

Software

OS: SUSE Linux Enterprise Server 15 SP5
 5.14.21-150500.53-default
 Compiler: C/C++: Version 2023.2.3 of Intel oneAPI DPC++/C++
 Compiler for Linux;
 Fortran: Version 2023.2.3 of Intel Fortran
 Compiler for Linux;
 Parallel: Yes
 Firmware: Version 4.3.3a released Jan-2024
 File System: btrfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other: jemalloc memory allocator V5.0.1
 Power Management: BIOS set to prefer power save
 with minimal impact on performance



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Results Table

Benchmark	Base								Peak							
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	16	173	10.2	<u>180</u>	<u>9.87</u>	182	9.73	16	<u>159</u>	<u>11.1</u>	158	11.2	<u>163</u>	<u>10.9</u>		
602.gcc_s	16	328	12.1	333	12.0	<u>329</u>	<u>12.1</u>	16	<u>319</u>	<u>12.5</u>	322	12.4	<u>316</u>	<u>12.6</u>		
605.mcf_s	16	<u>205</u>	<u>23.1</u>	204	23.1	207	22.9	16	<u>205</u>	<u>23.1</u>	204	23.1	<u>207</u>	<u>22.9</u>		
620.omnetpp_s	16	<u>162</u>	<u>10.1</u>	163	10.0	161	10.2	16	<u>162</u>	<u>10.1</u>	163	10.0	<u>161</u>	<u>10.2</u>		
623.xalancbmk_s	16	99.2	14.3	97.2	14.6	<u>97.2</u>	<u>14.6</u>	16	99.2	14.3	97.2	14.6	<u>97.2</u>	<u>14.6</u>		
625.x264_s	16	<u>77.7</u>	<u>22.7</u>	77.2	22.9	80.2	22.0	16	<u>76.3</u>	<u>23.1</u>	76.1	23.2	<u>76.1</u>	<u>23.2</u>		
631.deepsjeng_s	16	192	7.45	188	7.63	<u>191</u>	<u>7.51</u>	16	192	7.45	188	7.63	<u>191</u>	<u>7.51</u>		
641.leela_s	16	268	6.37	<u>268</u>	<u>6.37</u>	268	6.36	16	<u>268</u>	<u>6.37</u>	<u>268</u>	<u>6.37</u>	<u>268</u>	<u>6.36</u>		
648.exchange2_s	16	105	28.0	105	28.0	<u>105</u>	<u>28.0</u>	16	<u>105</u>	<u>28.0</u>	105	28.0	<u>105</u>	<u>28.0</u>		
657.xz_s	16	<u>258</u>	<u>24.0</u>	256	24.1	262	23.6	16	<u>258</u>	<u>24.0</u>	256	24.1	<u>262</u>	<u>23.6</u>		
SPECspeed®2017_int_base = 14.0				SPECspeed®2017_int_peak = 14.3												

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/lib/ia32:/home/cpu2017/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"
```

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Redhat Enterprise Linux 8.0

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5 sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>



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Platform Notes

BIOS Settings:
Intel Hyper-Threading Technology set to Disabled
Sub NUMA Clustering set to Disabled
LLC Dead Line set to Disabled
ADDC Sparing set to Disabled
Processor C6 Report set to Enabled
UPI Power Management set to Enabled
Enhanced CPU performance set to Auto

```
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Sat Feb 24 22:41:25 2024
```

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
 2. w
 3. Username
 4. ulimit -a
 5. sysinfo process ancestry
 6. /proc/cpuinfo
 7. lscpu
 8. numactl --hardware
 9. /proc/meminfo
 10. who -r
 11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
 12. Failed units, from systemctl list-units --state=failed
 13. Services, from systemctl list-unit-files
 14. Linux kernel boot-time arguments, from /proc/cmdline
 15. cpupower frequency-info
 16. sysctl
 17. /sys/kernel/mm/transparent_hugepage
 18. /sys/kernel/mm/transparent_hugepage/khugepaged
 19. OS release
 20. Disk information
 21. /sys/devices/virtual/dmi/id
 22. dmidecode
 23. BIOS
-

```
1. uname -a
Linux localhost 5.14.21-150500.53-default #1 SMP PREEMPT_DYNAMIC Wed May 10 07:56:26 UTC 2023 (b630043)
x86_64 x86_64 x86_64 GNU/Linux
```

```
2. w
22:41:25 up 2 min, 1 user, load average: 0.21, 0.19, 0.09
USER   TTY    FROM      LOGIN@    IDLE    JCPU    PCPU WHAT
root   ttyl     -       22:40   13.00s  2.45s  0.13s -bash
```

```
3. Username
From environment variable $USER: root
```

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Platform Notes (Continued)

```
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size            (kbytes, -d) unlimited
scheduling priority      (-e) 0
file size                (blocks, -f) unlimited
pending signals          (-i) 4127044
max locked memory        (kbytes, -l) 64
max memory size          (kbytes, -m) unlimited
open files               (-n) 1024
pipe size                (512 bytes, -p) 8
POSIX message queues     (bytes, -q) 819200
real-time priority        (-r) 0
stack size                (kbytes, -s) unlimited
cpu time                 (seconds, -t) unlimited
max user processes        (-u) 4127044
virtual memory             (kbytes, -v) unlimited
file locks               (-x) unlimited
```

```
-----  
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 30
login -- root
-bash
-bash
runcpu --define default-platform-flags -c ic2023.2.3-lin-sapphirerapids-speed-20231121.cfg --define cores=16
--tune all -o all --define drop_caches intspeed
runcpu --define default-platform-flags --configfile ic2023.2.3-lin-sapphirerapids-speed-20231121.cfg
--define cores=16 --tune all --output_format all --define drop_caches --nopower --runmode speed --tune
base:peak --size refspeed intspeed --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.023/templogs/preenv.intspeed.023.0.log --lognum 023.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

```
-----  
6. /proc/cpuinfo
model name      : INTEL(R) XEON(R) GOLD 6534
vendor_id       : GenuineIntel
cpu family      : 6
model          : 207
stepping        : 2
microcode       : 0x21000200
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrp_pbrsb
cpu cores       : 8
siblings        : 8
2 physical ids (chips)
16 processors (hardware threads)
physical id 0: core ids 0-7
physical id 1: core ids 0-7
physical id 0: apicids 0,2,4,6,8,10,12,14
physical id 1: apicids 128,130,132,134,136,138,140,142
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.
```

```
-----  
7. lscpu
```

```
From lscpu from util-linux 2.37.4:
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         46 bits physical, 57 bits virtual
```

(Continued on next page)



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Platform Notes (Continued)

```

Byte Order: Little Endian
CPU(s): 16
On-line CPU(s) list: 0-15
Vendor ID: GenuineIntel
Model name: INTEL(R) XEON(R) GOLD 6534
CPU family: 6
Model: 207
Thread(s) per core: 1
Core(s) per socket: 8
Socket(s): 2
Stepping: 2
CPU max MHz: 4200.0000
CPU min MHz: 800.0000
BogoMIPS: 7800.00
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
       clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
       lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
       nonstop_tsc cpuid aperf mperf tsc_known_freq pni pclmulqdq dtes64 monitor
       ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1
       sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
      lahf_lm abm 3dnowprefetch cpuid_fault epb cat_13 cat_12 cdp_13
       invpcid_single cdp_12 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow
       vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep
       bmi2 erms invpcid rtm cqmq rdt_a avx512f avx512dq rdseed adx smap
       avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
       xsaveopt xsavec xgetbv1 xsaves cqmq_llc cqmq_occup_llc cqmq_mbm_total
       cqmq_mbm_local avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts hwp
       hwp_act_window hwp_epp hwp_pkg_req avx512vbm1 umip pku ospke waitpkg
       avx512_vbm2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme
       avx512_vpopcntdq la57 rdpid bus_lock_detect cldemote movdiri movdir64b
       enqcmd fsrm md_clear serialize tsxlptrk pconfig arch_lbr avx512_fp16
       amx_tile flush_lld arch_capabilities
Virtualization: VT-x
L1d cache: 768 KiB (16 instances)
L1i cache: 512 KiB (16 instances)
L2 cache: 32 MiB (16 instances)
L3 cache: 45 MiB (2 instances)
NUMA node(s): 2
NUMA node0 CPU(s): 0-7
NUMA node1 CPU(s): 8-15
Vulnerability Itlb multihit: Not affected
Vulnerability Llft: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRSB-eIBRS SW sequence
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

```

```

From lscpu --cache:
  NAME ONE-SIZE ALL-SIZE WAYS TYPE      LEVEL    SETS PHY-LINE COHERENCY-SIZE
  L1d     48K    768K   12 Data          1      64        1           64
  L1i     32K    512K    8 Instruction   1      64        1           64
  L2      2M     32M   16 Unified        2     2048        1           64
  L3    22.5M   45M   15 Unified        3    24576        1           64

```

(Continued on next page)



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Platform Notes (Continued)

8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0-7
node 0 size: 515705 MB
node 0 free: 509098 MB
node 1 cpus: 8-15
node 1 size: 516086 MB
node 1 free: 515356 MB
node distances:
node 0 1
0: 10 21
1: 21 10

9. /proc/meminfo
MemTotal: 1056554896 kB

10. who -r
run-level 3 Feb 24 22:38

11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
Default Target Status
multi-user degraded

12. Failed units, from systemctl list-units --state=failed
UNIT LOAD ACTIVE SUB DESCRIPTION
* smartd.service loaded failed failed Self Monitoring and Reporting Technology (SMART) Daemon

13. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron display-manager getty@ irqbalance
issue-generator kbdsettings klog lvm2-monitor nsqd postfix purge-kernels rollback rsyslog
smartd sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime systemd-remount-fs
disabled autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info
firewalld gpm grub2-once haveged-switch-root ipmi ipmievfd issue-add-ssh-keys
kexec-load lunmask man-db-create multipathd nfs nfs-blkmap rpcbind rpmconfigcheck rsyncd
serial-getty@ smartd_generate_opts snmpd snmptrapd systemd-boot-check-no-failures
systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd udisks2
vncserver@
indirect wickedd

14. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default
root=UUID=1ae035da-2948-4dc2-9bb2-437ad0076efe
splash=silent
mitigations=auto
quiet
security=apparmor

15. cpupower frequency-info

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Platform Notes (Continued)

```
analyzing CPU 0:  
    current policy: frequency should be within 800 MHz and 4.20 GHz.  
        The governor "powersave" may decide which speed to use  
        within this range.
```

```
boost state support:  
    Supported: yes  
    Active: yes
```

```
-----  
16. sysctl  
    kernel.numa_balancing          1  
    kernel.randomize_va_space      2  
    vm.compaction_proactiveness   20  
    vm.dirty_background_bytes     0  
    vm.dirty_background_ratio     10  
    vm.dirty_bytes                0  
    vm.dirty_expire_centisecs    3000  
    vm.dirty_ratio                20  
    vm.dirty_writeback_centisecs  500  
    vm.dirtytime_expire_seconds   43200  
    vm.extfrag_threshold         500  
    vm.min_unmapped_ratio        1  
    vm.nr_hugepages              0  
    vm.nr_hugepages_mempolicy    0  
    vm.nr_overcommit_hugepages   0  
    vm.swappiness                 1  
    vm.watermark_boost_factor    15000  
    vm.watermark_scale_factor    10  
    vm.zone_reclaim_mode         0
```

```
-----  
17. /sys/kernel/mm/transparent_hugepage  
    defrag           [always] defer defer+madvise madvise never  
    enabled          [always] madvise never  
    hpage_pmd_size  2097152  
    shmem_enabled   always within_size advise [never] deny force
```

```
-----  
18. /sys/kernel/mm/transparent_hugepage/khugepaged  
    alloc_sleep_millisecs  60000  
    defrag               1  
    max_ptes_none       511  
    max_ptes_shared     256  
    max_ptes_swap       64  
    pages_to_scan       4096  
    scan_sleep_millisecs 10000
```

```
-----  
19. OS release  
    From /etc/*-release /etc/*-version  
    os-release SUSE Linux Enterprise Server 15 SP5
```

```
-----  
20. Disk information  
    SPEC is set to: /home/cpu2017  
    Filesystem  Type  Size  Used Avail Use% Mounted on  
    /dev/sda2    btrfs  222G  16G  205G  7%  /home
```

```
-----  
21. /sys/devices/virtual/dmi/id
```

(Continued on next page)



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Platform Notes (Continued)

Vendor: Cisco Systems Inc
Product: UCSC-C240-M7SX
Serial: WZP27100DJ4

22. dmidecode

Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

16x 0xCE00 M321R8GA0PB0-CWMCH 64 GB 2 rank 5600, configured at 4800

23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Cisco Systems, Inc.
BIOS Version: C240M7.4.3.3a.0.0118241337
BIOS Date: 01/18/2024
BIOS Revision: 5.32

Compiler Version Notes

=====
C | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak)
| 657.xz_s(base, peak)

=====
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====
C++ | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak)
| 641.leela_s(base, peak)

=====
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====
Fortran | 648.exchange2_s(base, peak)

=====
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

(Continued on next page)



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Base Compiler Invocation (Continued)

Fortran benchmarks:

ifx

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fopenmp
-DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

C++ benchmarks:

-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Fortran benchmarks:

-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Peak Compiler Invocation

C benchmarks:

icx

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

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Cisco Systems

Cisco UCS C240 M7 (Intel Xeon Gold 6534,
3.90GHz)

SPECspeed®2017_int_base = 14.0

SPECspeed®2017_int_peak = 14.3

CPU2017 License: 9019

Test Date: Feb-2024

Test Sponsor: Cisco Systems

Hardware Availability: Feb-2024

Tested by: Cisco Systems

Software Availability: Jun-2023

Peak Compiler Invocation (Continued)

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
600.perlbench_s: -w -m64 -std=c11 -Wl,-z,muldefs  
-fprofile-generate(pass 1)  
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)  
-flto -Ofast(pass 1) -xCORE-AVX512 -O3 -ffast-math  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-fiopenmp -DSPEC_OPENMP -fno-strict-overflow  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

```
602.gcc_s: -w -m64 -std=c11 -Wl,-z,muldefs  
-fprofile-generate(pass 1)  
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)  
-flto -Ofast(pass 1) -xCORE-AVX512 -O3 -ffast-math  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-fiopenmp -DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib  
-ljemalloc
```

605.mcf_s: basepeak = yes

```
625.x264_s: -w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP  
-fno-alias -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

657.xz_s: basepeak = yes

C++ benchmarks:

(Continued on next page)



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Hardware Availability: Feb-2024

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Software Availability: Jun-2023

Peak Optimization Flags (Continued)

620.omnetpp_s: basepeak = yes

623.xalancbmk_s: basepeak = yes

631.deepsjeng_s: basepeak = yes

641.leela_s: basepeak = yes

Fortran benchmarks:

648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Cisco-Platform-Settings-V1.0-EMR-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Cisco-Platform-Settings-V1.0-EMR-revA.xml>

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