



CINT2000 Result

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Hewlett-Packard Company
HP Integrity Superdome 64-way (1500 MHz Itanium 2)

SPECint_rate2000 = 904
SPECint_rate_base2000 = 904

SPEC license #: 3 Tested by: HP Richardson Test date: Aug-2003 Hardware Avail: Oct-2003 Software Avail: Sep-2003

2000	1500	1000	500	Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
				164.gzip	64	148	701	64	148	701
				175.vpr	64	134	778	64	134	778
				176.gcc	64	80.6	1013	64	80.6	1013
				181.mcf	64	106	1255	64	106	1255
				186.crafty	64	83.0	895	64	83.0	895
				197.parser	64	188	710	64	188	710
				252.eon	64	87.1	1108	64	87.1	1108
				253.perlbnk	64	156	859	64	156	859
				254.gap	64	128	636	64	128	636
				255.vortex	64	105	1349	64	105	1349
				256.bzip2	64	126	884	64	126	884
				300.twolf	64	238	936	64	238	936

Hardware

CPU: Intel Itanium 2
CPU MHz: 1500
FPU: Integrated
CPU(s) enabled: 64 cores, 64 chips, 1 core/chip
CPU(s) orderable: 6 to 64 by 2
Parallel: no
Primary Cache: 16KBI + 16KBD (on chip) per CPU
Secondary Cache: 256KB (on chip) per CPU
L3 Cache: 6.0MB (on chip) per CPU
Other Cache: N/A
Memory: 256GB (512 * 512MB DIMMs)
Disk Subsystem: 2x36GB 10K rpm SCSI (striped)
Other Hardware: N/A

Software

Operating System: HPUX11i-TCOE B.11.23
Compiler: HP C/ANSI C Compiler B.11.23
HP aC++ Compiler B.11.23
File System: hfs
System State: Multi-user

Notes/Tuning Information

Portability Flags

```
176.gcc : -DHOST_WORDS_BIG_ENDIAN
186.crafty : -DHP
252.eon : -DFMAX_IS_DOUBLE
253.perlbnk : -DSPEC_CPU2000_HP
254.gap : -DSPEC_CPU2000_HP -DSYS_IS_USG -DSYS_HAS_IOCTL_PROTO
          -DSYS_HAS_TIME_PROTO -DSYS_HAS_CALLOC_PROTO
```

Base Flags

```
all : +Oprofile=collect:all/+Oprofile=use
C : +Ofaster -exec +Odatalayout
C++ : +Ofaster +inline_level 2 -minshared
```

Peak Flags

same as baseline (basepeak=true set globally)

Kernel Tunables:

```
dbc_max_pct=20
dbc_min_pct=20
maxdsiz=3221225472
```



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Notes/Tuning Information (Continued)

```
maxssiz=401604608
maxdsiz_64bit=4396972769279
maxssiz_64bit=1073741824
vps_pagesize=4096
vps_ceiling=16384
```

Notes:

System was configured with 1/2 of memory interleaved and
1/2 of memory local to each cell

System configured as a single partition with 16 cells and
4 processors per cell

Processes were assigned to localities using the
HP-UX mpsched utility

Filesystem used for SPEC runs mounted behind,fs_async