



CINT2000 Result

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SGI

SGI Altix 350 (1400MHz/1.5MB, Itanium 2)

SPECint_rate2000 = --

SPECint_rate_base2000 = 173

SPEC license #: 4 Tested by: SGI Test date: Jan-2004 Hardware Avail: Jan-2004 Software Avail: Dec-2003

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.zip	16	162	160			
175.vpr	16	205	127			
176.gcc	16	110	185			
181.mcf	16	265	126			
186.crafty	16	90.7	205			
197.parser	16	254	132			
252.eon	16	92.4	261			
253.perlbnk	16	174	192			
254.gap	16	146	140			
255.vortex	16	125	282			
256.bzip2	16	189	147			
300.twolf	16	292	191			

Hardware

CPU: Intel Itanium 2
CPU MHz: 1400
FPU: Integrated
CPU(s) enabled: 16 cores, 16 chips, 1 core/chip
CPU(s) orderable: 2-16
Parallel: No
Primary Cache: 16KBI + 16KBD (on chip) per CPU
Secondary Cache: 256KB (on chip) per CPU
L3 Cache: 1.5MB (on chip) per CPU
Other Cache: N/A
Memory: 32 GB (8*512MB PC2100 DIMMS per 2cpu module)
Disk Subsystem: 1 x 36 GB SCSI (Seagate Cheetah 15k rpm)
Other Hardware: None

Software

Operating System: SGI ProPack(TM) v2.3
Compiler: Intel(R) C++ Compiler for Linux 8.0 (Build 20031017)
MicroQuill SmartHeap Library 7.01 (www.microquill.com)
File System: xfs
System State: Single-user

Notes/Tuning Information

+FDO: PASS1=-prof_gen PASS2=-prof_use

Baseline optimization flags:

C programs: -fast -auto_ilp32 +FDO
C++ programs: -fast -ansi_alias +FDO
Extra Libraries: libsmartheap64.a

Portability Flags:

176.gcc: -DSPEC_CPU2000_LP64 -Dalloca=_builtin_alloca -D_LIBC
186.crafty: -DLINUX_i386
252.eon: -DSPEC_CPU2000_LP64 -DHAS_ERRLIST -DFMAX_IS_DOUBLE
253.perlbnk: -DSPEC_CPU2000_LP64 -DSPEC_CPU2000_NEED_BOOL
-DSPEC_CPU2000_LINUX_IA64 -DSPEC_CPU2000_GLIBC22
254.gap: -DSPEC_CPU2000_LP64 -DSYS_HAS_CALLOC_PROTO -DSYS_IS_USG
-DSYS_HAS_IOCTL_PROTO -DSYS_HAS_TIME_PROTO -DSYS_HAS_SIGNAL_PROTO
255.vortex: -DSPEC_CPU2000_LP64

Processes were bound to CPUs using dplace.