



# CINT2000 Result

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## SGI

SGI Altix 3700 Bx2 (1600MHz 9M L3, Itanium 2)

SPECint\_rate2000 = --

SPECint\_rate\_base2000 = 66.5

SPEC license #: 4 | Tested by: SGI | Test date: Oct-2004 | Hardware Avail: Nov-2004 | Software Avail: Nov-2004

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.zip	4	132	49.3			
175.vpr	4	117	55.4			
176.gcc	4	69.3	73.7			
181.mcf	4	68.1	123			
186.crafty	4	78.0	59.5			
197.parser	4	173	48.4			
252.eon	4	65.4	92.2			
253.perlbnk	4	134	62.2			
254.gap	4	117	43.6			
255.vortex	4	85.4	103			
256.bzip2	4	121	57.7			
300.twolf	4	198	70.2			

### Hardware

CPU: Intel Itanium 2  
 CPU MHz: 1600  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 4 chips, 1 core/chip  
 CPU(s) orderable: 16-256  
 Parallel: No  
 Primary Cache: 16KBI + 16KBD on chip, per core  
 Secondary Cache: 256KB (I+D) on chip, per core  
 L3 Cache: 9.0MB (I+D) on chip, per core  
 Other Cache: N/A  
 Memory: 16 GB (1GB PC2700 DIMMS)  
 Disk Subsystem: 16 x 73 GB FibreChannel (Seagate Cheetah 15k rpm)  
 Other Hardware: None

### Software

Operating System: SGI ProPack(TM) v3.0 Service Pack 1  
 Compiler: Intel(R) C++ Compiler for Linux 8.1 (Build 20041021)  
 MicroQuill SmartHeap Library 7.01 (www.microquill.com)  
 File System: xfs  
 System State: Single-user

## Notes/Tuning Information

+FDO: PASS1=-prof\_gen PASS2=-prof\_use

Baseline optimization flags:

C programs: -fast -auto\_ilp32 +FDO  
 C++ programs: -fast -ansi\_alias +FDO  
 Extra Libraries: libsmartheap64.a

Portability Flags:

176.gcc: -DSPEC\_CPU2000\_LP64 -Dalloca=\_builtin\_alloca  
 186.crafty: -DLINUX\_i386  
 252.eon: -DSPEC\_CPU2000\_LP64 -DHAS\_ERRLIST -DFMAX\_IS\_DOUBLE  
 253.perlbnk: -DSPEC\_CPU2000\_LP64 -DSPEC\_CPU2000\_NEED\_BOOL  
 -DSPEC\_CPU2000\_LINUX\_IA64 -DSPEC\_CPU2000\_GLIBC22  
 254.gap: -DSPEC\_CPU2000\_LP64 -DSYS\_HAS\_CALLOC\_PROTO -DSYS\_IS\_USG  
 -DSYS\_HAS\_IOCTL\_PROTO -DSYS\_HAS\_TIME\_PROTO -DSYS\_HAS\_SIGNAL\_PROTO  
 255.vortex: -DSPEC\_CPU2000\_LP64 srcalt=closed\_files

Extra CPUs were disabled at firmware.  
 Processes were bound to CPUs using dplace.