



CFP2000 Result

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HCL INFOSYSTEMS LTD

Infiniti Global Line 4700HW-2 (3.66GHz Xeon processor with 1MB L2 cache)

SPECfp2000 = 1405

SPECfp_base2000 = 1379

SPEC license #: 2828 Tested by: HCL INFOSYSTEMS LTD Test date: May-2006 Hardware Avail: Apr-2006 Software Avail: Jan-2006

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	
168.wupwise	1600	67.1	2383	67.1	2383	
171.swim	3100	180	1725	180	1725	
172.mgrid	1800	130	1382	130	1382	
173.applu	2100	154	1367	154	1367	
177.mesa	1400	78.4	1786	77.4	1809	
178.galgel	2900	150	1930	150	1930	
179.art	2600	143	1813	143	1813	
183.earthquake	1300	99.6	1305	77.9	1670	
187.facerec	1900	150	1267	150	1267	
188.amp	2200	281	784	281	784	
189.lucas	2000	163	1229	163	1229	
191.fma3d	2100	154	1368	154	1368	
200.sixtrack	1100	153	718	153	718	
301.apsi	2600	219	1186	219	1186	

Hardware

CPU: Intel Xeon processor, 800MHz system bus
CPU MHz: 3657
FPU: Integrated
CPU(s) enabled: 4 cores, 4 chips, 1 core/chip (Hyper-Threading Technology disabled)
CPU(s) orderable: 1-4
Parallel: No
Primary Cache: 12K(I) micro-ops + 16KB(D) on chip
Secondary Cache: 1024KB (I+D) on chip
L3 Cache: None
Other Cache: None
Memory: 16 GB (16x1024MB DDR2-400)
Disk Subsystem: 73GB Seagate SCSI disk
Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux 4 AS U2, 2.6.9-22.ELsmp EM64T
Compiler: Intel(R) Fortran Compiler for EM64T, V9.0 (Build 20050430)
Intel(R) C Compiler for EM64T, V9.0 (Build 20050430)
File System: ext3
System State: Multi-user, runlevel 3

Notes/Tuning Information

Hyper-Threading Technology Disabled in BIOS
Hardware Prefetcher Enabled in BIOS
Adjacent Cache Line Prefetch Enabled in BIOS

Feedback optimization:
+FDO: PASS1 = -prof_gen PASS2 = -prof_use

FP Portability Flags:
178.galgel: -FI

FP Baseline optimization flags:
C programs: -fast -auto_ilp32 -march=pentium4 +FDO
Fortran programs: -fast +FDO
Fortran-77 programs: -fast -Zp8 +FDO

FP Peak optimization flags:
168.wupwise: basepeak=yes



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Notes/Tuning Information (Continued)

```
171.swim: basepeak=yes
72.mgrid: basepeak=yes
173.applu: basepeak=yes
177.mesa: -fast -auto_ilp32 +FDO
178.galgel: basepeak=yes
179.art: basepeak=yes
183.quake: -fast -auto_ilp32 +FDO
187.facerec: basepeak=yes
188.ammmp: basepeak=yes
189.lucas: basepeak=yes
191.fma3d: basepeak=yes
200.sixtrack: basepeak=yes
301.apsi: basepeak=yes
```