



CINT2000 Result

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Hewlett-Packard Company
ProLiant BL460c (3.0GHz, Intel Xeon processor 5160)

SPECint_rate2000 = 122
SPECint_rate_base2000 = 121

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Jun-2006 Hardware Avail: Jun-2006 Software Avail: May-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	4	78.1	83.2	4	78.1	83.1
175.vpr	4	77.5	83.8	4	74.0	87.8
176.gcc	4	41.5	123	4	41.5	123
181.mcf	4	82.5	101	4	82.5	101
186.crafty	4	36.5	127	4	36.5	127
197.parser	4	82.5	101	4	81.9	102
252.eon	4	33.2	182	4	33.2	181
253.perlbnk	4	52.7	158	4	52.6	159
254.gap	4	45.1	113	4	44.7	114
255.vortex	4	45.3	195	4	45.2	195
256.bzip2	4	80.6	86.3	4	80.6	86.3
300.twolf	4	87.4	159	4	87.4	159

Hardware

CPU: Intel Xeon processor 5160 (3.0GHz, 4MB L2 shared, 1333MHz bus)
CPU MHz: 3000
FPU: Integrated
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
CPU(s) orderable: 1,2 chips
Parallel: No
Primary Cache: 32KBI + 32KBD (on chip) per core
Secondary Cache: 4096KB(I+D) (on chip) shared
L3 Cache: N/A
Other Cache: N/A
Memory: 8x1024MB PC2-5300F
Disk Subsystem: 2x36GB 10K SAS
Other Hardware:

Software

Operating System: Windows Server 2003 Enterprise SP1
Compiler: Intel C++ Compiler for 32-bit applications, (Version 9.1 Build 20060323Z)
Microsoft Visual Studio .NET 7.0.9466 (for libraries)
MicroQuill Smartheap Library 7.0
File System: NTFS
System State: Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Base tuning for C programs: -fast +FDO ONESTEP=yes shlw32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO ONESTEP=yes
Portability flags:
 176.gcc: -Dalloca=_alloca /F10000000
 186.crafty: -DNT_i386
 253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
 254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip: -fast +FDO ONESTEP=yes
175.vpr: -fast +FDO ONESTEP=yes
176.gcc: basepeak=1
181.mcf: -fast +FDO ONESTEP=yes shlw32M.lib
186.crafty: -fast -Oa +FDO ONESTEP=yes shlw32M.lib
197.parser: -fast +FDO ONESTEP=yes
252.eon: -fast +FDO
253.perlbnk: -fast -Oa +FDO ONESTEP=yes shlw32M.lib
254.gap: -fast +FDO ONESTEP=yes
255.vortex: -fast +FDO ONESTEP=yes shlw32M.lib
256.bzip2: basepeak=1
```



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Notes/Tuning Information (Continued)

300.twolf: -fast +FDO shlw32M.lib
BIOS Configuration Notes
Power Regulator set to Static High Performance Mode
Adjacent Sector Prefetch disabled