



CINT2000 Result

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HITACHI

HITACHI BladeSymphony BS1000 (Dual Core Itanium(R) 2 processor 9050, 1.60GHz/24MB, FSB 400MHz)

SPECint_rate2000 = --

SPECint_rate_base2000 = 259

SPEC license #: 872 | Tested by: HITACHI | Test date: Jul-2006 | Hardware Avail: Aug-2006 | Software Avail: Jun-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	16	139	187			
175.vpr	16	121	214			
176.gcc	16	65.6	311			
181.mcf	16	75.0	446			
186.crafty	16	79.3	234			
197.parser	16	174	192			
252.eon	16	58.7	411			
253.perlbmk	16	130	256			
254.gap	16	154	132			
255.vortex	16	86.4	408			
256.bzip2	16	119	233			
300.twolf	16	207	270			

Hardware

CPU: Dual-Core Itanium(R) 2 Processor 9050
 CPU MHz: 1600
 FPU: Integrated
 CPU(s) enabled: 16 cores, 8 chips, 2 cores/chip (Hyper-Threading Technology disabled)
 CPU(s) orderable: 2,4,8,16
 Parallel: No
 Primary Cache: 16KBI + 16KBD on chip, per core
 Secondary Cache: 1MBI + 256KBD on chip, per core
 L3 Cache: 12MB(I+D) on chip, per core
 Other Cache: N/A
 Memory: 32GB (1GB DIMM x 32)
 Disk Subsystem: 1 x 73GB SCSI HDD
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux AS 4 update 3
 Compiler: Intel(R) C++ Compiler for Linux 9.1 (Build 20060523)
 File System: ext3
 System State: Multi-user run level 3

Notes/Tuning Information

Base tuning flags:

C : -fast -auto_ilp32 +FDO
 C++ : -fast -auto_ilp32 -IPF_fp_relaxed +FDO

+FDO: PASS1=-prof_gen PASS2=-prof_use

Portability flags:

176.gcc: -DSPEC_CPU2000_LP64 -Dalloca=_alloca
 186.crafty: -DLINUX_i386
 252.eon: -DSPEC_CPU2000_LP64 -DHAS_ERRLIST
 253.perlbmk: -DSPEC_CPU2000_LP64 -DSPEC_CPU2000_NEED_BOOL
 -DSPEC_CPU2000_LINUX_IA64 -DSPEC_CPU2000_GLIBC22
 254.gap: -DSPEC_CPU2000_LP64 -DSYS_HAS_CALLOC_PROTO -DSYS_IS_USG
 -DSYS_HAS_IOCTL_PROTO -DSYS_HAS_TIME_PROTO -DSYS_HAS_SIGNAL_PROTO
 255.vortex: -DSPEC_CPU2000_LP64

Hyper-Threading Technology disabled by firmware.
 Processes were bound to CPUs by using HITACHI bindlp command.
 ccNUMA configuration was selected by firmware.