



CINT2000 Result

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Fujitsu Siemens Computers

PRIMERGY TX600 S3, Intel Xeon processor 7140M, 3.40 GHz

SPECint_rate2000 = 162

SPECint_rate_base2000 = 162

SPEC license #: 22 | Tested by: Fujitsu Siemens Computers | Test date: Jul-2006 | Hardware Avail: Aug-2006 | Software Avail: May-2006

| Benchmark | Base Copies | Base Runtime | Base Ratio | Copies | Runtime | Ratio |
|-------------|-------------|--------------|------------|--------|---------|-------|
| 164.zip | 16 | 198 | 131 | 16 | 198 | 131 |
| 175.vpr | 16 | 237 | 110 | 16 | 237 | 110 |
| 176.gcc | 16 | 96.7 | 211 | 16 | 96.7 | 211 |
| 181.mcf | 16 | 201 | 166 | 16 | 201 | 166 |
| 186.crafy | 16 | 162 | 115 | 16 | 162 | 115 |
| 197.parser | 16 | 206 | 162 | 16 | 206 | 162 |
| 252.eon | 16 | 105 | 230 | 16 | 105 | 230 |
| 253.perlbnk | 16 | 188 | 178 | 16 | 188 | 178 |
| 254.gap | 16 | 159 | 129 | 16 | 159 | 129 |
| 255.vortex | 16 | 138 | 256 | 16 | 138 | 256 |
| 256.bzip2 | 16 | 178 | 156 | 16 | 178 | 156 |
| 300.twolf | 16 | 332 | 168 | 16 | 332 | 168 |

Hardware

CPU: Intel Xeon processor 7140M (3.40 GHz, 2x1MB L2, 16MB L3, 800 MHz system bus)
CPU MHz: 3400
FPU: Integrated
CPU(s) enabled: 8 cores, 4 chips, 2 cores/chip (Hyper-Threading Technology enabled)
CPU(s) orderable: 1,2,4
Parallel: No
Primary Cache: 12k micro-ops I + 16KBD on chip, per core
Secondary Cache: 1024KB(I+D) on chip, per core
L3 Cache: 16MB on chip, per chip
Other Cache: N/A
Memory: 16x1GB DDRII-RAM PC2-3200R (CAS 3-3-3)
Disk Subsystem: Fujitsu MAS3367NC (SCSI, 15krpm, 36GB)
Other Hardware: none

Software

Operating System: Windows Server 2003 Enterprise Edition + SP1 (32-bit)
Compiler: Intel C++ Compiler 9.1
Build 20060323Z (for 32-bit applications),
Microsoft Visual Studio .Net 2003
7.1.3088 (for libraries),
SmartHeap Library Version 8.0
from <http://www.microquill.com>
File System: NTFS
System State: Default

Notes/Tuning Information

GENERAL

+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use

Portability flags

176.gcc: -Dalloca=_alloca /F10000000
186.crafy: -DNT_i386
253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Base tuning flags

for C programs: -fast +FDO shlw32m.lib
for C++ programs: -fast -Qcxx_features +FDO shlw32m.lib

Peak tuning flags

same as baseline (basepeak=true set globally)

The system bus runs at 800 MHz

This result was measured with 32-bit binaries using the 32-bit version of the operating system.



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Notes/Tuning Information (Continued)

BIOS Configuration:

Hardware Prefetch = Enable, Adjacent Sector Prefetch = Enable

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