



# CFP2000 Result

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## Fujitsu Siemens Computers

PRIMERGY RX600 S3, Intel Xeon processor 7140M, 3.40 GHz

SPECfp\_rate2000 = 108

SPECfp\_rate\_base2000 = 108

SPEC license #: 22 | Tested by: Fujitsu Siemens Computers | Test date: Jul-2006 | Hardware Avail: Aug-2006 | Software Avail: Jan-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	16	251	118	16	251	118
171.swim	16	1066	54.0	16	1066	54.0
172.mgrid	16	631	52.9	16	631	52.9
173.applu	16	707	55.1	16	707	55.1
177.mesa	16	168	154	16	168	154
178.galgel	16	147	367	16	147	367
179.art	16	90.8	531	16	90.8	531
183.quake	16	441	54.7	16	441	54.7
187.facerec	16	203	174	16	203	174
188.amp	16	306	134	16	306	134
189.lucas	16	615	60.3	16	615	60.3
191.fma3d	16	566	68.8	16	566	68.8
200.sixtrack	16	265	77.1	16	265	77.1
301.apsi	16	384	126	16	384	126

### Hardware

CPU: Intel Xeon processor 7140M (3.40 GHz, 2x1MB L2, 16MB L3, 800 MHz system bus)  
CPU MHz: 3400  
FPU: Integrated  
CPU(s) enabled: 8 cores, 4 chips, 2 cores/chip (Hyper-Threading Technology enabled)  
CPU(s) orderable: 1,2,4 chips  
Parallel: No  
Primary Cache: 12k micro-ops(I) + 16KB(D) on chip, per core  
Secondary Cache: 1024KB(I+D) on chip, per core  
L3 Cache: 16MB on chip, per chip  
Other Cache: N/A  
Memory: 16x1GB DDRII-RAM PC2-3200R (CAS 3-3-3)  
Disk Subsystem: Fujitsu MAS3367NC (SCSI, 15krpm, 36GB)  
Other Hardware: none

### Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 9 with SP3  
Kernel 2.6.5-7.244-smp on an x86\_64  
Compiler: Intel C++ and Fortran Compiler 9.0 for EM64T  
Build 20060120 (for 64-bit applications)  
File System: ext3  
System State: Multi-user run level 3

## Notes/Tuning Information

### GENERAL

+FDO implies feedback-directed optimization  
PASS1: -prof\_gen PASS2: -prof\_use

### Optimization flags

ONESTEP=yes set for all benchmarks

### Portability flags

-DSPEC\_CPU2000\_LP64 applied to all benchmarks  
178.galgel: -FI for fixed-format Fortran

### Base tuning flags

for Fortran and C programs: -fast +FDO

### Peak tuning flags

same as baseline (basepeak=true set globally)



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## Notes/Tuning Information (Continued)

The system bus runs at 800 MHz

This result was measured with 64-bit binaries using the 64-bit version of the operating system.

This result was measured on the PRIMERGY TX600 S3. The PRIMERGY TX600 S3 and the PRIMERGY RX600 S3 are electronically equivalent.

BIOS Configuration:

Adjacent Sector Prefetch = Disable

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