



CINT2000 Result

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Itautec

Servidor Itautec LX201 (Intel Xeon 5120 processor)

SPECint_rate2000 = 40.5

SPECint_rate_base2000 = 40.5

SPEC license #: 9001 | Tested by: Itautec | Test date: Oct-2006 | Hardware Avail: Jul-2006 | Software Avail: May-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	2	124	26.3	2	124	26.3
175.vpr	2	112	29.1	2	112	29.1
176.gcc	2	54.4	46.9	2	54.4	46.9
181.mcf	2	108	38.7	2	108	38.7
186.crafty	2	58.5	39.6	2	58.5	39.6
197.parser	2	127	32.8	2	127	32.8
252.eon	2	54.0	55.9	2	54.0	55.9
253.perlbnk	2	85.5	48.9	2	85.5	48.9
254.gap	2	63.1	40.4	2	63.1	40.4
255.vortex	2	65.2	67.6	2	65.2	67.6
256.bzip2	2	118	29.6	2	118	29.6
300.twolf	2	143	48.8	2	143	48.8

Hardware

CPU: Intel Xeon 5120 processor (1.86 GHz, 4MB L2 shared, 1066 MHz system bus)
CPU MHz: 1860
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1
Parallel: No
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 4096KB(I+D) on chip, per chip, shared
L3 Cache: N/A
Other Cache: N/A
Memory: 4x2GB DDR2-RAM PC2-5300F (CAS 5-5-5)ECC
Disk Subsystem: 1 x 120GB Serial ATA 7200 RPM
Other Hardware: None

Software

Operating System: Windows Server 2003 Enterprise Edition + SP1 (64-bit)
Compiler: Intel C++ Compiler 9.1
Build 20060519Z (for 32-bit applications),
Microsoft Visual Studio .NET 7.0.9466 (for libraries),
MicroQuill SmartHeap for SMP Library 8.0
File System: NTFS
System State: Default

Notes/Tuning Information

PORTABILITY FLAGS

176.gcc: -Dalloca=_alloca /F10000000
186.crafy: -DNT_i386
253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

GENERAL

+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use

Base tuning flags

for C programs: -fast +FDO sh1SMPMt.lib
for C++ programs: -fast -Qcxx_features +FDO

EXTRA LIBRARIES

MicroQuill SmartHeap for SMP Library 8.0 available from
<http://www.microquill.com/>