



# CINT2000 Result

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## Fujitsu Siemens Computers

PRIMERGY RX300 S3, Intel Xeon processor E5310, 1.60 GHz

SPECint\_rate2000 = 132

SPECint\_rate\_base2000 = 132

SPEC license #: 22 | Tested by: Fujitsu Siemens Computers | Test date: Oct-2006 | Hardware Avail: Nov-2006 | Software Avail: May-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.zip	8	147	88.6	8	147	88.6
175.vpr	8	134	97.2	8	134	97.2
176.gcc	8	67.0	152	8	67.0	152
181.mcf	8	157	106	8	157	106
186.crafty	8	68.6	135	8	68.6	135
197.parser	8	148	113	8	148	113
252.eon	8	62.9	192	8	62.9	192
253.perlbnk	8	96.1	174	8	96.1	174
254.gap	8	95.7	107	8	95.7	107
255.vortex	8	77.0	229	8	77.0	229
256.bzip2	8	140	99.3	8	140	99.3
300.twolf	8	165	169	8	165	169

### Hardware

CPU: Intel Xeon processor E5310 (1.60 GHz, 2x4MB L2, 1066 MHz system bus)  
 CPU MHz: 1600  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Parallel: No  
 Primary Cache: 32KB(I) + 32KB(D) on chip, per core  
 Secondary Cache: 8MB(I+D) on chip, per chip (4MB shared per 2 cores)  
 L3 Cache: N/A  
 Other Cache: N/A  
 Memory: 8x1GB DDR2-RAM PC2-4200F (CAS 4-4-4)  
 Disk Subsystem: Seagate ST373454SS (SAS, 15.4krpm, 73GB)  
 Other Hardware: none

### Software

Operating System: Windows Server 2003 Enterprise Edition + SP1 (32-bit)  
 Compiler: Intel C++ Compiler 9.1  
 Build 20060323Z (for 32-bit applications),  
 Microsoft Visual Studio .Net 2003  
 7.1.3088 (for libraries),  
 SmartHeap Library Version 8.0  
 from <http://www.microquill.com>  
 File System: NTFS  
 System State: Default

## Notes/Tuning Information

### GENERAL

+FDO: PASS1=-Qprof\_gen PASS2=-Qprof\_use

### Portability flags

176.gcc: -Dalloca=\_alloca /F10000000  
 186.crafty: -DNT\_i386  
 253.perlbnk: -DSPEC\_CPU2000\_NTOS -DPERLDLL /MT  
 254.gap: -DSYS\_HAS\_CALLOC\_PROTO -DSYS\_HAS\_MALLOC\_PROTO

### Base tuning flags

for C programs: -fast +FDO shlw32m.lib  
 for C++ programs: -fast -Qcxx\_features +FDO shlw32m.lib

### Peak tuning flags

same as baseline (basepeak=true set globally)

The system bus runs at 1066 MHz

This result was measured with 32-bit binaries using the 32-bit version of the operating system.



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## Notes/Tuning Information (Continued)

BIOS configuration:

Adjacent Sector Prefetch = disable

The PRIMERGY RX300 S3 and the PRIMERGY TX300 S3 are electronically equivalent.

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<http://www.fujitsu-siemens.com/countries>