



CFP2000 Result

Copyright ©1999-2005, Standard Performance Evaluation Corporation

Dell

PowerEdge 2950 (Intel Xeon processor E5310, 1.60 GHz)

SPECfp_rate2000 = 76.7

SPECfp_rate_base2000 = 76.7

SPEC license #: 55 Tested by: Dell, Inc., Round Rock, TX Test date: Nov-2006 Hardware Avail: Dec-2006 Software Avail: Aug-2006

| Benchmark | Base Copies | Base Runtime | Base Ratio | Copies | Runtime | Ratio |
|--------------|-------------|--------------|------------|--------|---------|-------|
| 168.wupwise | 8 | 138 | 108 | 8 | 138 | 108 |
| 171.swim | 8 | 593 | 48.6 | 8 | 593 | 48.6 |
| 172.mgrid | 8 | 422 | 39.6 | 8 | 422 | 39.6 |
| 173.applu | 8 | 390 | 49.9 | 8 | 390 | 49.9 |
| 177.mesa | 8 | 84.6 | 153 | 8 | 84.6 | 153 |
| 178.galgel | 8 | 129 | 209 | 8 | 129 | 209 |
| 179.art | 8 | 173 | 139 | 8 | 173 | 139 |
| 183.quake | 8 | 277 | 43.5 | 8 | 277 | 43.5 |
| 187.facerec | 8 | 190 | 92.8 | 8 | 190 | 92.8 |
| 188.amp | 8 | 202 | 101 | 8 | 202 | 101 |
| 189.lucas | 8 | 336 | 55.2 | 8 | 336 | 55.2 |
| 191.fma3d | 8 | 343 | 56.7 | 8 | 343 | 56.7 |
| 200.sixtrack | 8 | 184 | 55.4 | 8 | 184 | 55.4 |
| 301.apsi | 8 | 324 | 74.5 | 8 | 324 | 74.5 |

Hardware

CPU: Quad-Core Intel Xeon processor E5310 (1066 MHz system bus)
CPU MHz: 1600
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Parallel: No
Primary Cache: 32KB(I) + 32KB(D) on chip, per core
Secondary Cache: 8 MB I+D on chip, per chip (4 MB shared per 2 cores)
L3 Cache: N/A
Other Cache: N/A
Memory: 8 GB (8 x 1 GB 667 MHz CL5 FB-DIMM DDR2 SDRAM)
Disk Subsystem: 1 x 80GB SATA 7200 RPM
Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux 4 Advanced Server Update 4 EM64T
Compiler: Intel C++ Compiler 9.0 for EM64T Build 20060120
Intel Fortran Compiler 9.0 for EM64T Build 20051201
File System: ext3
System State: Runlevel 3

Notes/Tuning Information

GENERAL

ONESTEP=yes for all benchmarks

+FDO implies feedback-directed optimization PASS1: -prof_gen PASS2: -prof_use

PORTABILITY FLAGS

-DSPEC_CPU2000_LP64 applied to all benchmarks

178.galgel: -FI for fixed-format Fortran

BASE TUNING

Baseline optimizations for C and Fortran: -fast +FDO

PEAK TUNING

basepeak=yes set for all benchmarks

BIOS SETTINGS

Adjacent Cache Line Prefetch disabled in BIOS