



CINT2000 Result

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Hewlett-Packard Company
ProLiant DL140 G3 (3.73 GHz, Intel Xeon processor 5080)

SPECint_rate2000 = **83.1**
SPECint_rate_base2000 = **82.6**

SPEC license #: 3 | Tested by: Hewlett-Packard Company | Test date: Nov-2006 | Hardware Avail: Jun-2006 | Software Avail: Oct-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	8	177	73.4	8	176	73.8
175.vpr	8	251	51.7	8	241	53.9
176.gcc	8	91.4	112	8	91.4	112
181.mcf	8	333	50.2	8	333	50.1
186.crafty	8	148	62.6	8	146	63.8
197.parser	8	210	79.4	8	209	80.0
252.eon	8	94.5	128	8	94.9	127
253.perlbnk	8	181	92.5	8	179	93.2
254.gap	8	117	87.1	8	116	87.7
255.vortex	8	136	130	8	136	130
256.bzip2	8	207	67.3	8	207	67.3
300.twolf	8	260	107	8	263	106

Hardware

CPU: Intel Xeon processor 5080 (3.73GHz, 2x2MB L2, 1066 MHz system bus)
CPU MHz: 3730
FPU: Integrated
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip (Hyper-Threading Technology enabled)
CPU(s) orderable: 1,2 chips
Parallel: No
Primary Cache: 12 K micro-ops I + 16 KB D on chip per core
Secondary Cache: 2 MB I+D on chip per core
L3 Cache: N/A
Other Cache: N/A
Memory: 8 GB (8 x 1 GB PC2-5300F)
Disk Subsystem: 2 x 36 GB 15 K SAS
Other Hardware:

Software

Operating System: Windows Server 2003 Enterprise SP1
Compiler: Intel C++ Compiler for 32-bit applications, (version 9.1 Build 20060816Z)
Microsoft Visual Studio .NET 7.0.9466 (for libraries)
MicroQuill Smartheap Library 8.0
File System: NTFS
System State: Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Base tuning for C programs: -fast +FDO ONESTEP=yes shlw32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO
Portability flags:
 176.gcc: -Dalloca=_alloca /F10000000
 186.crafty: -DNT_i386
 253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
 254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip: -fast +FDO ONESTEP=yes
175.vpr: -fast +FDO ONESTEP=yes
176.gcc: basepeak=1
181.mcf: -fast +FDO ONESTEP=yes shlw32M.lib
186.crafty: -fast -Oa +FDO ONESTEP=yes shlw32M.lib
197.parser: -fast +FDO ONESTEP=yes
252.eon: -fast +FDO
253.perlbnk: -fast -Oa +FDO ONESTEP=yes shlw32M.lib
254.gap: -fast +FDO ONESTEP=yes
255.vortex: -fast +FDO ONESTEP=yes shlw32M.lib
256.bzip2: basepeak=1
```



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Notes/Tuning Information (Continued)

300.twolf: -fast +FDO shlw32M.lib
BIOS Configuration Notes
Adjacent Cache Line Prefetch Disabled
Hyper-Threading Technology enabled