



SPEC[®] CINT2006 Result

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IBM Corporation

SPECint[®]2006 = Not Run

IBM BladeCenter HS21 XM (Intel Xeon E5320)

SPECint_base2006 = 11.1

CPU2006 license: 11

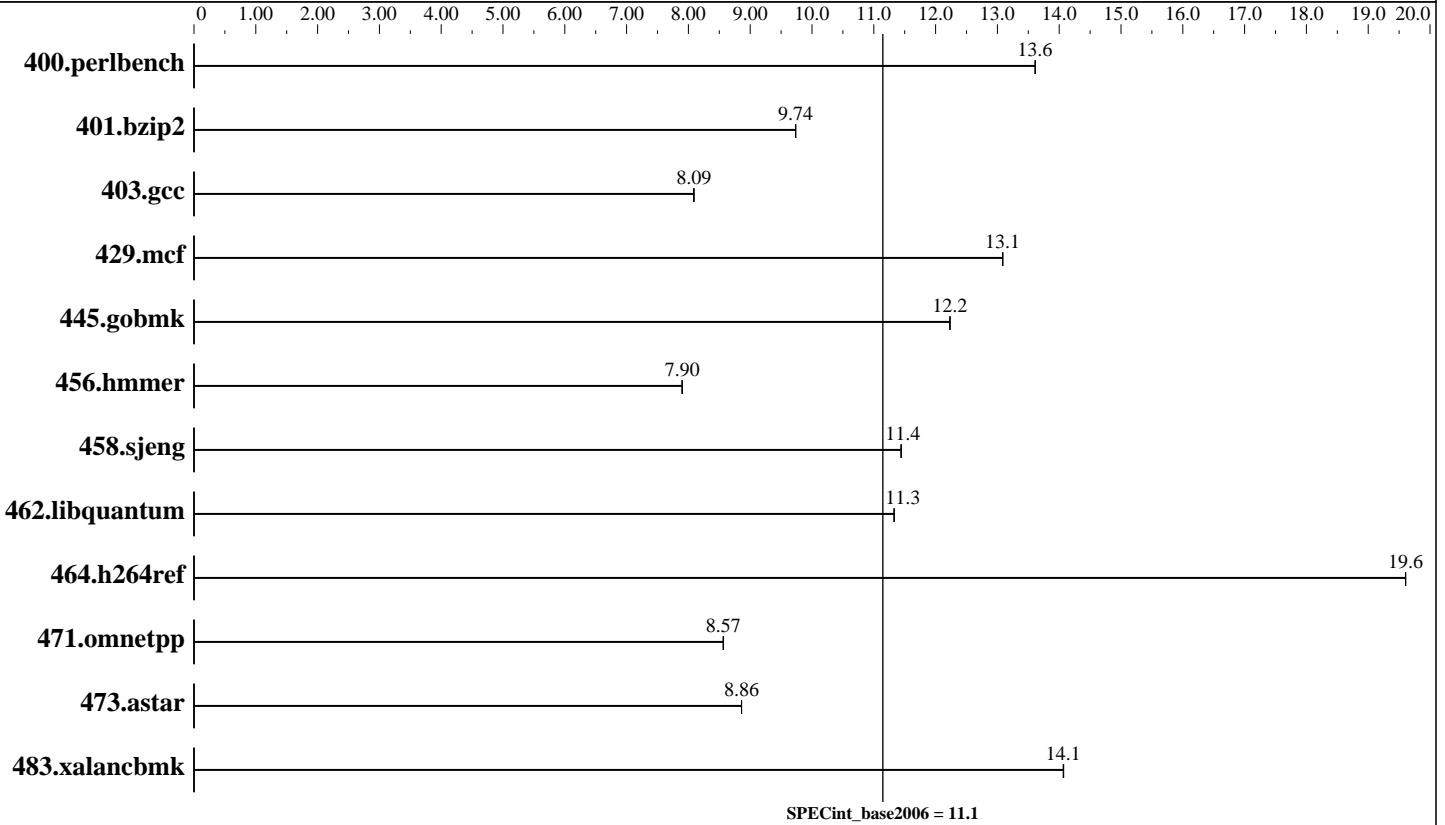
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Jan-2007

Hardware Availability: Feb-2007

Software Availability: Aug-2006



Hardware

CPU Name: Intel Xeon E5320
 CPU Characteristics: 1066MHz system bus
 CPU MHz: 1866
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 x 2GB DDR2-5300F ECC)
 Disk Subsystem: 1 x 74 GB SAS, 1000 RPM
 Other Hardware: None

Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
 Compiler: Intel C++ Compiler for IA32 version 9.1
 Build no 20060816
 Microsoft Visual Studio .Net 2003 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: Smart Heap Library, Version 8



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	718	13.6	718	13.6	<u>718</u>	<u>13.6</u>						
401.bzip2	<u>991</u>	<u>9.74</u>	991	9.74	991	9.73						
403.gcc	<u>995</u>	<u>8.09</u>	995	8.09	996	8.08						
429.mcf	<u>697</u>	<u>13.1</u>	697	13.1	697	13.1						
445.gobmk	<u>857</u>	<u>12.2</u>	858	12.2	857	12.2						
456.hmmr	1181	7.90	1181	7.90	<u>1181</u>	<u>7.90</u>						
458.sjeng	1058	11.4	1057	11.4	<u>1057</u>	<u>11.4</u>						
462.libquantum	1829	11.3	1829	11.3	<u>1829</u>	<u>11.3</u>						
464.h264ref	1129	19.6	<u>1129</u>	<u>19.6</u>	1129	19.6						
471.omnetpp	729	8.57	730	8.56	<u>730</u>	<u>8.57</u>						
473.astar	792	8.86	<u>792</u>	<u>8.86</u>	792	8.86						
483.xalancbmk	<u>490</u>	<u>14.1</u>	490	14.1	491	14.1						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE
```

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```



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Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.xml>

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For other inquiries, please contact webmaster@spec.org.

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