



SPEC® CINT2006 Result

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Itautec

SPECint®_rate2006 = 71.7

Servidor Itautec MX221 (Intel Xeon E5345)

SPECint_rate_base2006 = 71.7

CPU2006 license: 9001

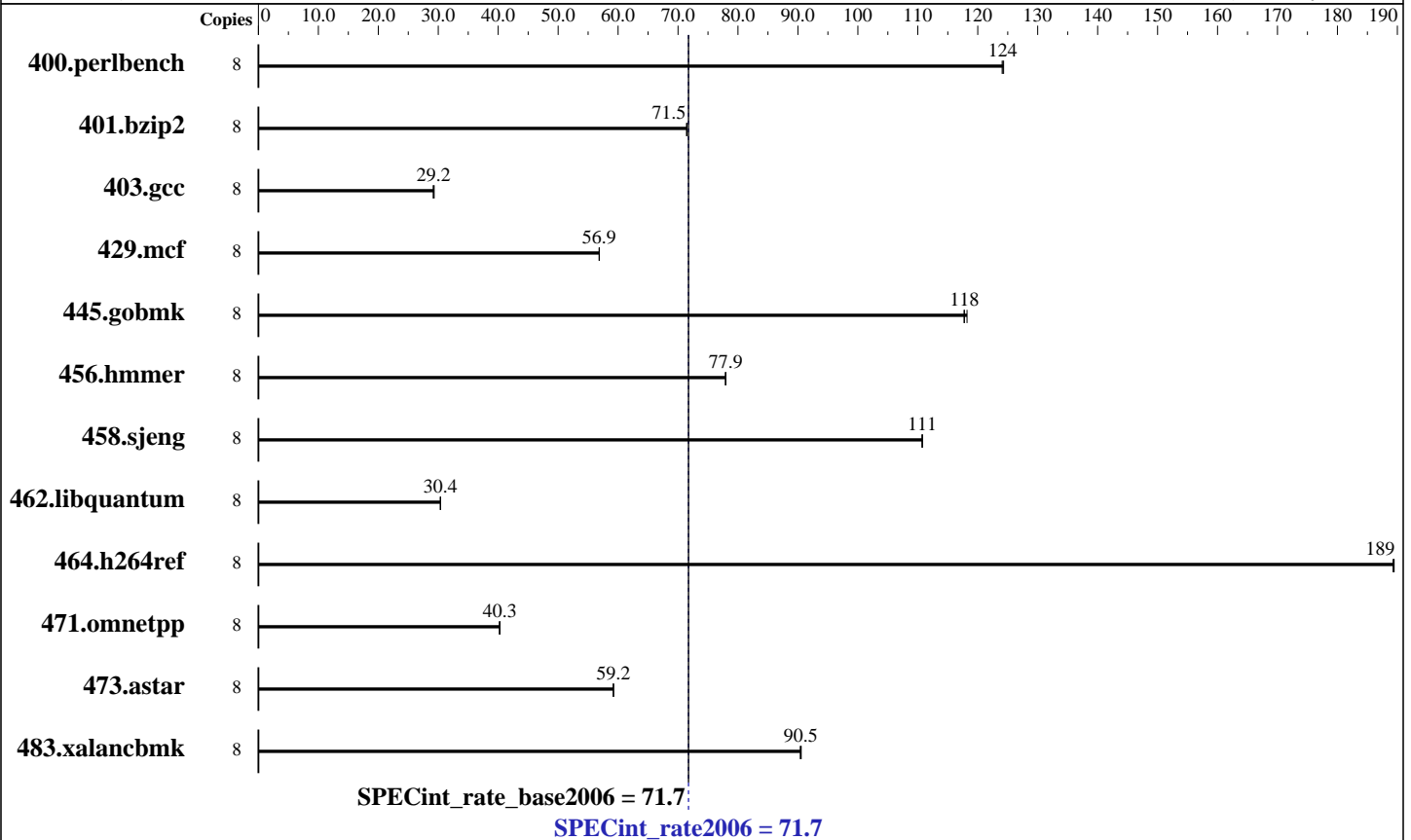
Test date: Feb-2007

Test sponsor: Itautec

Hardware Availability: Feb-2007

Tested by: Itautec

Software Availability: May-2006



Hardware

CPU Name: Intel Xeon E5345
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 2330
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1-2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2GB DDR2-RAM PC2-5300F CAS 5-5-5)
 Disk Subsystem: 73 GB SCSI, 10000RPM
 Other Hardware: None

Software

Operating System: Windows Server 2003 Enterprise Edition + SP1 (32-bit)
 Compiler: Intel C++ Compiler for IA32 version 9.1
 Package ID W_CC_C_9.1.025 Build no 20060519Z
 Microsoft Visual Studio .NET 2003 7.1.3088 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: Microquill SmartHeap Library v.8.0 for SMP



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	629	124	630	124	629	124	8	629	124	630	124	629	124
401.bzip2	8	1081	71.4	1080	71.5	1080	71.5	8	1081	71.4	1080	71.5	1080	71.5
403.gcc	8	2205	29.2	2207	29.2	2197	29.3	8	2205	29.2	2207	29.2	2197	29.3
429.mcf	8	1283	56.9	1283	56.9	1284	56.8	8	1283	56.9	1283	56.9	1284	56.8
445.gobmk	8	713	118	710	118	713	118	8	713	118	710	118	713	118
456.hammer	8	958	77.9	958	77.9	957	78.0	8	958	77.9	958	77.9	957	78.0
458.sjeng	8	874	111	874	111	874	111	8	874	111	874	111	874	111
462.libquantum	8	5458	30.4	5460	30.4	5459	30.4	8	5458	30.4	5460	30.4	5459	30.4
464.h264ref	8	935	189	935	189	934	189	8	935	189	935	189	934	189
471.omnetpp	8	1242	40.3	1244	40.2	1241	40.3	8	1242	40.3	1244	40.2	1241	40.3
473.astar	8	948	59.2	949	59.2	948	59.2	8	948	59.2	949	59.2	948	59.2
483.xalancbmk	8	611	90.4	610	90.5	610	90.5	8	611	90.4	610	90.5	610	90.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

The Servidor Itautec MX221 and the Servidor Itautec MX201 are electronically equivalent.

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
-fast /F512000000 sh1SMPMt.lib -link /FORCE:MULTIPLE

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Base Optimization Flags (Continued)

C++ benchmarks:
-fast -Qcxx_features /F512000000 sh1SMPMt.lib
-link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Optimization Flags

C benchmarks:
400.perlbench: basepeak = yes
401.bzip2: basepeak = yes
403.gcc: basepeak = yes
429.mcf: basepeak = yes
445.gobmk: basepeak = yes
456.hmmmer: basepeak = yes
458.sjeng: basepeak = yes
462.libquantum: basepeak = yes
464.h264ref: basepeak = yes
C++ benchmarks:
471.omnetpp: basepeak = yes
473.astar: basepeak = yes
483.xalanbmk: basepeak = yes

The flags file that was used to format this result can be browsed at
<http://www.spec.org/cpu2006/flags/Itaotec-ic91-flags.20090714.html>

You can also download the XML flags source by saving the following link:
<http://www.spec.org/cpu2006/flags/Itaotec-ic91-flags.20090714.xml>



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For other inquiries, please contact webmaster@spec.org.

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