SPEC® CFP2006 Result

Hewlett-Packard Company

$SPECfp^{®}2006 =$ 14.5

ProLiant BL480c
(2.66 GHz, Intel Xeon processor X5355)

$SPECfp\_base2006 =$ 14.3

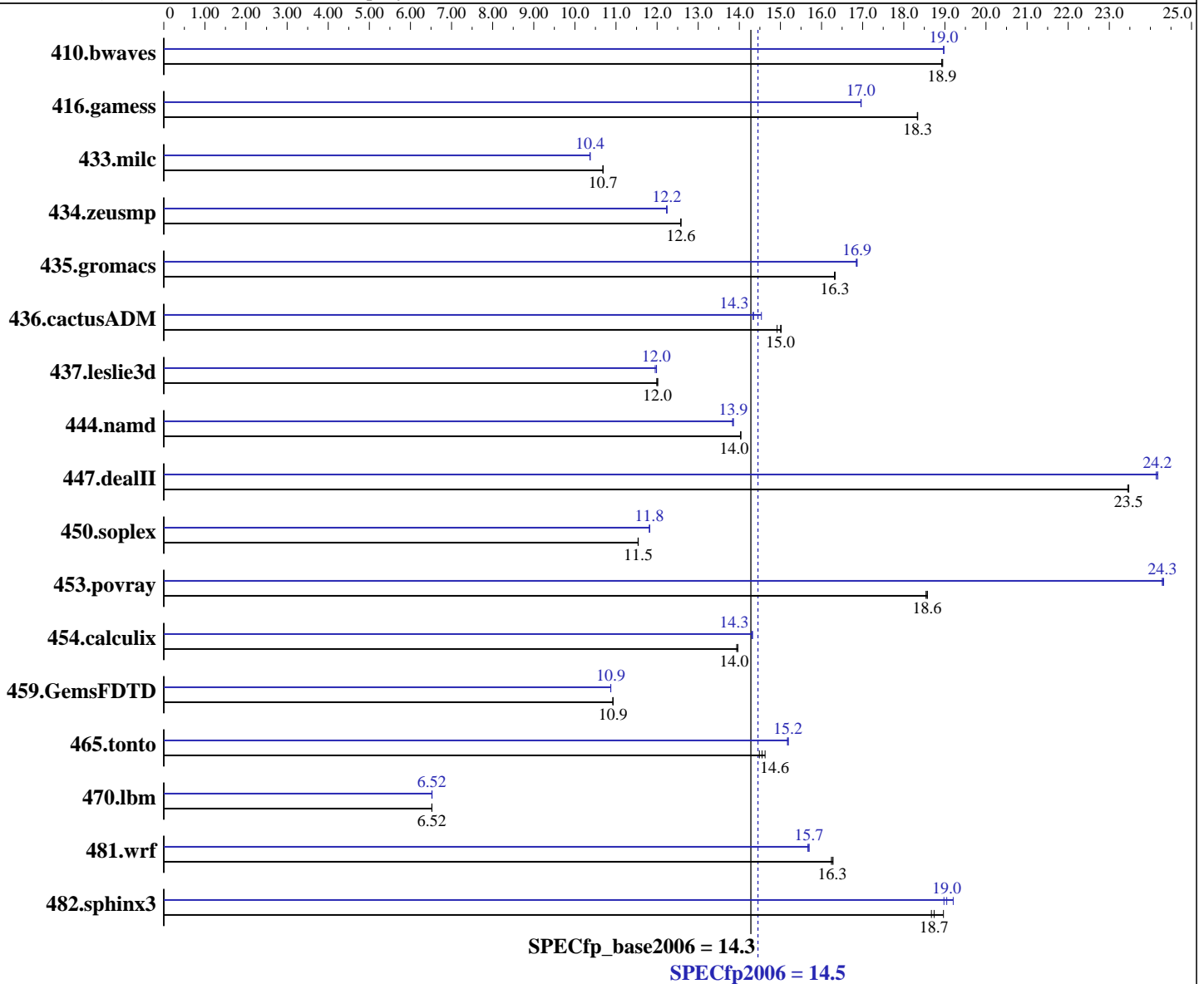**CPU2006 license:** 3

Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Feb-2007

Hardware Availability: Jan-2007

Software Availability: Nov-2006

Benchmark	Result (blue)	Result (base)
410.bwaves	19.0	18.9
416.gamess	17.0	18.3
433.milc	10.4	10.7
434.zeusmp	12.2	12.6
435.gromacs	16.9	16.3
436.cactusADM	14.3	15.0
437.leslie3d	12.0	12.0
444.namd	13.9	14.0
447.dealII	24.2	23.5
450.soplex	11.8	11.5
453.povray	24.3	18.6
454.calculix	14.3	14.0
459.GemsFDTD	10.9	10.9
465.tonto	15.2	14.6
470.lbm	6.52	6.52
481.wrf	15.7	16.3
482.sphinx3	19.0	18.7

SPECfp_base2006 = 14.3

SPECfp2006 = 14.5

Hardware

CPU Name: Intel Xeon X5355

CPU Characteristics: 2.66 GHz, 2x4 MB L2 shared, 1333MHz system bus

CPU MHz: 2666

FPU: Integrated

CPU(s) enabled: 1 core, 1 chip, 4 cores/chip

CPU(s) orderable: 1,2 chips

Primary Cache: 32 KB I + 32 KB D on chip per core

Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

Continued on next page

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64) kernel 2.6.16.21-0.8-default

Compiler: Intel C++ Compiler for Intel EM64T-based applications, Version 9.1 Build 20061101, Package ID: l_cc_c_9.1.045
Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1 Build 20061101, Package ID: l_fc_c_9.1.040

Auto Parallel: No

Continued on next page

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L3 Cache:	None	File System:	ext2
Other Cache:	None	System State:	Multi-user run level 3
Memory:	16 GB (8x2 GB PC2-5300F CL5)	Base Pointers:	64-bit
Disk Subsystem:	1x72 GB 10k SAS	Peak Pointers:	32/64-bit
Other Hardware:	None	Other Software:	None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	**718**	**18.9**	718	18.9	717	18.9	**716**	**19.0**	716	19.0	716	19.0
416.gamess	1068	18.3	1068	18.3	**1068**	**18.3**	**1154**	**17.0**	1155	17.0	1154	17.0
433.milc	**859**	**10.7**	859	10.7	859	10.7	**885**	**10.4**	885	10.4	886	10.4
434.zeusmp	723	12.6	724	12.6	**723**	**12.6**	743	12.2	744	12.2	**744**	**12.2**
435.gromacs	437	16.3	**437**	**16.3**	438	16.3	424	16.8	423	16.9	**423**	**16.9**
436.cactusADM	796	15.0	**797**	**15.0**	801	14.9	834	14.3	**833**	**14.3**	822	14.5
437.leslie3d	782	12.0	784	12.0	**783**	**12.0**	**784**	**12.0**	784	12.0	786	12.0
444.namd	571	14.0	571	14.0	**571**	**14.0**	**579**	**13.9**	580	13.8	579	13.9
447.dealII	**487**	**23.5**	488	23.4	487	23.5	**473**	**24.2**	473	24.2	474	24.1
450.soplex	**723**	**11.5**	723	11.5	723	11.5	706	11.8	**706**	**11.8**	706	11.8
453.povray	287	18.5	286	18.6	**287**	**18.6**	219	24.3	219	24.3	**219**	**24.3**
454.calculix	**591**	**14.0**	592	13.9	591	14.0	577	14.3	576	14.3	**576**	**14.3**
459.GemsFDTD	**971**	**10.9**	971	10.9	971	10.9	**976**	**10.9**	976	10.9	976	10.9
465.tonto	673	14.6	679	14.5	**676**	**14.6**	648	15.2	**648**	**15.2**	649	15.2
470.lbm	2106	6.52	**2107**	**6.52**	2107	6.52	2105	6.53	**2107**	**6.52**	2107	6.52
481.wrf	688	16.2	686	16.3	**687**	**16.3**	713	15.7	**712**	**15.7**	712	15.7
482.sphinx3	1028	19.0	1044	18.7	**1040**	**18.7**	1015	19.2	**1023**	**19.0**	1027	19.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch Disabled in BIOS.
"/usr/bin/taskset" used to bind processes to CPUs.
Environment stack size set to 'unlimited'
Single user Kernel used
```

Base Compiler Invocation

C benchmarks:
```
icc
```

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Base Compiler Invocation (Continued)

C++ benchmarks:
    icpc

Fortran benchmarks:
    ifort

Benchmarks using both Fortran and C:
    icc ifort

Base Portability Flags

```
       410.bwaves: -DSPEC_CPU_LP64
       416.gamess: -DSPEC_CPU_LP64
         433.milc: -DSPEC_CPU_LP64
       434.zeusmp: -DSPEC_CPU_LP64
      435.gromacs: -DSPEC_CPU_LP64 -nofor_main
    436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
      437.leslie3d: -DSPEC_CPU_LP64
        444.namd: -DSPEC_CPU_LP64
       447.dealII: -DSPEC_CPU_LP64
       450.soplex: -DSPEC_CPU_LP64
      453.povray: -DSPEC_CPU_LP64
     454.calculix: -DSPEC_CPU_LP64 -nofor_main
    459.GemsFDTD: -DSPEC_CPU_LP64
        465.tonto: -DSPEC_CPU_LP64
         470.lbm: -DSPEC_CPU_LP64
         481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
      482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
    -fast

C++ benchmarks:
    -fast

Fortran benchmarks:
    -fast

Benchmarks using both Fortran and C:
    -fast

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Tested by:	Hewlett-Packard Company	**Software Availability:**	Nov-2006

Peak Compiler Invocation

C benchmarks:
    `icc`

C++ benchmarks:
    `icpc`

Fortran benchmarks:
    `ifort`

Benchmarks using both Fortran and C:
    `icc ifort`

Peak Portability Flags

`Same as Base Portability Flags`

Peak Optimization Flags

C benchmarks:
    `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -auto_ilp32`

C++ benchmarks:
    `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -auto_ilp32`

Fortran benchmarks:
    `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

Benchmarks using both Fortran and C:
    `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -auto_ilp32`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/hp-ic91-flags.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/hp-ic91-flags.xml`

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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