



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

PRIMERGY RX300 S3, Intel Xeon processor 5130, 2.0 GHz

SPECint_rate2006 = 45.7

SPECint_rate_base2006 = 43.5

CPU2006 license: 22

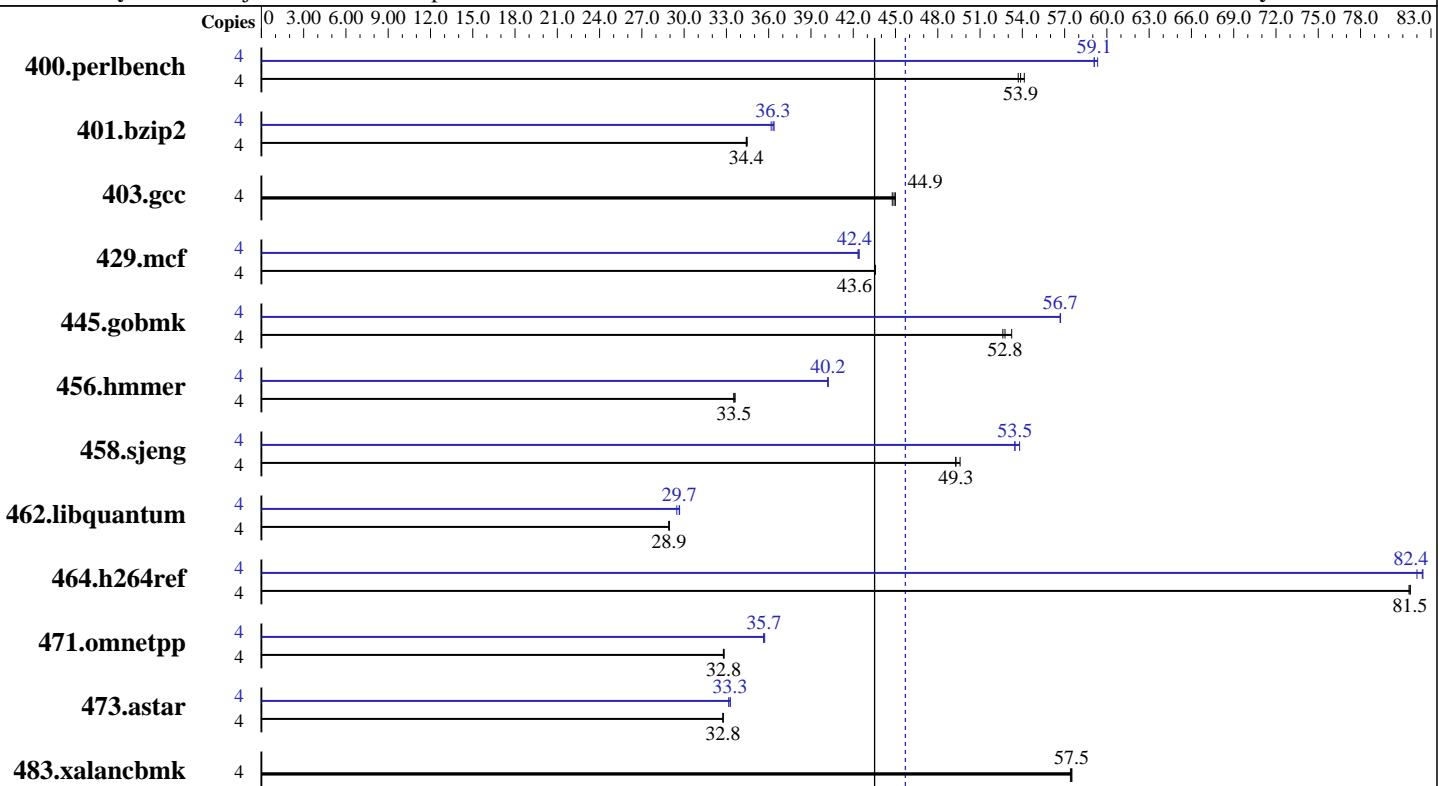
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: May-2007

Hardware Availability: Jul-2006

Software Availability: Feb-2007



Hardware		Software	
CPU Name:	Intel Xeon 5130	Operating System:	64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
CPU Characteristics:	1333 MHz system bus	Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
CPU MHz:	2000	Auto Parallel:	No
FPU:	Integrated	File System:	ext2
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip	System State:	Multiuser, Runlevel 3
CPU(s) orderable:	1,2 chips	Base Pointers:	32-bit
Primary Cache:	32 KB I + 32 KB D on chip per core	Peak Pointers:	32/64-bit
Secondary Cache:	4 MB I+D on chip per chip	Other Software:	Smart Heap Library, Version 8.1
L3 Cache:	None		
Other Cache:	None		
Memory:	8 GB (8x1 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)		
Disk Subsystem:	SAS (73GB 15400 rpm)		
Other Hardware:	None		



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	728	53.7	725	53.9	722	54.1	4	661	59.1	659	59.3	661	59.1
401.bzip2	4	1120	34.5	1121	34.4	1122	34.4	4	1061	36.4	1067	36.2	1063	36.3
403.gcc	4	717	44.9	719	44.8	716	45.0	4	717	44.9	719	44.8	716	45.0
429.mcf	4	837	43.6	837	43.6	838	43.6	4	860	42.4	861	42.4	862	42.3
445.gobmk	4	797	52.6	795	52.8	788	53.2	4	740	56.7	740	56.7	740	56.7
456.hammer	4	1113	33.5	1113	33.5	1110	33.6	4	928	40.2	928	40.2	928	40.2
458.sjeng	4	976	49.6	982	49.3	982	49.3	4	905	53.5	900	53.8	905	53.5
462.libquantum	4	2865	28.9	2863	28.9	2867	28.9	4	2795	29.7	2793	29.7	2811	29.5
464.h264ref	4	1086	81.5	1087	81.4	1086	81.5	4	1074	82.4	1075	82.4	1079	82.0
471.omnetpp	4	763	32.8	761	32.8	762	32.8	4	701	35.7	702	35.6	700	35.7
473.astar	4	857	32.8	857	32.8	857	32.8	4	844	33.3	847	33.2	844	33.3
483.xalancbmk	4	480	57.5	481	57.4	480	57.5	4	480	57.5	481	57.4	480	57.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs

General Notes

The system bus runs at 1333 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hammer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:
Adjacent Sector Prefetch = Disable

The PRIMERGY RX300 S3 and the PRIMERGY TX300 S3 are electronically equivalent.

For information about Fujitsu Siemens Computers in your country please see:
<http://www.fujitsu-siemens.com/countries>

Base Compiler Invocation

C benchmarks:
icc

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Base Compiler Invocation (Continued)

C++ benchmarks:

`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_X64`

462.libquantum: `-DSPEC_CPU_LINUX`

483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:

`-fast`

C++ benchmarks:

`-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap`

Peak Compiler Invocation

C benchmarks (except as noted below):

`icc`

401.bzip2: `/opt/intel/cce/9.1.047/bin/icc`
`-I/opt/intel/cce/9.1.047/include`
`-L/opt/intel/cce/9.1.047/lib`

456.hmmr: `/opt/intel/cce/9.1.047/bin/icc`
`-I/opt/intel/cce/9.1.047/include`
`-L/opt/intel/cce/9.1.047/lib`

462.libquantum: `/opt/intel/cce/9.1.047/bin/icc`
`-I/opt/intel/cce/9.1.047/include`
`-L/opt/intel/cce/9.1.047/lib`

C++ benchmarks:

`icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_X64`

401.bzip2: `-DSPEC_CPU_LP64`

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Peak Portability Flags (Continued)

456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast
-L/opt/SmartHeap_8_1/lib -lsmartheap

445.gobmk: Same as 429.mcf

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 429.mcf

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -xP -O3 -ipo
-no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

473.astar: -prof_gen(pass 1) -prof_use(pass 2) -fast
-L/opt/SmartHeap_8_1/lib -lsmartheap

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml



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