



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu Siemens Computers

**SPECint®\_rate2006 = 30.8**

PRIMERGY RX100 S4, Intel Xeon processor 3070,  
2.67 GHz

**SPECint\_rate\_base2006 = 29.3**

CPU2006 license: 22

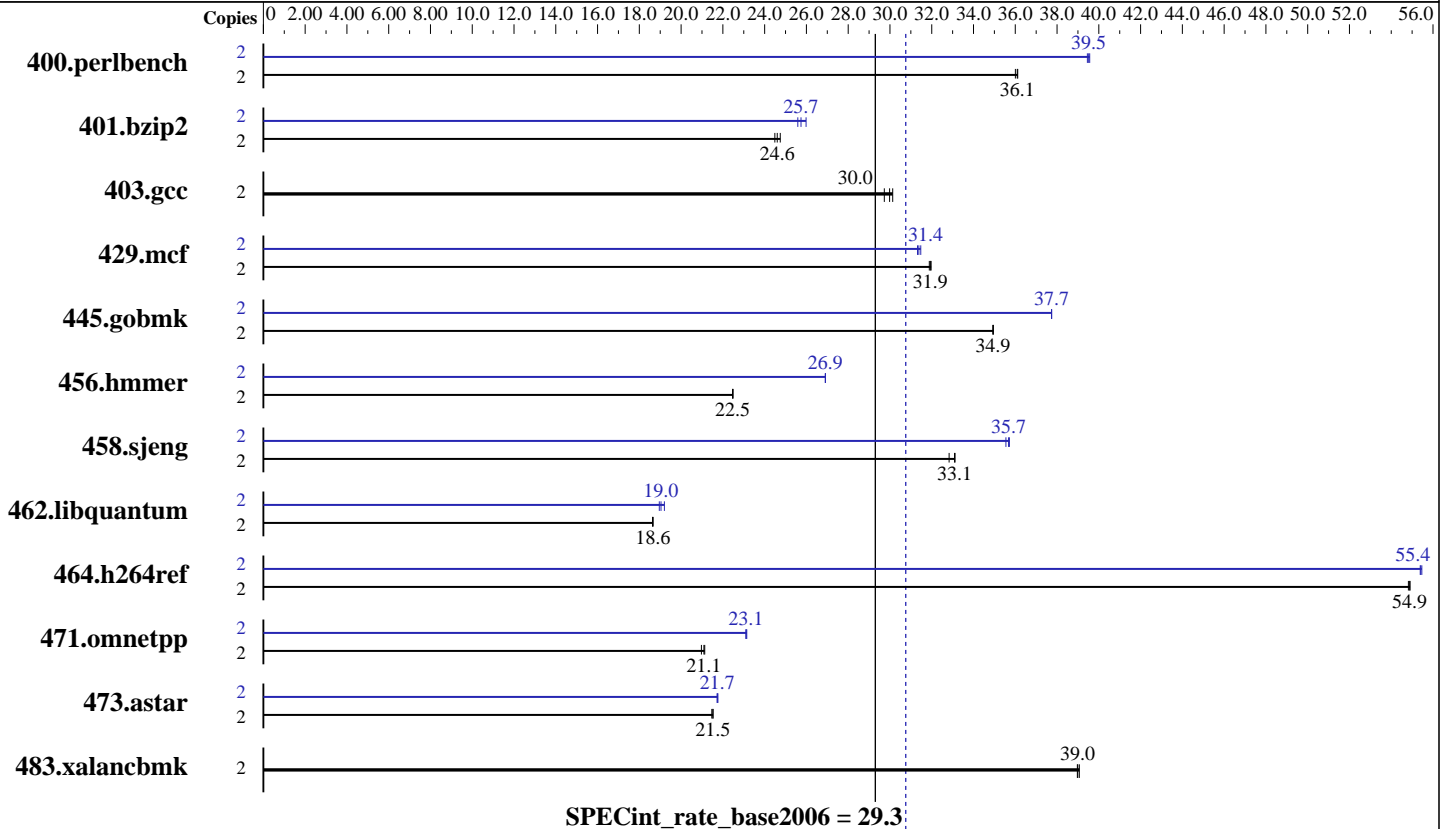
Test date: May-2007

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: May-2007

Tested by: Fujitsu Siemens Computers

Software Availability: Feb-2007



### Hardware

CPU Name: Intel Xeon 3070  
 CPU Characteristics: 1067 MHz system bus  
 CPU MHz: 2667  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 4 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (4x2 GB DDR2 PC2-4200E, 2 rank, CAS 4-4-4, with ECC)  
 Disk Subsystem: SATA (160 GB, 7200 rpm)  
 Other Hardware: None

### Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86\_64  
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l\_cc\_p\_9.1.047  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Multiuser, Runlevel 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Smart Heap Library, Version 8.1



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu Siemens Computers

PRIMERGY RX100 S4, Intel Xeon processor 3070,  
2.67 GHz

SPECint\_rate2006 = 30.8

SPECint\_rate\_base2006 = 29.3

CPU2006 license: 22

Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: May-2007

Hardware Availability: May-2007

Software Availability: Feb-2007

## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	541	36.1	<b><u>541</u></b>	<b><u>36.1</u></b>	543	36.0	2	494	39.6	495	39.5	<b><u>495</u></b>	<b><u>39.5</u></b>
401.bzip2	2	780	24.7	788	24.5	<b><u>784</u></b>	<b><u>24.6</u></b>	2	743	26.0	<b><u>750</u></b>	<b><u>25.7</u></b>	754	25.6
403.gcc	2	542	29.7	534	30.1	<b><u>537</u></b>	<b><u>30.0</u></b>	2	542	29.7	534	30.1	<b><u>537</u></b>	<b><u>30.0</u></b>
429.mcf	2	571	32.0	<b><u>571</u></b>	<b><u>31.9</u></b>	572	31.9	2	<b><u>581</u></b>	<b><u>31.4</u></b>	580	31.5	582	31.3
445.gobmk	2	600	34.9	<b><u>601</u></b>	<b><u>34.9</u></b>	601	34.9	2	556	37.7	<b><u>556</u></b>	<b><u>37.7</u></b>	556	37.7
456.hmmmer	2	<b><u>830</u></b>	<b><u>22.5</u></b>	830	22.5	830	22.5	2	693	26.9	<b><u>693</u></b>	<b><u>26.9</u></b>	694	26.9
458.sjeng	2	<b><u>731</u></b>	<b><u>33.1</u></b>	737	32.8	731	33.1	2	678	35.7	681	35.6	<b><u>678</u></b>	<b><u>35.7</u></b>
462.libquantum	2	2224	18.6	<b><u>2222</u></b>	<b><u>18.6</u></b>	2221	18.7	2	<b><u>2177</u></b>	<b><u>19.0</u></b>	2158	19.2	2186	19.0
464.h264ref	2	806	54.9	<b><u>806</u></b>	<b><u>54.9</u></b>	807	54.8	2	798	55.5	799	55.4	<b><u>799</u></b>	<b><u>55.4</u></b>
471.omnetpp	2	592	21.1	596	21.0	<b><u>593</u></b>	<b><u>21.1</u></b>	2	540	23.1	541	23.1	<b><u>541</u></b>	<b><u>23.1</u></b>
473.astar	2	654	21.5	<b><u>652</u></b>	<b><u>21.5</u></b>	652	21.5	2	645	21.8	646	21.7	<b><u>646</u></b>	<b><u>21.7</u></b>
483.xalancbmk	2	353	39.1	354	39.0	<b><u>354</u></b>	<b><u>39.0</u></b>	2	353	39.1	354	39.0	<b><u>354</u></b>	<b><u>39.0</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
'/usr/bin/taskset' used to bind processes to CPUs

## General Notes

The system bus runs at 1067 MHz

All binaries were built with 32-bit Intel compiler except:  
401.bzip2, 456.hmmmer and 462.libquantum in peak were built with  
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:  
Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers in your country please see:  
<http://www.fujitsu-siemens.com/countries>

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Fujitsu Siemens Computers**

**SPECint\_rate2006 = 30.8**

PRIMERGY RX100 S4, Intel Xeon processor 3070,  
2.67 GHz

**SPECint\_rate\_base2006 = 29.3**

**CPU2006 license:** 22

**Test date:** May-2007

**Test sponsor:** Fujitsu Siemens Computers

**Hardware Availability:** May-2007

**Tested by:** Fujitsu Siemens Computers

**Software Availability:** Feb-2007

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_X64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast

C++ benchmarks:  
-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap\_8\_1/lib -lsmartheap

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu Siemens Computers

PRIMERGY RX100 S4, Intel Xeon processor 3070,  
2.67 GHz

**SPECint\_rate2006 = 30.8**

**SPECint\_rate\_base2006 = 29.3**

**CPU2006 license:** 22

**Test sponsor:** Fujitsu Siemens Computers

**Tested by:** Fujitsu Siemens Computers

**Test date:** May-2007

**Hardware Availability:** May-2007

**Software Availability:** Feb-2007

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof\_gen(pass 1) -prof\_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof\_gen(pass 1) -prof\_use(pass 2) -fast  
-L/opt/SmartHeap\_8\_1/lib -lsmartheap

445.gobmk: Same as 429.mcf

456.hmmmer: Same as 400.perlbench

458.sjeng: Same as 429.mcf

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: -prof\_gen(pass 1) -prof\_use(pass 2) -xP -O3 -ipo  
-no-prec-div -L/opt/SmartHeap\_8\_1/lib -lsmartheap

473.astar: -prof\_gen(pass 1) -prof\_use(pass 2) -fast  
-L/opt/SmartHeap\_8\_1/lib -lsmartheap

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090714.09.html](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html)

You can also download the XML flags source by saving the following link:

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090714.09.xml](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml)

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.  
Report generated on Tue Jul 22 11:40:19 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 29 May 2007.