



SPEC® CINT2006 Result

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Lenovo Group Limited

(Test Sponsor: Intel Corporation)

SPECint®2006 = Not Run

Lenovo ThinkPad T61(Intel Core 2 Duo T7100)

SPECint_base2006 = 12.1

CPU2006 license: 13

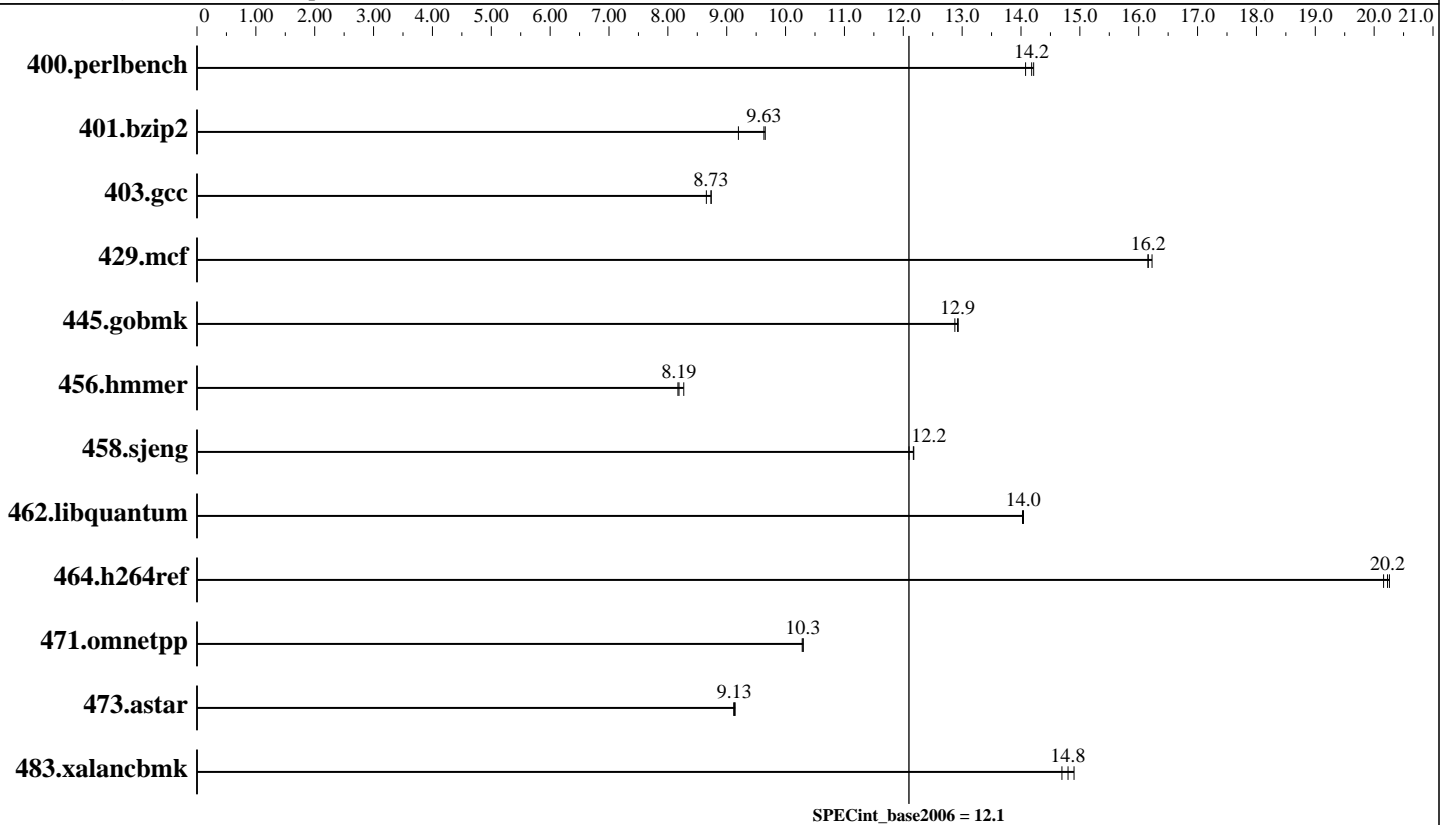
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Jun-2007

Hardware Availability: Jun-2007

Software Availability: Feb-2007



Hardware

CPU Name: Intel Core 2 Duo T7100
 CPU Characteristics: 1.80 GHz, 2MB L2, 800 MHz Bus
 CPU MHz: 1800
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 2 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 2 GB (2x1GB Qimonda* PC-5300 DDR2-667 5-5-5-12)
 Disk Subsystem: Hitachi* HTS721010G9 SATA 100GB 7200RPM
 Other Hardware: None

Software

Operating System: Windows XP Professional w/ SP2
 Compiler: Intel C++ Compiler for IA32 version 9.1
 Build no 20060816
 Microsoft Visual Studio .Net 2003 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: SmartHeap library V7.4 from Microquill



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	694	14.1	687	14.2	689	14.2						
401.bzip2	1049	9.20	1002	9.63	1000	9.65						
403.gcc	930	8.65	922	8.74	922	8.73						
429.mcf	564	16.2	562	16.2	564	16.2						
445.gobmk	815	12.9	811	12.9	812	12.9						
456.hmmr	1142	8.17	1128	8.27	1139	8.19						
458.sjeng	1000	12.1	994	12.2	994	12.2						
462.libquantum	1477	14.0	1476	14.0	1476	14.0						
464.h264ref	1094	20.2	1092	20.3	1098	20.2						
471.omnetpp	607	10.3	607	10.3	608	10.3						
473.astar	770	9.12	768	9.14	769	9.13						
483.xalancbmk	470	14.7	466	14.8	463	14.9						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Binaries built on Windows XP Professional w/ SP2
XP Power Profile set to AC/High Performance

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.00.xml>

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For other inquiries, please contact webmaster@spec.org.

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