



# SPEC® CINT2006 Result

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## Hewlett-Packard Company

ProLiant DL380 G5  
(3.0 GHz, Intel Xeon processor X5365)

**SPECint\_rate2006 = 116**

**SPECint\_rate\_base2006 = 97.4**

CPU2006 license: 3

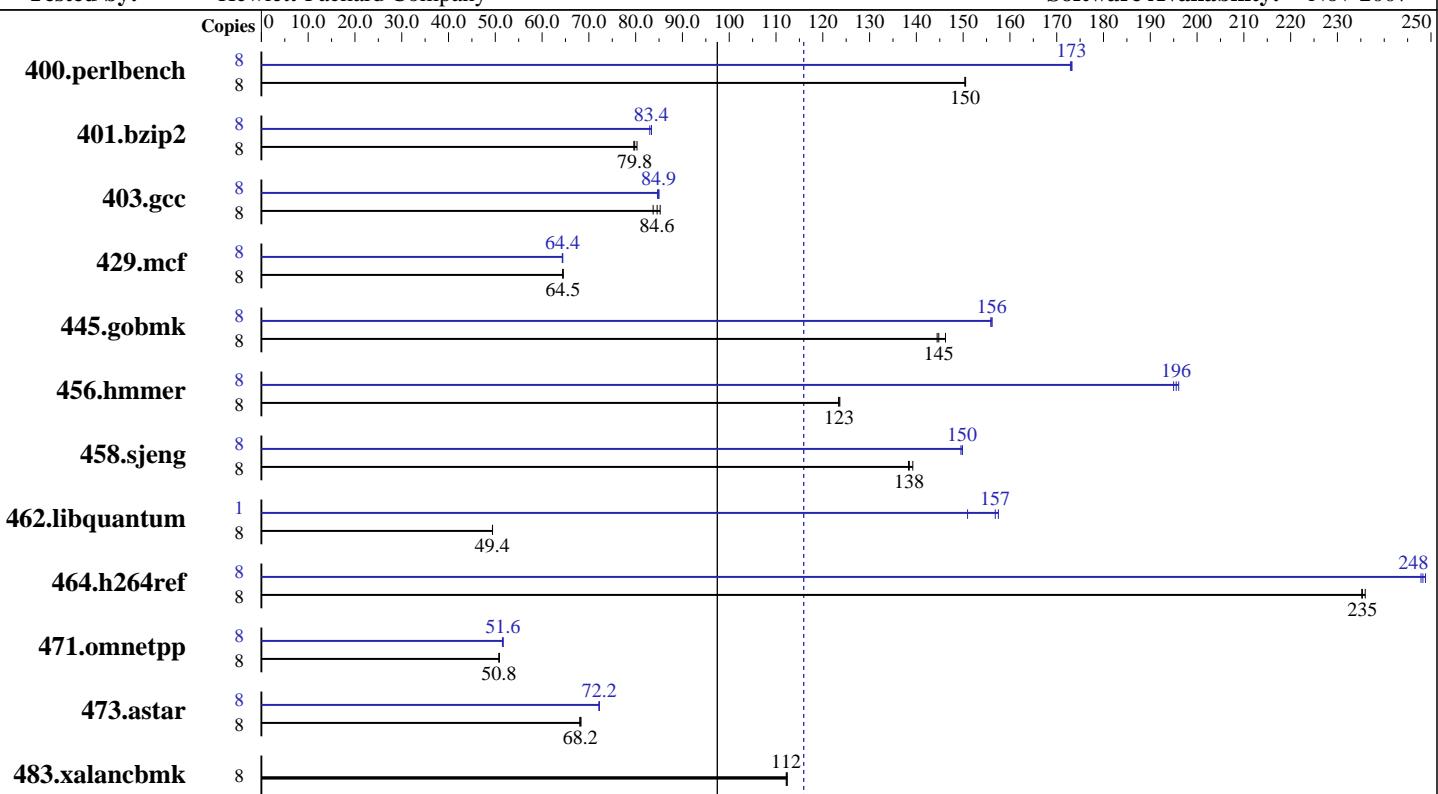
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Aug-2007

Hardware Availability: Sep-2007

Software Availability: Nov-2007



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### Hardware

CPU Name: Intel Xeon X5365  
CPU Characteristics: 3.0 GHz, 2x4 MB L2 shared, 1333 MHz system bus  
CPU MHz: 3000  
FPU: Integrated  
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
CPU(s) orderable: 1,2 chips  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores  
L3 Cache: None  
Other Cache: None  
Memory: 16 GB (8x2 GB PC2-5300F CL5)  
Disk Subsystem: 1x72 GB 15 K SAS  
Other Hardware: None

### Software

Operating System: SuSE Linux Enterprise Server 10 (x86\_64) SP1 kernel 2.6.16.46-0.12-smp  
Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070725  
Auto Parallel: Yes  
File System: ext2  
System State: Multi-user run level 3  
Base Pointers: 32-bit  
Peak Pointers: 32/64-bit  
Other Software: MicroQuill SmartHeap Library 8.1 binutils-2.17.50



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	519	151	520	150	<b>519</b>	<b>150</b>	8	452	173	451	173	<b>451</b>	<b>173</b>
401.bzip2	8	962	80.3	970	79.6	<b>967</b>	<b>79.8</b>	8	926	83.4	930	83.0	<b>926</b>	<b>83.4</b>
403.gcc	8	769	83.7	<b>761</b>	<b>84.6</b>	756	85.2	8	<b>758</b>	<b>84.9</b>	758	85.0	<b>761</b>	84.7
429.mcf	8	1133	64.4	<b>1131</b>	<b>64.5</b>	1131	64.5	8	1133	64.4	<b>1133</b>	<b>64.4</b>	1134	64.3
445.gobmk	8	574	146	581	144	<b>580</b>	<b>145</b>	8	537	156	<b>537</b>	<b>156</b>	538	156
456.hammer	8	605	123	604	124	<b>605</b>	<b>123</b>	8	383	195	<b>382</b>	<b>196</b>	381	196
458.sjeng	8	<b>699</b>	<b>138</b>	695	139	700	138	8	<b>646</b>	<b>150</b>	646	150	647	150
462.libquantum	8	3355	49.4	3356	49.4	<b>3356</b>	<b>49.4</b>	1	132	158	<b>132</b>	<b>157</b>	137	151
464.h264ref	8	750	236	<b>752</b>	<b>235</b>	753	235	8	711	249	714	248	<b>713</b>	<b>248</b>
471.omnetpp	8	<b>984</b>	<b>50.8</b>	984	50.8	984	50.8	8	968	51.7	<b>968</b>	<b>51.6</b>	971	51.5
473.astar	8	822	68.3	<b>824</b>	<b>68.2</b>	825	68.1	8	778	72.2	<b>778</b>	<b>72.2</b>	777	72.2
483.xalancbmk	8	<b>492</b>	<b>112</b>	492	112	491	112	8	<b>492</b>	<b>112</b>	492	112	491	112

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to physical,0  
KMP\_STACKSIZE set to 64M

## Platform Notes

BIOS configuration:

Power Regulator set to Static High Performance Mode  
Adjacent Sector Prefetch Disabled

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

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## Base Portability Flags (Continued)

462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/home/cmpllr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc  
-L/home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib  
-I/home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

456.hmmr: /home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc  
-L/home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib  
-I/home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmr: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -O0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-flags.20090714.01.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-flags.20090714.01.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.

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