SPEC® CFP2006 Result

IBM Corporation

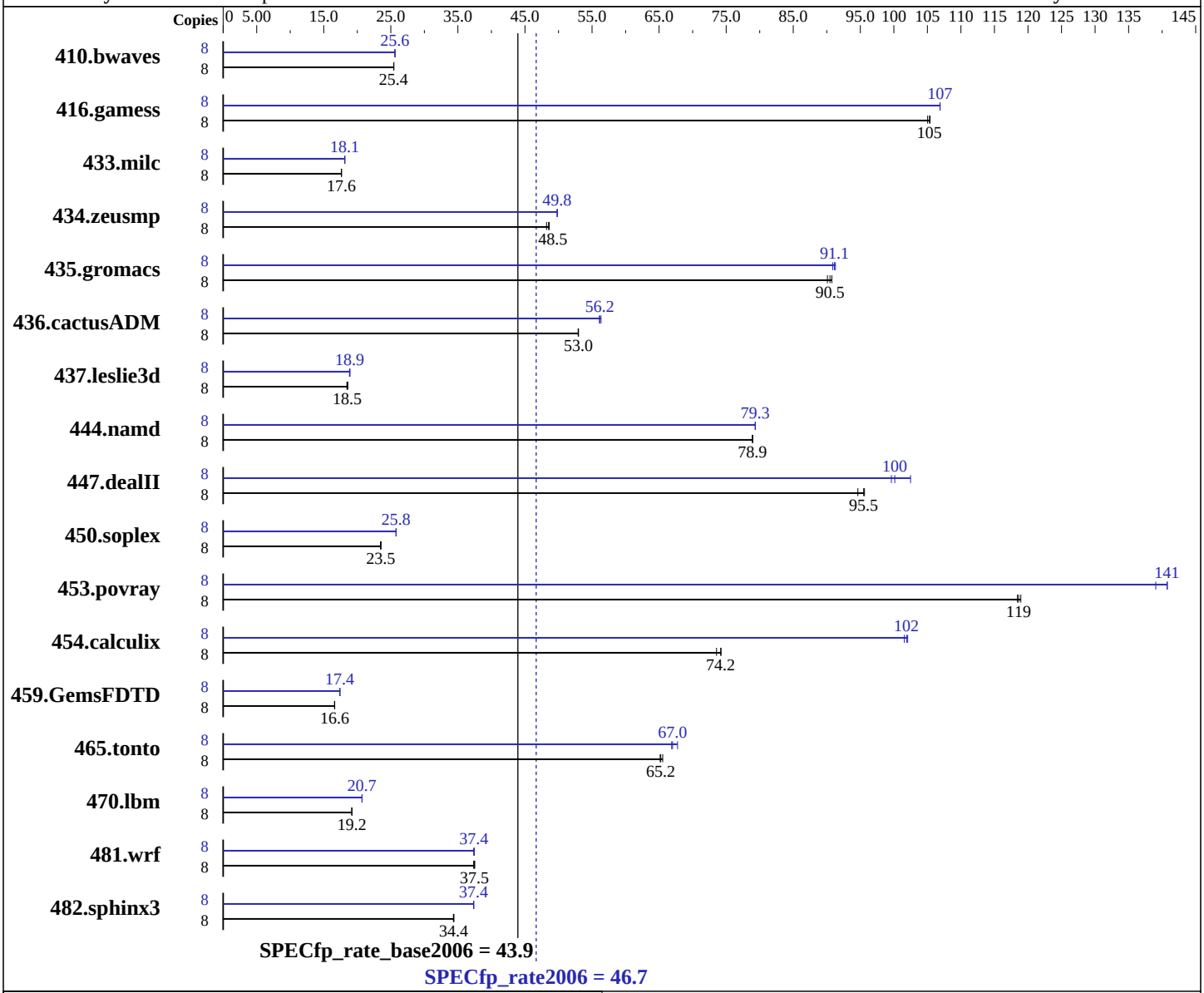
IBM BladeCenter HS21 XM (Intel Xeon E5320)

SPECfp®_rate2006 = 46.7

SPECfp_rate_base2006 = 43.9

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Sep-2007
Hardware Availability: Feb-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon E5320
CPU Characteristics:	1066MHz system bus
CPU MHz:	1860
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

Continued on next page

Software

Operating System:	SLES 10 (x86_64), 2.6.16.21-0.8-smp
Compiler:	Intel C++ and Fortran Compiler for Linux version 10.1 Build 20070725
Auto Parallel:	No
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	64-bit
Peak Pointers:	32/64-bit

Continued on next page

SPEC CFP2006 Result

IBM Corporation

IBM BladeCenter HS21 XM (Intel Xeon E5320)

SPECfp_rate2006 = 46.7

SPECfp_rate_base2006 = 43.9

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Sep-2007
Hardware Availability: Feb-2007
Software Availability: Nov-2007

L3 Cache: None
Other Cache: None
Memory: 16 GB (8 x 2 GB DDR2-5300F ECC)
Disk Subsystem: 1 x 36 GB SAS, 10000 RPM
Other Hardware: None

Other Software: MicroQuill SmartHeap 8.1

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	4270	25.5	4276	25.4	**4275**	**25.4**	8	4241	25.6	**4244**	**25.6**	4245	25.6
416.gamess	8	**1487**	**105**	1491	105	1486	105	8	1466	107	**1465**	**107**	1465	107
433.milc	8	**4164**	**17.6**	4164	17.6	4164	17.6	8	4048	18.1	**4049**	**18.1**	4049	18.1
434.zeusmp	8	1510	48.2	**1500**	**48.5**	1497	48.6	8	1462	49.8	1461	49.8	**1462**	**49.8**
435.gromacs	8	634	90.1	**631**	**90.5**	629	90.8	8	**627**	**91.1**	628	90.9	626	91.3
436.cactusADM	8	1806	52.9	**1805**	**53.0**	1804	53.0	8	1697	56.3	**1702**	**56.2**	1704	56.1
437.leslie3d	8	4046	18.6	4076	18.4	**4064**	**18.5**	8	**3987**	**18.9**	3993	18.8	3974	18.9
444.namd	8	813	78.9	**813**	**78.9**	813	78.9	8	809	79.3	**809**	**79.3**	809	79.3
447.dealII	8	**958**	**95.5**	957	95.6	967	94.6	8	893	102	**914**	**100**	919	99.6
450.soplex	8	2836	23.5	2843	23.5	**2839**	**23.5**	8	2589	25.8	**2587**	**25.8**	2587	25.8
453.povray	8	**359**	**119**	359	118	358	119	8	306	139	**302**	**141**	302	141
454.calculix	8	897	73.6	**890**	**74.2**	889	74.2	8	**648**	**102**	647	102	650	102
459.GemsFDTD	8	5114	16.6	**5104**	**16.6**	5103	16.6	8	4874	17.4	4880	17.4	**4879**	**17.4**
465.tonto	8	1201	65.5	1208	65.2	**1207**	**65.2**	8	**1175**	**67.0**	1162	67.7	1177	66.9
470.lbm	8	5728	19.2	**5728**	**19.2**	5728	19.2	8	5309	20.7	**5309**	**20.7**	5308	20.7
481.wrf	8	2383	37.5	**2385**	**37.5**	2393	37.3	8	2385	37.5	2392	37.4	**2389**	**37.4**
482.sphinx3	8	4542	34.3	4533	34.4	**4535**	**34.4**	8	4173	37.4	4176	37.3	**4174**	**37.4**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
taskset utility used to bind CPU(s) to processes
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Fortran benchmarks:
`ifort`

Continued on next page

Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks (except as noted below):
/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/include

Continued on next page

Peak Compiler Invocation (Continued)

433.milc: icc

C++ benchmarks (except as noted below):
icpc

450.soplex: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/bin/icpc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/include

Fortran benchmarks (except as noted below):
ifort

437.leslie3d: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/bin/ifort
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/include

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias
-auto-ilp32

470.lbm: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-scalar-rep- -prefetch -opt-malloc-options=3

Continued on next page

Peak Optimization Flags (Continued)

482.sphinx3: -fast -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias -auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -fast -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -fast -prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -fast

437.leslie3d: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch -opt-malloc-options=3

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0 -prefetch

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -prefetch -auto-ilp32

454.calculix: -fast -unroll-aggressive -auto-ilp32

481.wrf: -fast -auto-ilp32

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.21.html

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.21.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 14:48:16 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 16 October 2007.