SPEC® CINT2006 Result

NEC Corporation

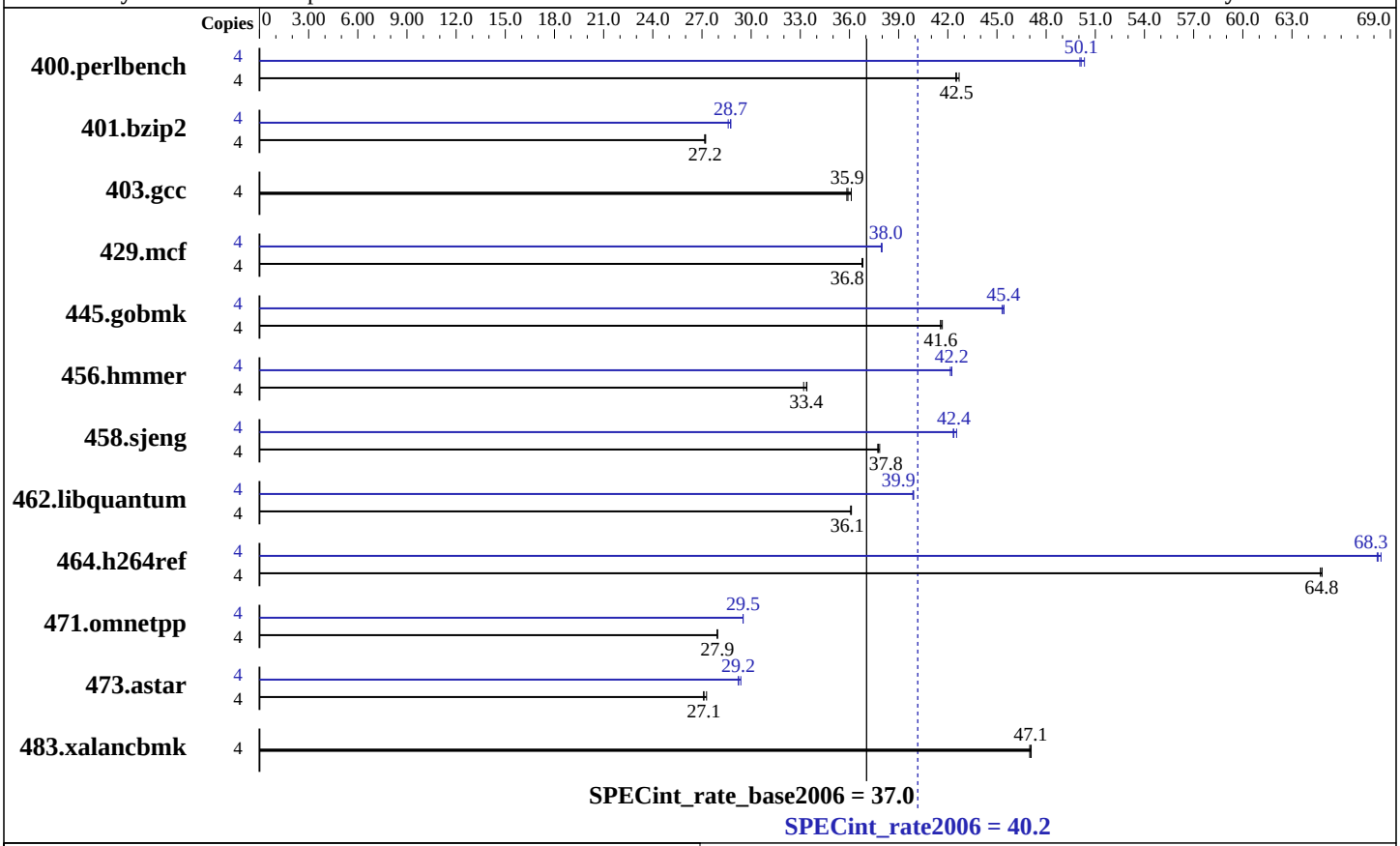
Express5800/120Li
(Intel Xeon processor 5110)

SPECint®_rate2006 = 40.2

SPECint_rate_base2006 = 37.0

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Oct-2007
Hardware Availability: May-2007
Software Availability: Jun-2007



Hardware

CPU Name:	Intel Xeon 5110
CPU Characteristics:	1.60 GHz, 4 MB L2, 1066 MHz bus
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (8x1 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem:	1x73.2 GB SAS, 15000RPM
Other Hardware:	None

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp for x86_64
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 10.0 - Build 20070426 Package ID: l_cc_p_10.0.023
Auto Parallel:	No
File System:	ext2
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap library 8.1 binutils-2.17.tar.gz, Version 2.17

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	915	42.7	920	42.5	**919**	**42.5**	4	**779**	**50.1**	781	50.1	776	50.3
401.bzip2	4	1418	27.2	1421	27.2	**1420**	**27.2**	4	1350	28.6	**1343**	**28.7**	1342	28.8
403.gcc	4	898	35.8	892	36.1	**897**	**35.9**	4	898	35.8	892	36.1	**897**	**35.9**
429.mcf	4	991	36.8	**991**	**36.8**	992	36.8	4	961	38.0	**961**	**38.0**	961	37.9
445.gobmk	4	1007	41.7	1010	41.6	**1010**	**41.6**	4	924	45.4	926	45.3	**924**	**45.4**
456.hmmer	4	1124	33.2	**1118**	**33.4**	1117	33.4	4	883	42.2	885	42.2	**884**	**42.2**
458.sjeng	4	**1282**	**37.8**	1279	37.8	1283	37.7	4	1138	42.5	**1142**	**42.4**	1143	42.3
462.libquantum	4	2295	36.1	2297	36.1	**2297**	**36.1**	4	2079	39.9	**2078**	**39.9**	2076	39.9
464.h264ref	4	**1366**	**64.8**	1368	64.7	1365	64.8	4	1294	68.4	**1297**	**68.3**	1298	68.2
471.omnetpp	4	895	27.9	**895**	**27.9**	895	27.9	4	847	29.5	**847**	**29.5**	847	29.5
473.astar	4	**1035**	**27.1**	1036	27.1	1029	27.3	4	961	29.2	956	29.4	**961**	**29.2**
483.xalancbmk	4	587	47.0	586	47.1	**587**	**47.1**	4	587	47.0	586	47.1	**587**	**47.1**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs
```

General Notes

```
All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
for peak, are compiled in 64-bit mode
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`

Continued on next page

Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc
-L/opt/intel/cce/10.0.023/lib
-I/opt/intel/cce/10.0.023/include

456.hmmer: /opt/intel/cce/10.0.023/bin/icc
-L/opt/intel/cce/10.0.023/lib
-I/opt/intel/cce/10.0.023/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias -prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast`

403.gcc: `basepeak = yes`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O2 -ipo -no-prec_div -ansi-alias`

456.hmmer: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4`

462.libquantum: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always`

464.h264ref: `Same as 456.hmmer`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec_div -ansi-alias -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap`

473.astar: `Same as 471.omnetpp`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/NEC-ic10-INT-ia32-intel64-linux-flags.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/NEC-ic10-INT-ia32-intel64-linux-flags.xml`

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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