



SPEC® CINT2006 Result

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Intel Corporation

Intel D975XBX2 motherboard (Intel Core 2 Quad QX6800)

SPECint®2006 = 22.1

SPECint_base2006 = 20.2

CPU2006 license: 13

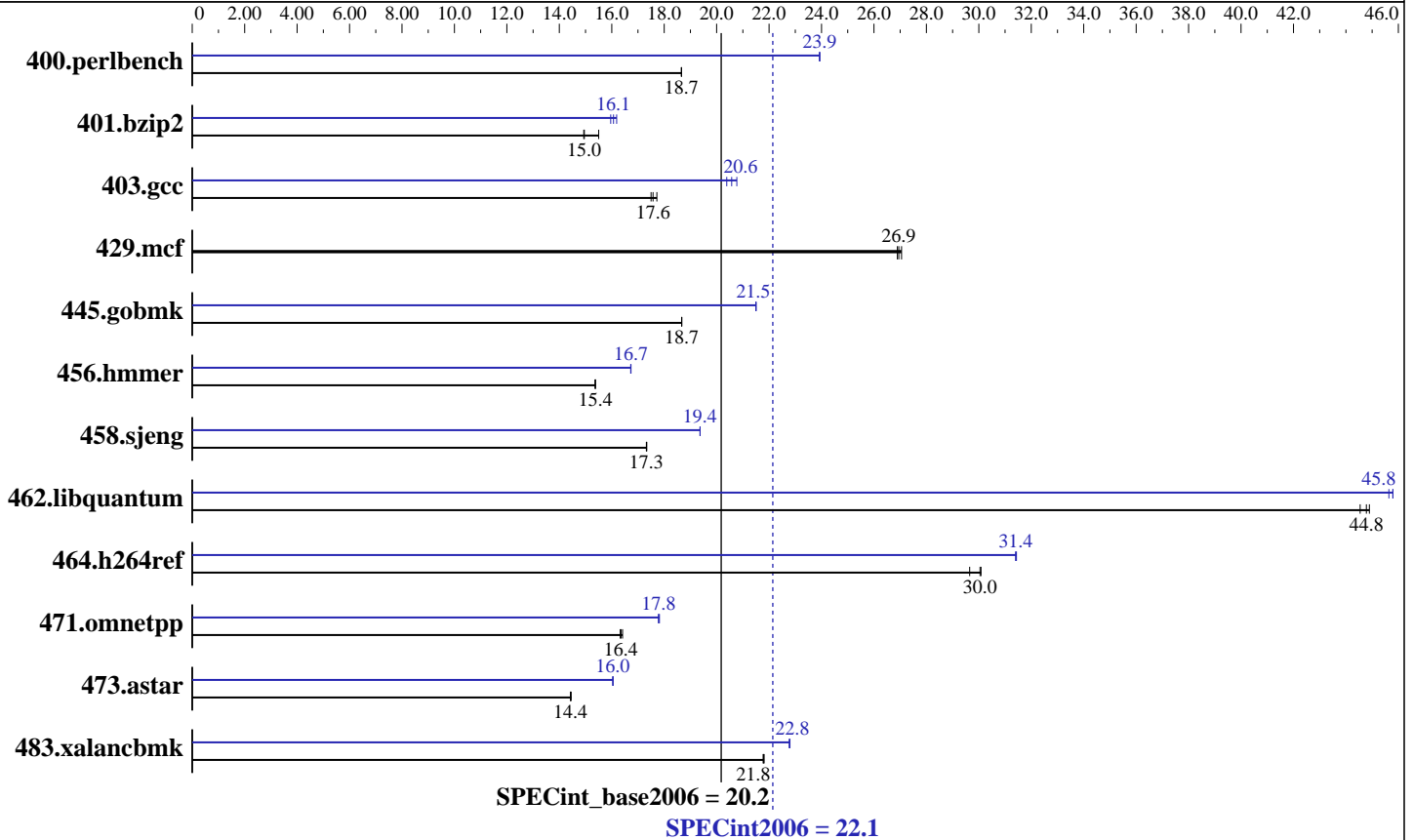
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Nov-2007

Hardware Availability: Sep-2007

Software Availability: Nov-2007



Hardware

CPU Name: Intel Core 2 Quad QX6800
 CPU Characteristics: 2.93 GHz 1066 MHz FSB
 CPU MHz: 2933
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 4 GB (4x1GB Micron MT16HTF12864AY-80ED4 DDR2-800 CL5)
 Disk Subsystem: Seagate 320GB NCQ SATA, 16MB cache, 7200 RPM
 Other Hardware: None

Software

Operating System: Windows Vista64 Ultimate
 Compiler: Intel C++ Compiler for IA32 version 10.1
 Build 20070913 Package ID: w_cc_p_10.1.011
 Microsoft Visual Studio 2005 SP1 (for libraries)
 Auto Parallel: Yes
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: SmartHeap Library Version 8.1 from <http://www.microquill.com/>



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	524	18.7	524	18.7	<u>524</u>	<u>18.7</u>	408	23.9	<u>408</u>	<u>23.9</u>	408	23.9
401.bzip2	<u>645</u>	<u>15.0</u>	646	14.9	623	15.5	<u>601</u>	<u>16.1</u>	605	16.0	596	16.2
403.gcc	<u>458</u>	<u>17.6</u>	460	17.5	454	17.7	388	20.8	<u>391</u>	<u>20.6</u>	395	20.4
429.mcf	337	27.1	339	26.9	<u>338</u>	<u>26.9</u>	337	27.1	339	26.9	<u>338</u>	<u>26.9</u>
445.gobmk	562	18.7	562	18.7	<u>562</u>	<u>18.7</u>	488	21.5	<u>488</u>	<u>21.5</u>	488	21.5
456.hammer	607	15.4	607	15.4	<u>607</u>	<u>15.4</u>	<u>558</u>	<u>16.7</u>	558	16.7	558	16.7
458.sjeng	698	17.3	699	17.3	<u>698</u>	<u>17.3</u>	625	19.4	<u>625</u>	<u>19.4</u>	625	19.4
462.libquantum	465	44.5	<u>463</u>	<u>44.8</u>	462	44.9	454	45.6	<u>453</u>	<u>45.8</u>	452	45.8
464.h264ref	<u>737</u>	<u>30.0</u>	746	29.6	736	30.1	705	31.4	704	31.4	<u>705</u>	<u>31.4</u>
471.omnetpp	381	16.4	383	16.3	<u>382</u>	<u>16.4</u>	351	17.8	<u>351</u>	<u>17.8</u>	351	17.8
473.astar	486	14.5	<u>487</u>	<u>14.4</u>	487	14.4	438	16.0	<u>438</u>	<u>16.0</u>	438	16.0
483.xalancbmk	316	21.8	<u>317</u>	<u>21.8</u>	317	21.8	303	22.8	<u>303</u>	<u>22.8</u>	303	22.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with Shin-G ATX case, Antec NeoPower 480W power supply
Product description located as of 11/2007:

<http://www.intel.com/products/motherboard/D975XBX2/index.htm>

The system bus runs at 1066 MHz

System was configured with Asus EN8800GTX discrete graphics card

Binaries were built on Windows Vista32

The following VS 2005 SP1 updates were applied: KB926601 and KB932232

Base Compiler Invocation

C benchmarks:

```
icl -Qvc8 -Qc99
```

C++ benchmarks:

```
icl -Qvc8
```

Base Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
```

```
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

```
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```



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Base Optimization Flags

C benchmarks:
-fast -Qparallel -Qpar-runtime-control -Qvec-guard-write /F512000000
libguide40.lib

C++ benchmarks:
-fast -Qcxx_features /F512000000 shlw32m.lib libguide40.lib
-link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qvc8 -Qc99

C++ benchmarks:
icl -Qvc8

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qprefetch -Qparallel -Qpar-runtime-control /F512000000
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE
401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch
/F512000000 libguide40.lib
403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
libguide40.lib

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Peak Optimization Flags (Continued)

429.mcf: basepeak = yes

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo
-Qprec-div- -Qansi-alias /F512000000

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias -Qopt-multi-version-aggressive /F512000000
libguide40.lib

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
/F512000000 libguide40.lib

462.libquantum: -fast -Qunroll4 -Ob0 -Qprefetch
-Qopt-streaming-stores:always -Qparallel
-Qpar-runtime-control /F512000000 libguide40.lib

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias /F512000000 libguide40.lib

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qopt-ra-region-strategy=block -Qcxx_features /F512000000
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qopt-ra-region-strategy=routine -Qcxx_features /F512000000
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qcxx_features /F512000000 shlw32m.lib libguide40.lib
-link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.09.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.09.xml>



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For other inquiries, please contact webmaster@spec.org.

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