



SPEC® CINT2006 Result

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Dell Inc.

SPECint®_rate2006 = 53.1

PowerEdge 2950 (Intel Xeon 5130, 2.00 GHz)

SPECint_rate_base2006 = 46.7

CPU2006 license: 55

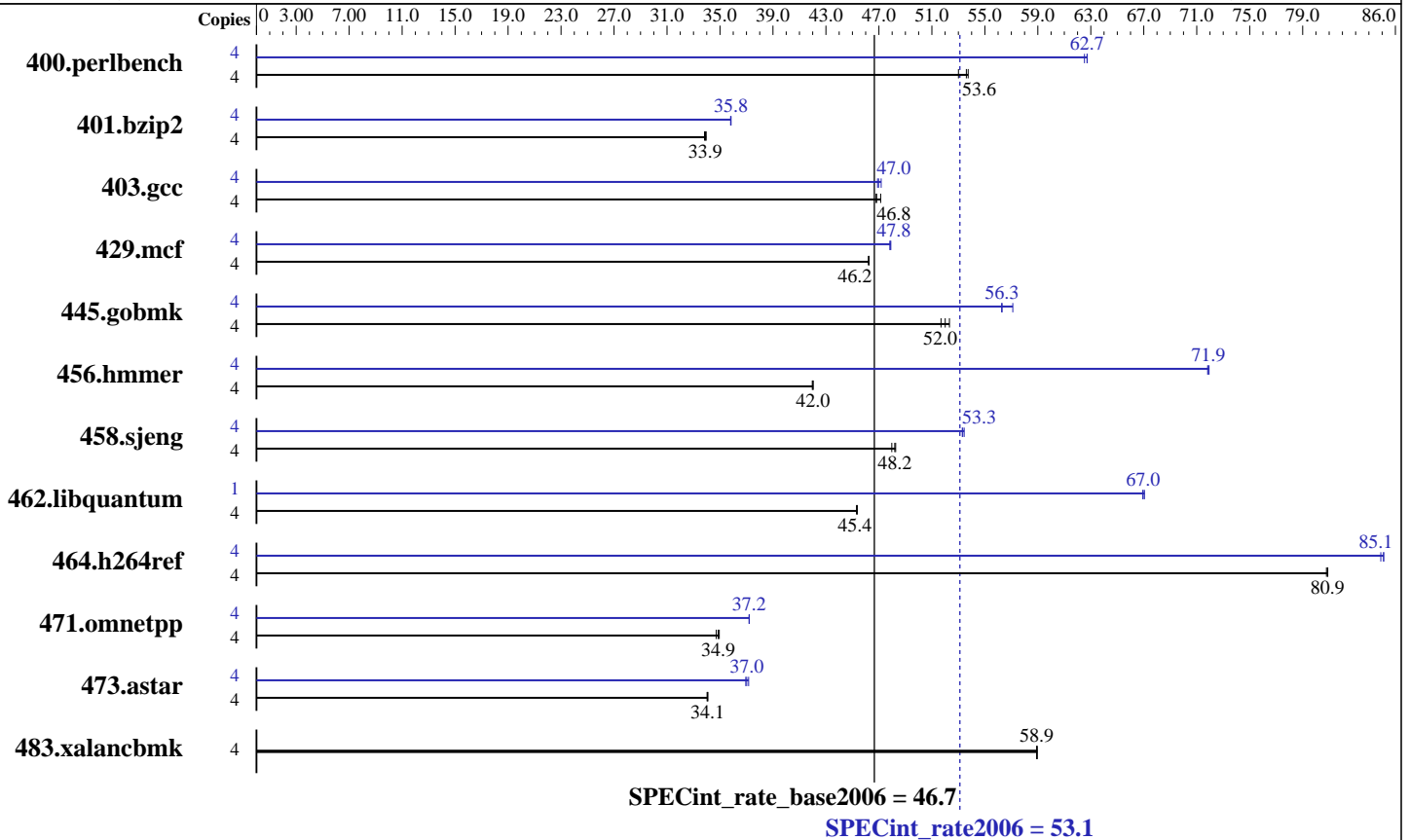
Test date: Oct-2007

Test sponsor: Dell Inc.

Hardware Availability: Dec-2006

Tested by: Dell Inc.

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon 5130
 CPU Characteristics: 1333 MHz system bus
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2 GB 667 MHz ECC CL5 FB-DIMM)
 Disk Subsystem: 1 x 73 GB SAS 15k RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070725
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap library V8.1



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Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|----------------|--------|-------------|-------------|-------------|-------------|-------------|-------------|--------|------------|-------------|-------------|-------------|---------|-------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 4 | 737 | 53.0 | 727 | 53.7 | 729 | 53.6 | 4 | 623 | 62.7 | 623 | 62.7 | 625 | 62.5 |
| 401.bzip2 | 4 | 1139 | 33.9 | 1141 | 33.8 | 1137 | 34.0 | 4 | 1077 | 35.8 | 1077 | 35.8 | 1078 | 35.8 |
| 403.gcc | 4 | 688 | 46.8 | 683 | 47.1 | 688 | 46.8 | 4 | 687 | 46.9 | 685 | 47.0 | 683 | 47.2 |
| 429.mcf | 4 | 789 | 46.2 | 789 | 46.2 | 789 | 46.3 | 4 | 762 | 47.8 | 762 | 47.9 | 763 | 47.8 |
| 445.gobmk | 4 | 802 | 52.3 | 812 | 51.7 | 807 | 52.0 | 4 | 735 | 57.1 | 745 | 56.3 | 746 | 56.3 |
| 456.hammer | 4 | 889 | 42.0 | 888 | 42.0 | 888 | 42.0 | 4 | 519 | 71.9 | 519 | 71.9 | 520 | 71.8 |
| 458.sjeng | 4 | 1009 | 48.0 | 1004 | 48.2 | 1003 | 48.3 | 4 | 906 | 53.4 | 908 | 53.3 | 908 | 53.3 |
| 462.libquantum | 4 | 1829 | 45.3 | 1827 | 45.4 | 1827 | 45.4 | 1 | 309 | 67.1 | 309 | 67.0 | 310 | 66.9 |
| 464.h264ref | 4 | 1096 | 80.8 | 1095 | 80.9 | 1094 | 80.9 | 4 | 1043 | 84.9 | 1040 | 85.1 | 1040 | 85.1 |
| 471.omnetpp | 4 | 717 | 34.9 | 715 | 34.9 | 720 | 34.7 | 4 | 672 | 37.2 | 672 | 37.2 | 672 | 37.2 |
| 473.astar | 4 | 824 | 34.1 | 825 | 34.0 | 824 | 34.1 | 4 | 758 | 37.0 | 756 | 37.2 | 760 | 36.9 |
| 483.xalancbmk | 4 | 468 | 58.9 | 468 | 58.9 | 468 | 59.0 | 4 | 468 | 58.9 | 468 | 58.9 | 468 | 59.0 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

OMP_NUM_THREADS set to number of cores
KMP_STACK_SIZE set to 64M
KMP_AFFINITY set to physical,0

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs

Platform Notes

BIOS Settings:
Adjacent Cache Line Prefetch = Disabled (default Enabled)

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalanbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.02.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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