



# SPEC® CINT2006 Result

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## Fujitsu Siemens Computers

PRIMERGY BX620 S4, Intel Xeon processor L5335,  
2.0 GHz

SPECint®\_rate2006 = 91.1

SPECint\_rate\_base2006 = 78.9

CPU2006 license: 22

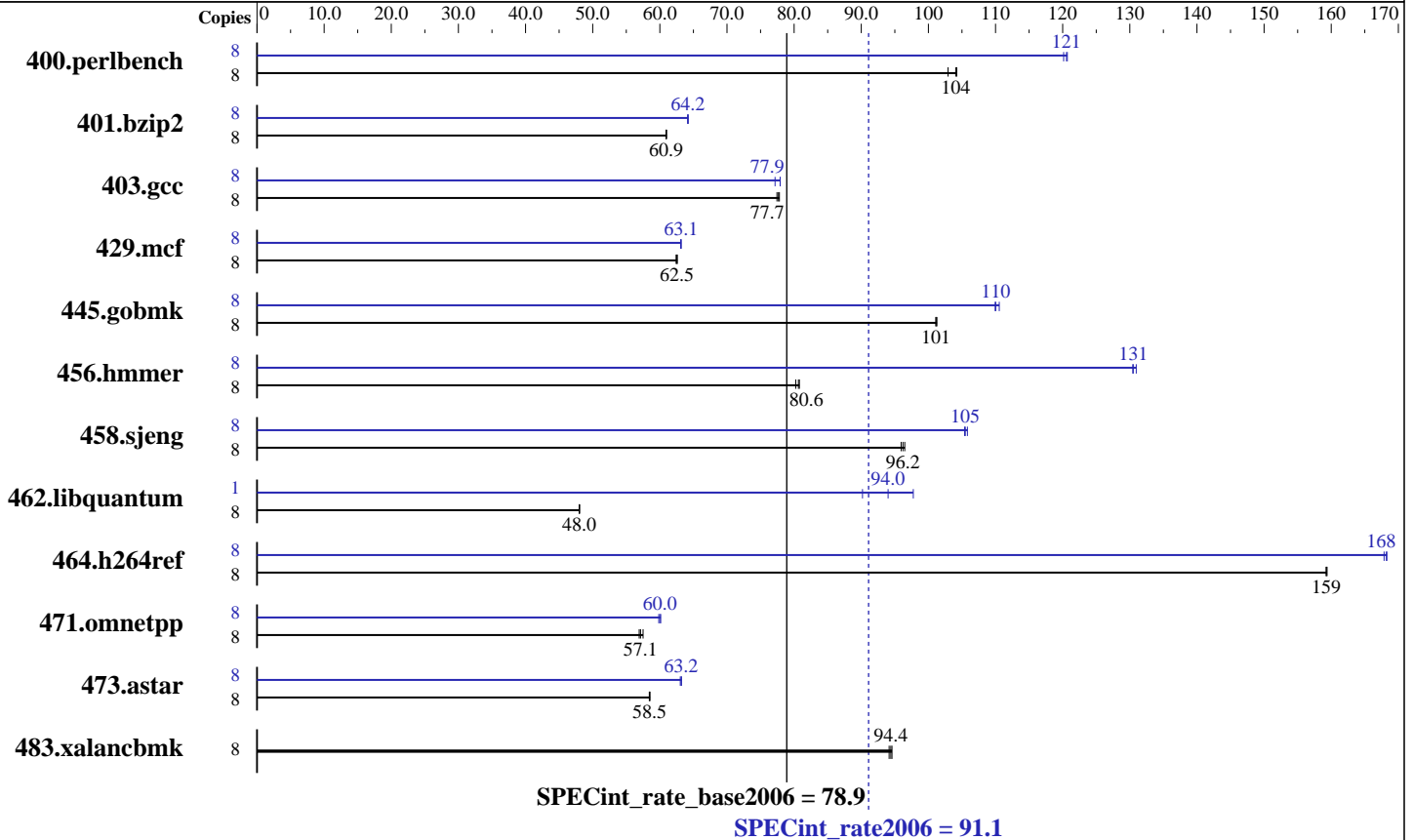
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Dec-2007

Hardware Availability: Sep-2007

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon L5335  
 CPU Characteristics: 1333 MHz system bus  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8x2 GB PC2-5300F, 2 rank, CAS 5-5-5, with ECC)  
 Disk Subsystem: Seagate ST973451SS (SAS, 73GB, 15000rpm)  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler for Linux32 and Linux64 Version 10.1 - Build 20070725  
 Auto Parallel: Yes  
 File System: ext2  
 System State: Multiuser, Runlevel 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: MicroQuill Smart Heap Library, Version 8.1  
 binutils-2.17.tar.gz, Version 2.17



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	759	103	750	104	<u>751</u>	<u>104</u>	8	<u>648</u>	<u>121</u>	651	120	648	121
401.bzip2	8	<u>1267</u>	<u>60.9</u>	1267	60.9	1265	61.0	8	<u>1202</u>	<u>64.2</u>	1202	64.2	1204	64.1
403.gcc	8	832	77.4	<u>829</u>	<u>77.7</u>	828	77.8	8	826	77.9	<u>827</u>	<u>77.9</u>	835	77.2
429.mcf	8	1169	62.4	1165	62.6	<u>1167</u>	<u>62.5</u>	8	1155	63.2	<u>1155</u>	<u>63.1</u>	1156	63.1
445.gobmk	8	830	101	829	101	<u>830</u>	<u>101</u>	8	763	110	<u>763</u>	<u>110</u>	759	111
456.hammer	8	<u>926</u>	<u>80.6</u>	930	80.2	924	80.8	8	570	131	<u>572</u>	<u>131</u>	572	130
458.sjeng	8	1003	96.5	<u>1006</u>	<u>96.2</u>	1009	95.9	8	<u>918</u>	<u>105</u>	915	106	918	105
462.libquantum	8	3454	48.0	<u>3453</u>	<u>48.0</u>	3449	48.1	1	<u>220</u>	<u>94.0</u>	230	90.2	212	97.7
464.h264ref	8	1111	159	1112	159	<u>1112</u>	<u>159</u>	8	<u>1052</u>	<u>168</u>	1052	168	1055	168
471.omnetpp	8	878	56.9	<u>875</u>	<u>57.1</u>	870	57.5	8	836	59.8	832	60.1	<u>833</u>	<u>60.0</u>
473.astar	8	961	58.4	<u>961</u>	<u>58.5</u>	959	58.5	8	891	63.0	<u>889</u>	<u>63.2</u>	888	63.2
483.xalancbmk	8	584	94.6	<u>585</u>	<u>94.4</u>	586	94.1	8	584	94.6	<u>585</u>	<u>94.4</u>	586	94.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

OMP\_NUM\_THREADS set to number of cores (default).

## General Notes

This result has been produced with binaries provided and compiled by Intel.

All binaries were built with 32-bit Intel compiler except:  
401.bzip2 and 456.hammer in peak were built with 64-bit Intel  
compiler by changing the path for include and library files.

BIOS configuration:

Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers please see:

<http://www.fujitsu-siemens.com>

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc  
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib  
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc  
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib  
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX

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## Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalanbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.10.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.10.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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