



SPEC® CINT2006 Result

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IBM Corporation

SPECint®_rate2006 = Not Run

IBM System x3650 (Intel Xeon E5440)

SPECint_rate_base2006 = 106

CPU2006 license: 11

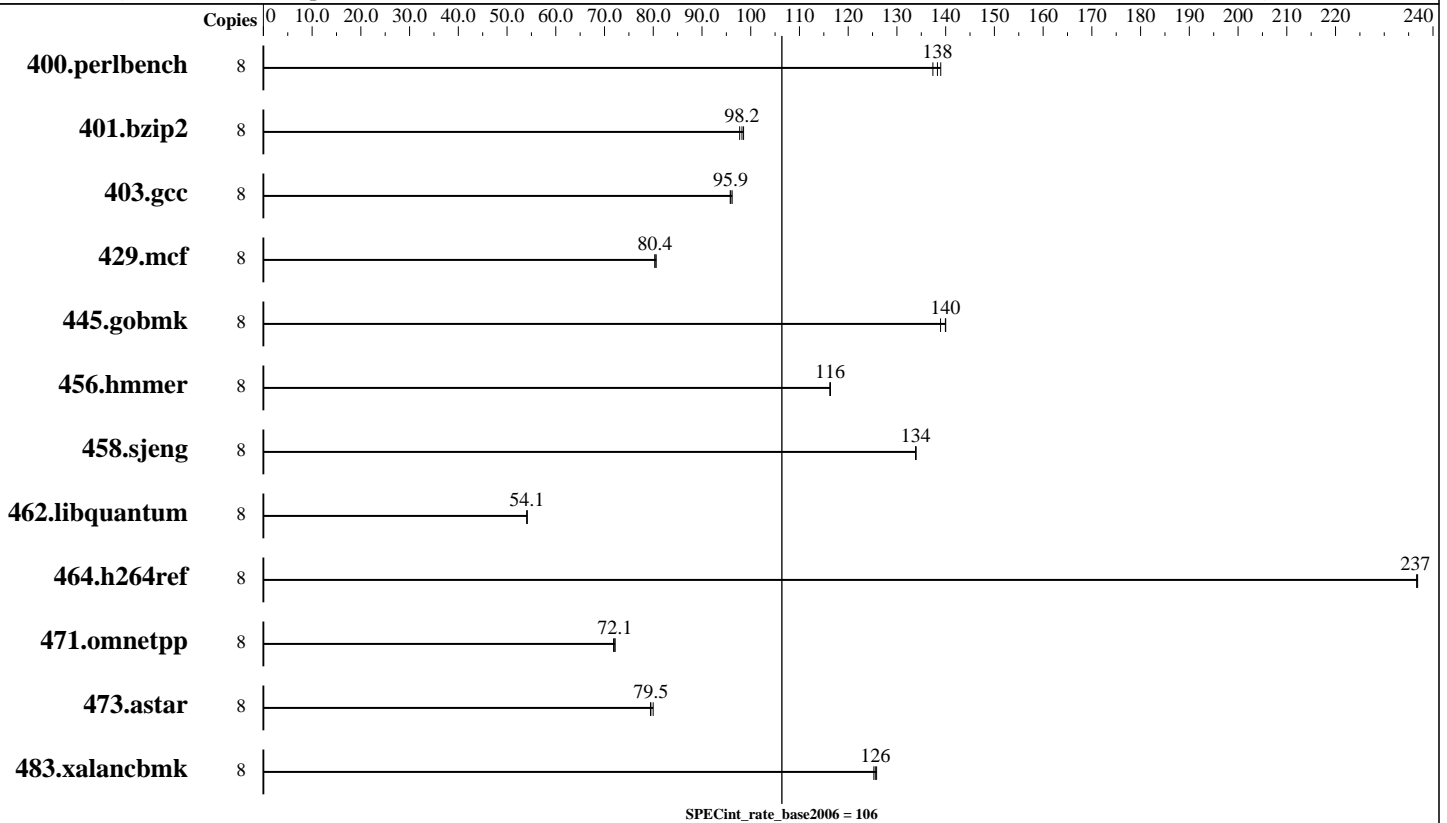
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Jan-2008

Hardware Availability: Jan-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5440
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 2826
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 x 2 GB DDR2-5300F ECC)
 Disk Subsystem: 1 x 36 GB SAS, 15000 RPM
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64), kernel 2.6.16.21-0.8-smp
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: No
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: MicroQuill SmartHeap 8.1 Binutils 2.17.50.0.15



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	569	137	<u>565</u>	<u>138</u>	562	139							
401.bzip2	8	<u>786</u>	<u>98.2</u>	784	98.5	790	97.7							
403.gcc	8	670	96.2	<u>671</u>	<u>95.9</u>	672	95.8							
429.mcf	8	905	80.6	<u>908</u>	<u>80.4</u>	908	80.3							
445.gobmk	8	<u>600</u>	<u>140</u>	599	140	604	139							
456.hammer	8	641	116	642	116	<u>642</u>	<u>116</u>							
458.sjeng	8	723	134	723	134	<u>723</u>	<u>134</u>							
462.libquantum	8	<u>3064</u>	<u>54.1</u>	3066	54.1	3063	54.1							
464.h264ref	8	<u>748</u>	<u>237</u>	747	237	748	237							
471.omnetpp	8	693	72.1	696	71.9	<u>693</u>	<u>72.1</u>							
473.astar	8	<u>706</u>	<u>79.5</u>	707	79.4	702	80.0							
483.xalancbmk	8	439	126	441	125	<u>440</u>	<u>126</u>							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

All benchmarks compiled in 32-bit mode
Hardware Sector Prefetch Disabled and Adjacent Sector Prefetch Disabled
Powersaved dameon was disabled in OS
taskset utility used to bind CPU(s) to processes

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

`-fast -inline-calloc -opt-malloc-options=3`

C++ benchmarks:

`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap`

Base Other Flags

C benchmarks:

`403.gcc: -Dalloca=_alloca`

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.19.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.19.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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