



SPEC® CINT2006 Result

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Acer Incorporated

SPECint®_rate2006 = Not Run

Acer Altos R720 (Intel Xeon E5405)

SPECint_rate_base2006 = 84.6

CPU2006 license: 97

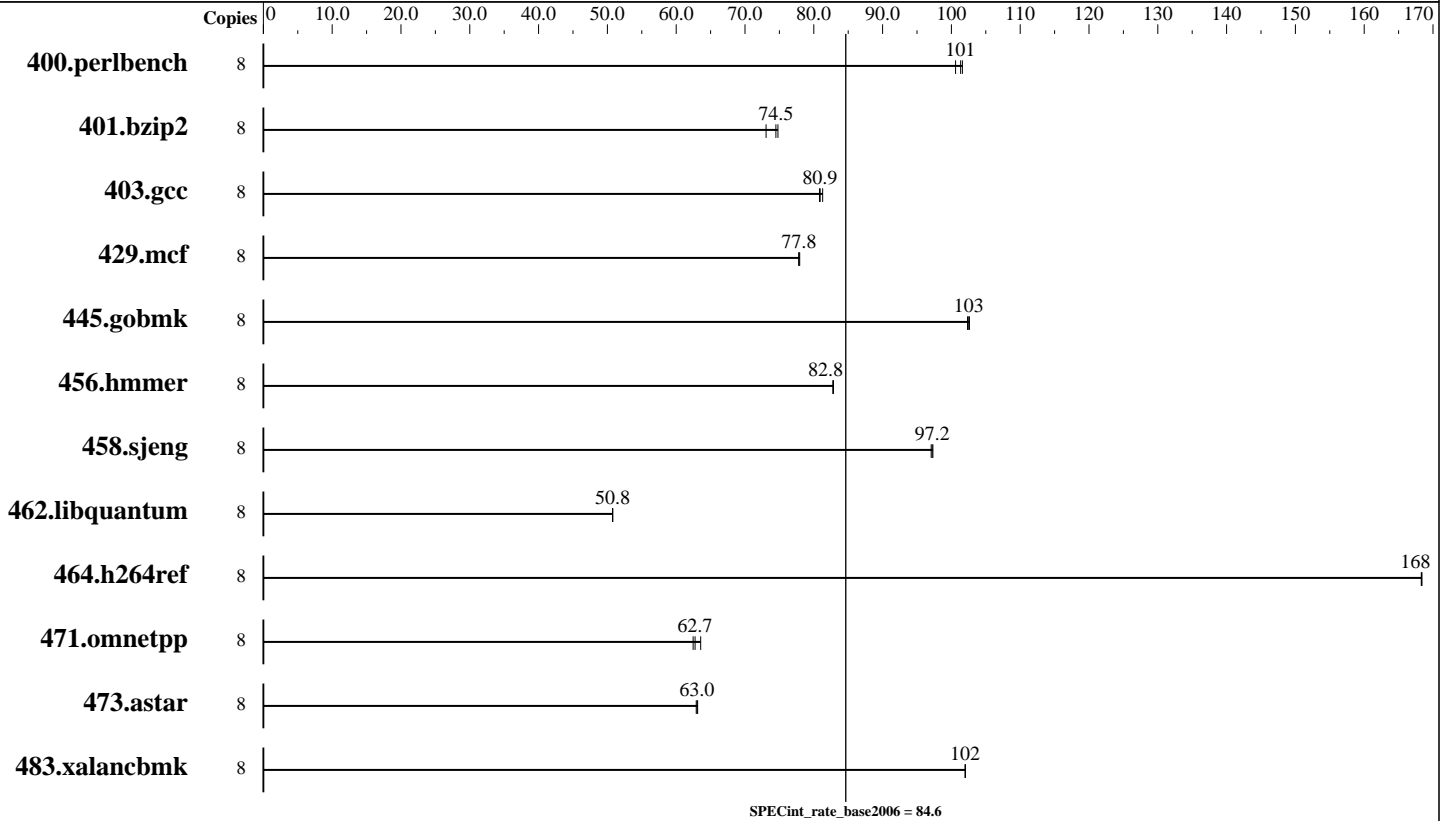
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Jan-2008

Hardware Availability: Nov-2007

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5405
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1, 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 x 2048MB ECC FB-DIMM DDR2-667 CL5-5-5)
 Disk Subsystem: 1 x 73GB 10000PRM SAS HDD
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 SP1 (x86_64)
 Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 10.1 for Linux
 Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, level 3
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: Microquill SmartHeap V8.1
 Binutils 2.17.50.0.15



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	769	102	<u>771</u>	<u>101</u>	777	101							
401.bzip2	8	1057	73.1	1032	74.8	<u>1036</u>	<u>74.5</u>							
403.gcc	8	<u>796</u>	<u>80.9</u>	792	81.3	797	80.8							
429.mcf	8	936	78.0	<u>937</u>	<u>77.8</u>	938	77.8							
445.gobmk	8	<u>819</u>	<u>103</u>	820	102	818	103							
456.hmmmer	8	901	82.9	<u>901</u>	<u>82.8</u>	902	82.8							
458.sjeng	8	995	97.3	997	97.1	<u>996</u>	<u>97.2</u>							
462.libquantum	8	<u>3265</u>	<u>50.8</u>	3265	50.8	3266	50.8							
464.h264ref	8	1052	168	<u>1052</u>	<u>168</u>	1051	168							
471.omnetpp	8	787	63.6	800	62.5	<u>797</u>	<u>62.7</u>							
473.astar	8	<u>891</u>	<u>63.0</u>	890	63.1	893	62.9							
483.xalancbmk	8	541	102	541	102	<u>541</u>	<u>102</u>							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

This result was measured on the Acer Altos R520.
The Altos R720 and Altos R520 are electronically equivalent.

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmmer, for peak, are compiled in 64-bit mode

BIOS Settings

Hardware Prefetcher set to OFF

Adjacent Cache Line Prefetch set to OFF

All results used taskset to bind processes to cores.
"ulimit -s unlimited" was set for stacksize unlimited

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.08.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.08.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.

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