SPEC® CINT2006 Result
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Fujitsu Siemens Computers

PRIMERGY TX300 S4, Intel Xeon L5420, 2.50 GHz

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 97.8

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Jan-2008
Hardware Availability: Mar-2008
Software Availability: Nov-2007

Benchmark	Copies	Result
400.perlbench	8	125
401.bzip2	8	87.8
403.gcc	8	89.6
429.mcf	8	79.5
445.gobmk	8	126
456.hmmer	8	103
458.sjeng	8	121
462.libquantum	8	50.8
464.h264ref	8	207
471.omnetpp	8	68.2
473.astar	8	74.5
483.xalancbmk	8	117

SPECint_rate_base2006 = 97.8

Hardware
CPU Name: Intel Xeon L5420
CPU Characteristics: 1333 MHz system bus
CPU MHz: 2500
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem: Seagate ST973451SS (SAS, 73GB, 15000rpm)
Other Hardware: None

Software
Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler: Intel C++ Compiler for Linux32 and Linux64 Version 10.1 - Build 20070725
Auto Parallel: No
File System: ext2
System State: Multiuser, Runlevel 3
Base Pointers: 32-bit
Peak Pointers: Not Applicable
Other Software: MicroQuill SmartHeap Library, Version 8.1 binutils-2.17.tar.gz, Version 2.17

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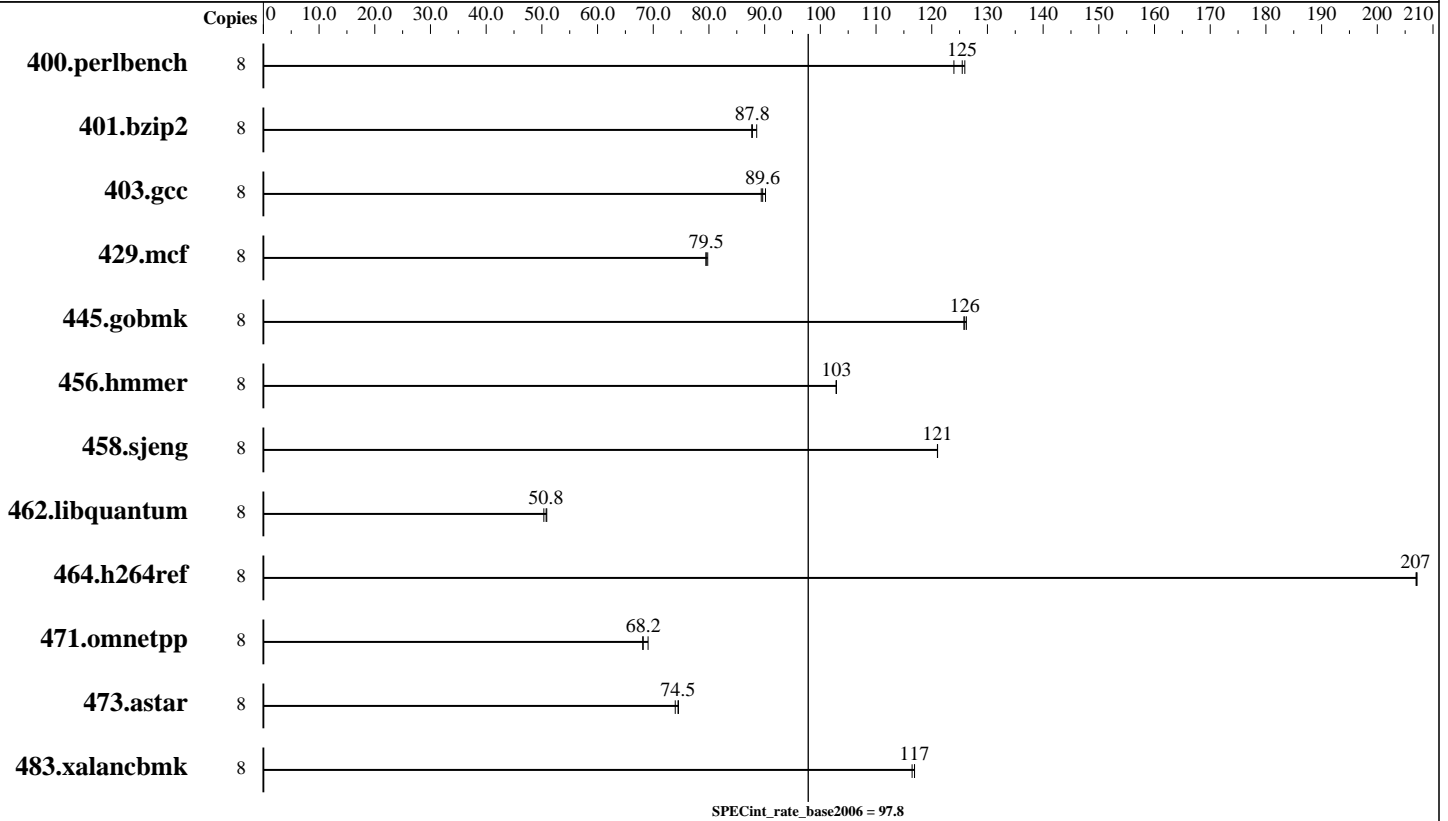
SPECint_rate_base2006 = 97.8

Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	**623**	**125**	630	124	621	126							
401.bzip2	8	880	87.7	**879**	**87.8**	872	88.6							
403.gcc	8	720	89.4	714	90.2	**718**	**89.6**							
429.mcf	8	918	79.5	915	79.8	**918**	**79.5**							
445.gobmk	8	667	126	665	126	**666**	**126**							
456.hmmer	8	**726**	**103**	726	103	725	103							
458.sjeng	8	800	121	800	121	**800**	**121**							
462.libquantum	8	3292	50.4	**3265**	**50.8**	3258	50.9							
464.h264ref	8	**855**	**207**	855	207	855	207							
471.omnetpp	8	734	68.1	**733**	**68.2**	724	69.1							
473.astar	8	754	74.5	759	73.9	**754**	**74.5**							
483.xalancbmk	8	474	116	**472**	**117**	472	117							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

This result has been produced with binaries provided and compiled by Intel.

BIOS configuration:
Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com

Base Compiler Invocation

C benchmarks:
     icc

C++ benchmarks:
     icpc

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

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Tested by:	Fujitsu Siemens Computers	**Software Availability:**	Nov-2007

Base Optimization Flags

C benchmarks:
```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

    403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.01.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.01.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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