



SPEC® CINT2006 Result

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IBM Corporation

SPECint®_rate2006 = 26.8

IBM BladeCenter HS12 (Intel Core 2 Duo E6405)

SPECint_rate_base2006 = 23.3

CPU2006 license: 11

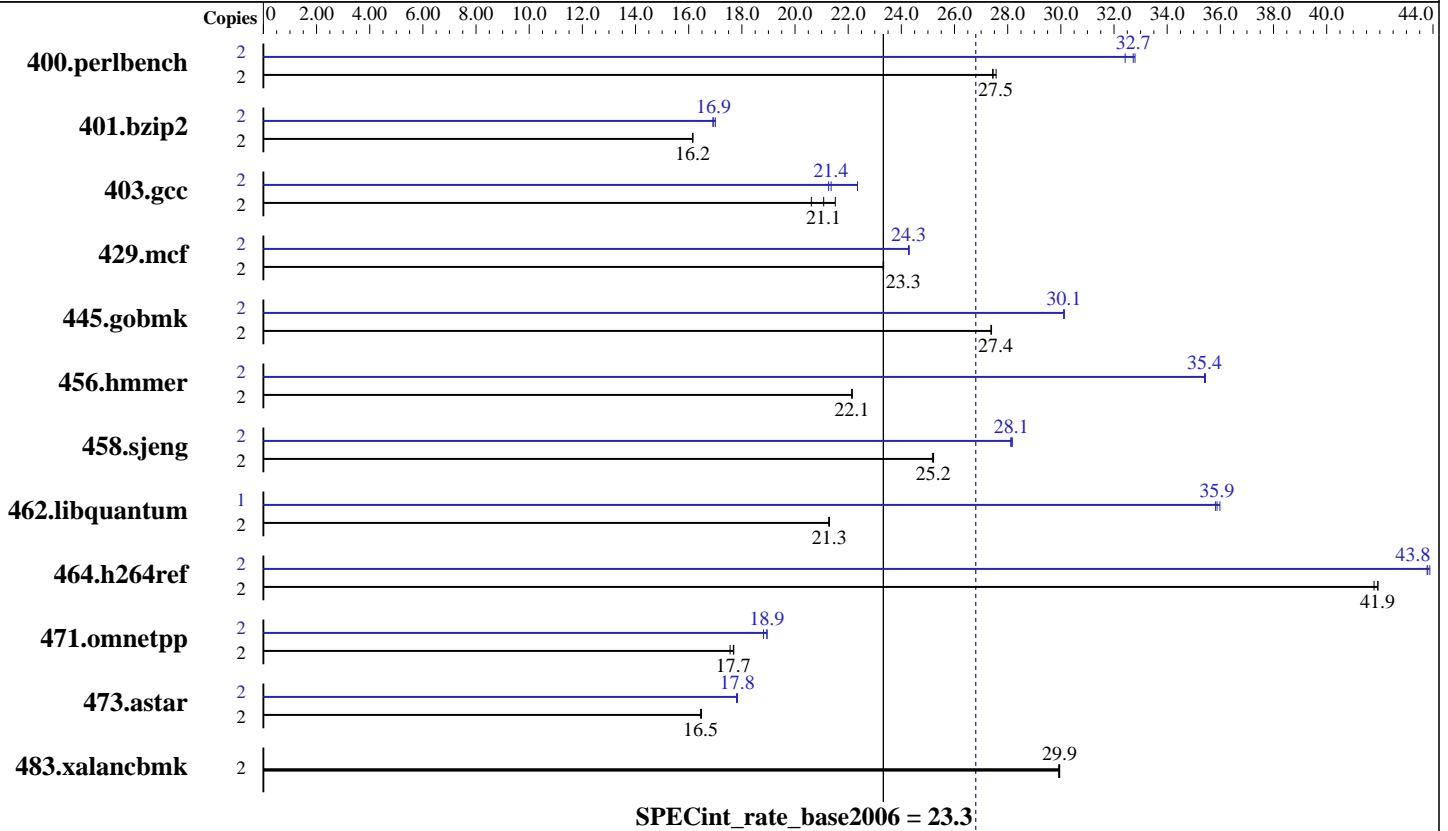
Test date: Apr-2008

Test sponsor: IBM Corporation

Hardware Availability: May-2008

Tested by: IBM Corporation

Software Availability: Nov-2007



Hardware

CPU Name: Intel Core 2 Duo E6405
 CPU Characteristics: 1066MHz system bus
 CPU MHz: 2133
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 2 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4 x 2 GB DDR2-5300 ECC)
 Disk Subsystem: 1 x 73 GB SAS, 10000 RPM
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap 8.1 Binutils 2.17.50.0.15



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	<u>712</u>	<u>27.5</u>	709	27.6	712	27.4	2	<u>597</u>	<u>32.7</u>	596	32.8	603	32.4
401.bzip2	2	1195	16.2	1195	16.2	<u>1195</u>	<u>16.2</u>	2	1135	17.0	1141	16.9	<u>1139</u>	<u>16.9</u>
403.gcc	2	781	20.6	<u>764</u>	<u>21.1</u>	748	21.5	2	757	21.3	<u>754</u>	<u>21.4</u>	720	22.4
429.mcf	2	<u>782</u>	<u>23.3</u>	782	23.3	782	23.3	2	751	24.3	752	24.3	<u>751</u>	<u>24.3</u>
445.gobmk	2	<u>766</u>	<u>27.4</u>	766	27.4	766	27.4	2	696	30.1	<u>697</u>	<u>30.1</u>	697	30.1
456.hammer	2	<u>843</u>	<u>22.1</u>	843	22.1	842	22.2	2	527	35.4	527	35.4	<u>527</u>	<u>35.4</u>
458.sjeng	2	<u>960</u>	<u>25.2</u>	960	25.2	962	25.2	2	859	28.2	861	28.1	<u>860</u>	<u>28.1</u>
462.libquantum	2	1949	21.3	<u>1946</u>	<u>21.3</u>	1946	21.3	1	579	35.8	576	36.0	<u>578</u>	<u>35.9</u>
464.h264ref	2	1056	41.9	1059	41.8	<u>1056</u>	<u>41.9</u>	2	<u>1010</u>	<u>43.8</u>	1009	43.9	1011	43.8
471.omnetpp	2	<u>707</u>	<u>17.7</u>	712	17.6	706	17.7	2	660	18.9	<u>660</u>	<u>18.9</u>	664	18.8
473.astar	2	852	16.5	<u>853</u>	<u>16.5</u>	853	16.5	2	<u>787</u>	<u>17.8</u>	789	17.8	787	17.8
483.xalancbmk	2	461	30.0	<u>461</u>	<u>29.9</u>	461	29.9	2	461	30.0	<u>461</u>	<u>29.9</u>	461	29.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hammer, for peak, are compiled in 64-bit mode
Hardware Sector Prefetch Enabled and Adjacent Sector Prefetch Disabled
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M
taskset utility used to bind CPU(s) to processes

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmr: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090713.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090713.xml>

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