SPEC® CINT2006 Result

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Fujitsu Siemens Computers

PRIMERGY RX300 S4, Intel Xeon X5260, 3.33 GHz

SPECint®2006 =   27.1

SPECint_base2006 =   22.7

CPU2006 license: 22

Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: May-2008

Hardware Availability: Dec-2007

Software Availability: Nov-2007

Benchmark	Peak	Base
400.perlbench	26.7	21.6
401.bzip2	19.5	18.0
403.gcc	20.8	17.1
429.mcf	24.9	23.1
445.gobmk	24.1	21.7
456.hmmer	30.4	17.5
458.sjeng	21.9	19.6
462.libquantum	116	65.1
464.h264ref	36.6	34.6
471.omnetpp	17.4	16.0
473.astar	18.1	16.6
483.xalancbmk		27.6

SPECint_base2006 = 22.7

SPECint2006 = 27.1

Hardware

CPU Name:	Intel Xeon X5260
CPU Characteristics:	1333 MHz system bus
CPU MHz:	3333
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB PC2-5300F, 2 rank, CL 5-5-5, ECC)
Disk Subsystem:	1x SAS, 73 GB, 15000 rpm
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler for Linux32 and Linux64, Version 10.1, Build 20070913
Auto Parallel:	Yes
File System:	ext2
System State:	Multi-User Run Level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap Library, Version 8.1 binutils-2.17.50.0.5-0.1.x86_64

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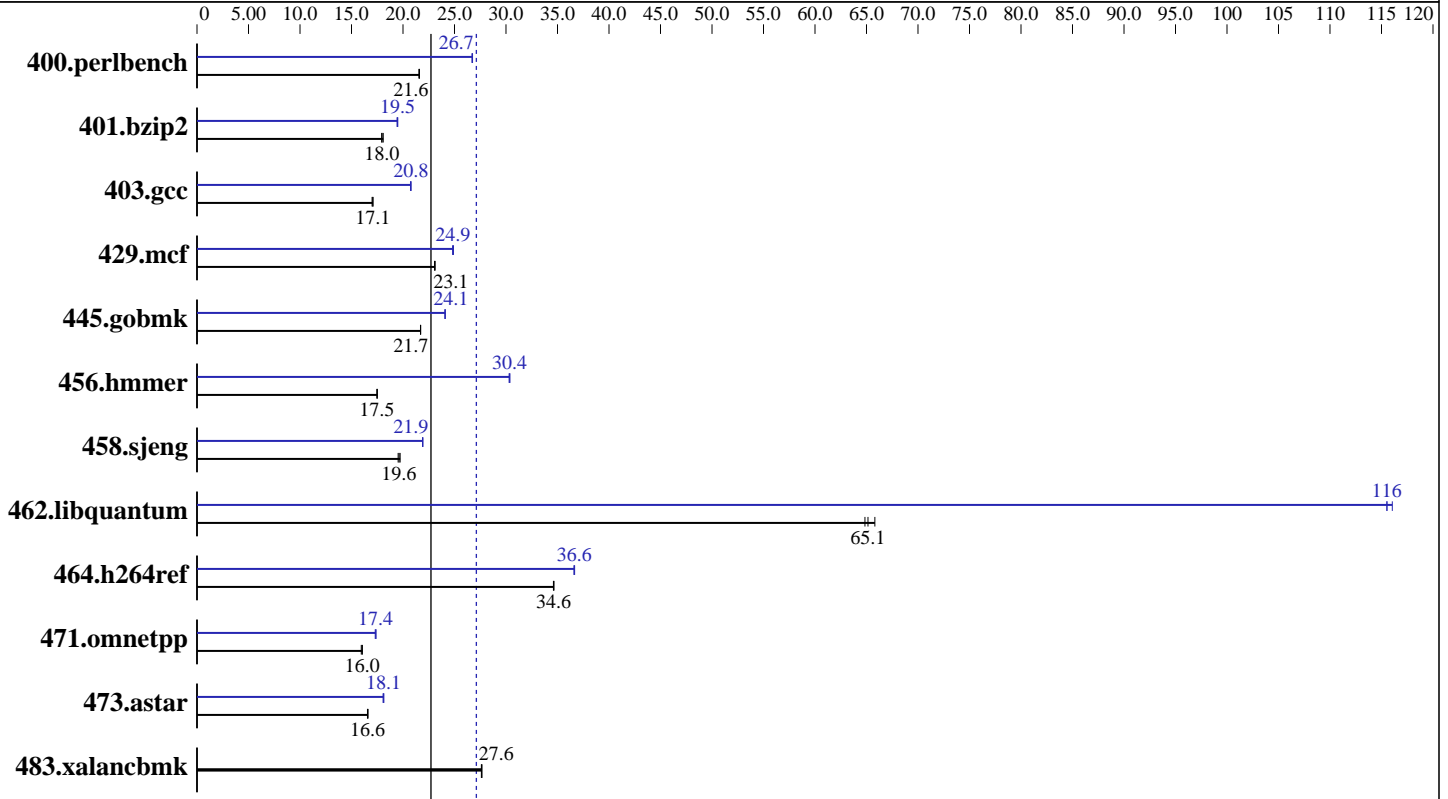
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Results Table

Benchmark	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio
400.perlbench	**453**	**21.6**	452	21.6	453	21.5	366	26.7	**365**	**26.7**	365	26.8
401.bzip2	**537**	**18.0**	534	18.1	538	17.9	497	19.4	**496**	**19.5**	495	19.5
403.gcc	**472**	**17.1**	474	17.0	471	17.1	387	20.8	**388**	**20.8**	388	20.7
429.mcf	395	23.1	395	23.1	**395**	**23.1**	366	24.9	368	24.8	**367**	**24.9**
445.gobmk	483	21.7	483	21.7	**483**	**21.7**	436	24.1	435	24.1	**436**	**24.1**
456.hmmer	534	17.5	533	17.5	**534**	**17.5**	308	30.3	**307**	**30.4**	307	30.4
458.sjeng	614	19.7	620	19.5	**617**	**19.6**	552	21.9	553	21.9	**552**	**21.9**
462.libquantum	315	65.8	320	64.8	**318**	**65.1**	**179**	**116**	179	116	179	116
464.h264ref	640	34.6	639	34.7	**639**	**34.6**	605	36.6	604	36.7	**604**	**36.6**
471.omnetpp	**390**	**16.0**	389	16.0	392	15.9	**360**	**17.4**	361	17.3	360	17.4
473.astar	423	16.6	**423**	**16.6**	424	16.6	**387**	**18.1**	387	18.1	389	18.1
483.xalancbmk	250	27.6	**250**	**27.6**	249	27.7	250	27.6	**250**	**27.6**	249	27.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores (default)
```

Platform Notes

```
Hardware Prefetch = Enable, Adjacent Sector Prefetch = Enable
```

General Notes

```
All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmer in peak were built with 64-bit Intel
compiler by changing the path for include and library files.

For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com
```

Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

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Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
-fast -vec-guard-write -parallel -par-runtime-control
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:
```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```
```
401.bzip2: /opt/intel/cce/10.1.008/bin/icc
        -L/opt/intel/cce/10.1.008/lib
        -I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
        -L/opt/intel/cce/10.1.008/lib
        -I/opt/intel/cce/10.1.008/include
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
```

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Peak Portability Flags (Continued)

483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias`
`-prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch`
`-auto-ilp32`

403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O2 -ipo`
`-no-prec-div -ansi-alias`

456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive`
`-auto-ilp32`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4`

462.libquantum: `-fast -unroll4 -Ob0 -prefetch`
`-opt-streaming-stores always -vec-guard-write`
`-opt-malloc-options=3 -parallel -par-runtime-control`

464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2`
`-ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo`
`-no-prec-div -ansi-alias -opt-ra-region-strategy=block`
`-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap`

473.astar: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo`
`-no-prec-div -ansi-alias -opt-ra-region-strategy=routine`
`-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

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Peak Other Flags

C benchmarks:

    403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.01.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.01.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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